

# TUC: Time-sensitive and Modular Analysis of Anonymous Communication

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## **Abstract**

The anonymous communication protocol Tor constitutes the most widely deployed technology for providing anonymity for user communication over the Internet. Several frameworks have been proposed that show strong anonymity guarantees for such protocols; none of these frameworks, however, are capable of modeling the class of traffic-related timing attacks against Tor, such as traffic correlation and website fingerprinting.

In this work, we present TUC: the first framework that allows for rigorously proving strong anonymity guarantees in the presence of time-sensitive adversaries that mount traffic-related timing attacks. TUC incorporates a comprehensive notion of time in an asynchronous communication model with sequential activation, while offering strong compositionality properties for security proofs. We apply TUC to evaluate a novel countermeasure for Tor against website fingerprinting attacks. Our analysis relies on a formalization of the onion routing protocol that underlies Tor and proves rigorous anonymity guarantees in the presence of traffic-related timing attacks.

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# 1 Introduction

Anonymous communication protocols, as provided by the Tor network [54], are an increasingly popular way for users to improve their privacy by hiding their location, i.e., their IP address. The Tor network is currently used by hundreds of thousands of users around the world [53].

For precisely understanding the anonymity guarantees provided by Tor, several rigorous analyses have been conducted [3, 10, 22, 52, 4, 23], which show strong anonymity guarantees for the onion routing protocol used by Tor; however, all of these analyses abstract from network-level timing attacks, such as traffic correlation or website fingerprinting, which arguably form the most important class of attacks against Tor’s anonymity guarantees [14, 20, 44, 32, 24, 31, 42, 45, 47, 48, 56, 33]. One of the main obstacles in including such time-sensitive attacks into a rigorous analysis is the lack of a communication model that enables a composable security analysis of complex protocols against time-sensitive adversaries.

In this paper, we follow the successful line of research on simulation-based composable security, started with Goldreich et al. [26] and put forward by [7, 12, 17, 29, 40, 43], which enable a composable security analysis of complex cryptographic protocols.

**Contribution.** In this work, we present TUC: the first framework that allows for rigorously proving strong anonymity guarantees in the presence of time-sensitive adversaries that mount traffic-related timing attacks. TUC incorporates a comprehensive notion of time in an asynchronous communication model with sequential activation, while offering strong compositionality properties for security proofs. In particular, TUC is based on a modified version of GNUC [29], which is one of the recent pieces of work [12, 40] that address many of the problems faced by earlier designs for simulation-based security frameworks [12].

We discuss the modifications to the communication model of GNUC in order to adequately account for time, and we show solutions for problems that occur when handling time-sensitive interaction between different parties over the network. In particular, we discuss that previous frameworks inherently are not suited for modeling time-sensitive asynchronous communication because they allow unrestricted activation orders: it might, e.g., happen that a message that was sent in the past (over a direct connection) arrives after a time-out mechanism already closed a port, only because the sending party was not activated early enough. We propose a remedy by only allowing consistency enforcing activation orders, which enforce that all parties receive all messages at the correct time. It turns out that all consistency enforcing activation orders are equivalent. As a result, we fix the activation order and thereby, in contrast to previous work, neither the environment nor the adversary has to learn any unrealistic information about activation requests. We show that valued properties, such as the joint state theorem and universal composability, hold in our time-sensitive framework as well.

Finally, we apply TUC to the onion routing protocol that underlies Tor, and we show how traffic-related timing attacks, such as inter-packet delay, traffic watermarking, and website fingerprinting attacks, can be mounted by an adversary in TUC. As a case study, we leverage TUC to analyze a simple countermeasure against website fingerprinting attacks and to prove  $k$ -recipient anonymity guarantees for this countermeasure.

**Outline.** Section 2 discusses related work. Section 3 introduces the time-sensitive TUC framework, and presents the activation order independence of TUC. Section 4 then introduces the notion of secure realization into this time sensitive communication model and shows that classic results of composable security are preserved in the time sensitive setting. In Section 6, we discuss how known traffic-related timing attacks on Tor can be represented in TUC. Moreover, we provide a countermeasure against website fingerprinting attacks and prove it secure in TUC.

## 2 Related Work

Tor [54] is one of the most widely used anonymous communication protocols to date [53] and is based on the (first generation) onion routing protocol by Goldschlag et al. [27]. There has been significant work in analyzing the anonymity guarantees provided by Tor [10, 22, 52, 4, 23, 21]. The major shortcoming of previous work is that it does not consider timing features of network traffic, which are used in timing-based traffic analyses. Considering the amount of proposed attacks [47, 24, 14, 56, 45, 9, 49, 28, 19] in the literature that use these timing features, it is clear that a rigorous framework that encompasses time-sensitive adversaries is required.

Some protocols, such as the onion routing protocol, are inherently insecure against global adversaries, but provide guarantees against partially global adversaries, which might only control servers or the user’s links (like ISPs), and are useful in practice. Such systems cannot always be properly analyzed in time-insensitive frameworks [7, 12, 17, 29, 40, 43] because in these frameworks partially global adversaries are too weak: they cannot measure time-sensitive features, such as measure inter-packet delay or throughput per time interval, and they thus can also not measure effects of some active attacks, such as traffic watermarking or slowing down certain parties by mounting denial-of-service attacks. Since TUC enables the adversary to measure time-sensitive features, this family of attacks can be mounted by an adversary in TUC; thus, TUC is better suited for analyzing such weaker adversary scenarios.

This work contributes to the successful line of work on simulation-based universal composability frameworks [7, 12, 17, 29, 40, 43]. These frameworks allow for a composable analysis of large and complex multi-party protocols, where the security of the whole protocol is derived from the security analysis of the sub-protocols of which it is composed. We chose to base our TUC framework on the GNUC framework by Hofheinz and Shoup [29]. While GNUC is not as general as other frameworks due to its strict poly-time notion and its tree-like structure of party-structure, it has the advantage that a composed ideal poly-time protocol implies a composed real poly-time protocol due to its strict polynomial-time notion [29, Section 11.8], and simplifies the proof of the composition theorem and thereby also our extension due to its simple party-structure. We are, however, confident that the main mechanisms for introducing our comprehensive notion of time, including time-sensitive adversaries, can also be applied to other frameworks, such as the RSIM[7], IITM [40], and UC [12] framework.

Previous work on synchronous communication granting protocol parties the capability to measure time or to proceed round-wise in order to enable proofs about properties, such as guaranteed termination or input termination [35, 1, 17, 36, 46, 50]. Such approaches, however, do not grant the adversary the capability to measure the time at which a message arrives.

Modeling timing attacks in synchronous frameworks might be possible, assuming very fast rounds and thus highly synchronized clocks, (in the order of milliseconds), but such an approach has two severe technical limitations: first, highly synchronized clocks can seldom be assumed in practice, in particular not for commodity hardware; second, such an attempt would technically only result in guarantees for protocols with highly synchronized clocks but not for protocols with loosely synchronized or unsynchronized clocks, while traffic-related timing attacks solely depends on the adversary’s clock and not on the protocol parties’ clocks. TUC grants the adversary access to a precise clock, independent of the parties’ clocks.

Networks of timed automata are well studied. However, they are seldom used for cryptographic purposes. While there has been work on the time sensitive analysis of Dolev-Yao style abstracted cryptographic protocols using timed automata [8, 16, 34, 39, 38, 41], this line of work analyzes Dolev-Yao style abstractions and does not allow for more fine-grained adversary types we want to capture in our model. We therefore stick to a Turing Machine based network model which is typically used in the analysis of cryptographic protocols.

### 3 Time-sensitive Network Model

In this section we present TUC, the first simulation-based composability framework that considers a time-sensitive adversary. TUC builds upon previous asynchronous simulation-based frameworks, such as GNUC [29] and the framework by Unruh [55], but fundamentally extends these frameworks by incorporating a notion of time while preserving the highly desired properties such as universal composability

**A general overview.** We introduce time by capturing via a timer the current global time for every machine in the network. Whenever a machine is activated, its timer is updated based on the number of steps done by the machine and the speed of it. The speed of a machine is either predetermined if it already existed at initialization, or is determined by the protocol that it executes if the machine is created during runtime. Furthermore, we require that the actual local time experienced by each machine is given by a strictly monotonically increasing function of its current global time, thereby modeling unsynchronized clocks.

We stick to the classic sequential activation model; however, by introducing a notion of time we inherently also allow parallel computation. Therefore, it can happen that one machine is already far in the future while all other machines are still in the past. In order to achieve consistency, i.e. to achieve that no party receives messages from the future, we introduce a distinguished machine, called

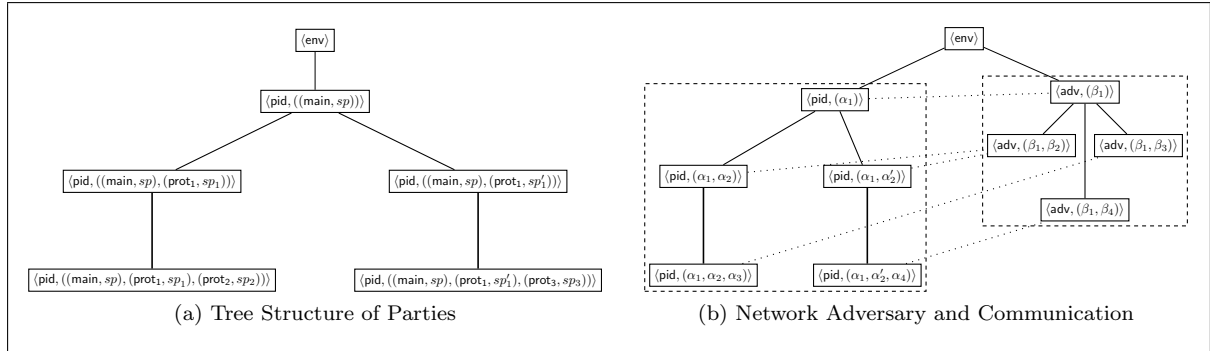


Figure 1: The Network Model in TUC

the *execution*. This execution basically manages the timer of each machine and the timely delivery of messages between machines.

The execution attaches to each message that is sent through the network a *time-stamp*, which is only visible to the execution. This time-stamp, loosely speaking, denotes the local time of the sending party when the message was sent.

The environment and the adversary might consist of several machines that work in parallel. A natural way of modeling this capability is to represent the environment and the adversary as a set of parallel machine. While such a model is more accurate, we decided for the sake simplicity to over-approximate this strength of the environment and the adversary by allowing both parties to make an arbitrary (but poly-bounded) amount of computation steps in one time-step.

As in GNUC, a protocol is formalized as a runtime library that assigns to each machine the program code to be executed by the respective machine and the speed of the machine that executes the code. We stress that a network has only one such runtime library, i.e. one protocol.

### 3.1 Execution

The whole network is run inside single a machine we call execution (EXEC). The execution runs all parties in the network as sub-machines, delivers messages between these sub-machines, and maintains a timer for every sub-machine. We define the output of EXEC as the output of the environment ENV after observing the communication between the involved parties. We capture this output by introducing a random variable.

**Definition 1.** *The execution EXEC is a probabilistic, poly-time Turing Machine which receives the security parameter  $\eta$  and outputs a value in  $\{0, 1\}$ .  $\text{EXEC}_\eta(\Pi, \mathcal{A}, \text{ENV})$  denotes the output of ENV after EXEC ran the network of machines running protocols in  $\Pi$  together with the network adversary  $\mathcal{A}$  and the environment ENV. EXEC stops whenever ENV halts and outputs a bit.*

We first describe the single aspects of the execution EXEC in the subsequent subsections, and at the end of this section we present the full description of EXEC in Figure 9.

#### 3.1.1 Machines & Session Identifiers

In order to adequately represent complex protocols in our model, we adopt the notion of protocol machines from GNUC [29]. Each party  $P$  that participates in network communication is represented by a tree of machines, each of which provides the sub-protocols used by  $P$ . This tree structure simplifies the substitution operation, which is used in the composition theorem (presented in Section 3.3.1). Our definition of a party is in line with what is presented as a structured system of interactive machines presented in [29, Section 3]. For the sake of presentation, we assume Turing machines as a machine model, but in Section 3.5 we describe how to generalize our model, to more realistic machine models, such as random access machines or how to model real-world computer architectures.

Each machine  $M$  in the network is identified by a unique *machine ID*  $\text{id}_M$ . This machine ID consists of a party identifier  $\text{pid}_M$  and a session identifier  $\text{sid}_M$ :  $\text{id}_M = \langle \text{pid}_M, \text{sid}_M \rangle$ . The session identifier  $\text{sid} = (\alpha_1, \dots, \alpha_k)$  consists of the *parent session identifier*  $(\alpha_1, \dots, \alpha_{k-1})$ , the protocol name  $\text{protNAME}$ , some session parameters  $sp$ , and the role:  $\alpha_k = (\text{protNAME}, sp, \text{role})$ . The protocol name is used to determine the code executed by the respective machine, as detailed in Section 3.3.

**Definition 2.** The machine ID  $\text{id}_M$  of a machine  $M$  is a tuple  $\text{id}_M = \langle \text{pid}_M, \text{sid}_M \rangle$ , where  $\text{pid}$  is the party identifier and  $\text{sid} = (\alpha_1, \dots, \alpha_k)$  the session identifier of  $M$ . The last component of  $\alpha_k = (\text{protNAME}, sp, \text{role})$  is the basename of  $M$ , where  $\text{protNAME}$  specifies protocol name executed by  $M$ ,  $sp$  contains session parameters, and  $\text{role}$  specifies the role in the protocol, e.g., client or server. The machine ID is unique to each machine.

Given such machine IDs  $\text{id}_M$ , we define a *party* as a collection of machines  $\{M_1, \dots, M_l\}$  such that each machine  $M_i$  has the same party identifier  $\text{pid}_{M_i}$  and the session identifiers of all machines induce a tree, if the parent session identifier of each machine is the session identifier of another machine (except for the root).

**Definition 3.** A party  $P$  is a collection of machines  $\{M_1, \dots, M_l\}$  with following properties:

$$P1 : \forall i, j \in \{1, \dots, l\} : \text{pid}_{M_i} = \text{pid}_{M_j}$$

$$P2 : \exists! M \in \{M_1, \dots, M_l\} : \text{sid}_M = (\alpha) \text{ (called root of } P\text{)}$$

$$P3 : \forall M_j \in \{M_1, \dots, M_l\} \setminus \{M\}, \exists M_i \in \{M_1, \dots, M_l\} : \text{sid}_{M_j} \text{ is a one-step extension of } \text{sid}_{M_i}, \text{ i.e. } \\ \text{sid}_{M_i} = (\alpha_1, \dots, \alpha_{k-1}) \text{ and } \text{sid}_{M_j} = (\alpha_1, \dots, \alpha_k)$$

By Definition 2 and 3, the set of machines inside a party  $P$  put up a tree, given we understand each session ID as a node in a graph and have an edge between session IDs that are one-step extensions of each other.

**Corollary 1.** A party  $P$  consists of a tree of machines.

Accordingly we will occasionally denote machines in such a machine-tree as *nodes*, and, given a pair of machines  $M_i$  and  $M_j$  satisfying property  $P3$ , we call  $M_i$  *parent* of  $M_j$ , and  $M_j$  *child* of  $M_i$ .

As we will discuss in Section 3.1.4, we restrict communication between machines to communication between parent-children pairs, and machines that are so-called *peers*: a peer is a machine with the same protocol name  $\text{protNAME}$  and the same session parameter  $sp$  for a basename  $(\text{protNAME}, sp, \text{role})$ .

**Definition 4.** Two machines  $M$  and  $M'$  with the basenames  $(\text{protNAME}_M, sp_M, \text{role}_M)$  and  $(\text{protNAME}_{M'}, sp_{M'}, \text{role}_{M'})$  are peers if  $\text{pid}_M \neq \text{pid}_{M'}$  and they both have the same protocol name  $\text{protNAME}_M = \text{protNAME}_{M'}$  and the same session parameter  $sp_M = sp_{M'}$ .

This formalization follows the intuition that a party  $P$  represents the protocol stack executed on a real world machine, and network communication is done between sub-processes running the same protocol-code.<sup>1</sup>

We adopt all of the constraints listed in [29, Section 4.5] that ensure that each party indeed consists of a tree of machines and that each machine can only communicate with its parent, its children and its peers.

Inside a party, a machine in the machine tree can create new machines as children by sending a message to the yet non-existent machine. The execution then checks whether the message sent induces a valid extension – where validity is defined by the protocol used in the network, see Section 3.3 – of the machine tree and creates the new machine.

### 3.1.2 Environment and Adversary

Influences to network communication outside of the regular parties are traditionally captured in two special parties: the environment represents user behavior, operating systems or other entities that control the actions of the network parties, while the adversary represents adversarial behavior in the network.

**Definition 5.** The network adversary  $\mathcal{A}$  is the unique party with party identifier  $\text{pid}_{\mathcal{A}} = \text{adv}$ . The environment  $\text{ENV}$  consists of only one machine with machine ID  $\text{id}_{\text{ENV}} = \langle \text{env} \rangle$  and is parent of all root machines of parties in the network.

Similarly to the other parties,  $\mathcal{A}$  consists of a machine-tree, where  $\mathcal{A}$  has a sub-adversary for each basename in the network. Each of these sub-adversaries receives intercepted messages from the network originating from machines with the respective basename. The distributed design of the adversary allows us to formulate the construction for the composition theorem in Section 4.2.2 in a much simpler way. The tree-structure of the adversary is depicted in Figure 1b.

<sup>1</sup>More general protocol models are conceivable; however, we inherit this restriction from GNUC.

**Initialization:** All input tapes are set to empty, all timer-variables are set to the initial value and no links are compromised.

**Machine Activation:** Every time a party  $M \in \mathcal{M}$  gives control to the network execution, the current global time  $T_M$  for  $M$  is updated:  $T_M := T_M + \frac{n}{c(M)}$ , where  $n$  is the number of steps performed by  $M$  in its last activation, and  $c(M)$  is its speed coefficient.

**upon input (time) from  $M \in \mathcal{M}$**

- 1: retrieve  $T_M$
- 2: compute local time  $t_M := f_M(T_M)$
- 3: activate  $M$  with input  $t_M$  on the time tape

**before every input (cmd,  $t$ ) from  $M \in \{\mathcal{A}, \text{Env}\}$**

- 1: **if**  $t > 0$  **then**
- 2:   set  $T_M := T_M + t$
- 3:   proceed with cmd
- 4: **else** activate  $M$  with error

Figure 2: Timing and Initialization in EXEC with machine set  $\mathcal{M}$ , where  $f_M$  is the  $M$ 's local time function and  $f_M = id$  for  $M \in \{\text{ENV}, \mathcal{A}\}$

### 3.1.3 Timing

We extend the basic communication model presented in the previous sections to include time. To each machine in the network we attach a timer and utilize the execution EXEC to maintain these timers. In order to allow unsynchronized clocks between network parties, we introduce local-time functions, which transform the timer's value to the local time experienced by each machine. We achieve time-consistency for messages exchanged between machines by introducing time-stamps for these messages. The execution EXEC utilizes these time-stamps for delivering messages to the recipient at the correct time.

We introduce time into our model by assigning a timer to every machine in the system.

**Definition 6.** *The timer  $T_M \in \mathbb{Q}$  of a machine  $M$  is a rational-valued variable associated with  $M$  that is maintained by the execution.*

The timer  $T_M$  is initialized to 0 at the beginning of the execution.  $T_M$  records the current global time of  $M$  and is updated every time  $M$  returns control to the execution. How much  $T_M$  is updated depends on the speed of  $M$ . Each machine has a different speed. Except for the environment and the adversary, the speed of each machine  $M$  is characterized by a *speed coefficient*  $c_M$ , which specifies how many computation steps  $M$  does per time unit. Hence, for the timer  $T_M$  of  $M$  we have

$$T_M := T_M + \frac{n}{c_M}$$

where  $n$  is the number of steps  $M$  did in its last activation.

The poly-time notion we use in our communication model (see Section 3.1.9) necessitates that a machine makes a polynomially bounded number of steps per time unit: a machine with an exponential speed coefficient would not be able to meaningfully progress in time as each machine in our network model is restricted to at most a polynomial number of computation steps per activation. Hence, we require the speed coefficients be polynomials.

**Definition 7.** *The speed coefficient of a machine  $M$  specifies the number of computation steps that  $M$  can perform per unit time. The speed coefficient is a polynomial  $c_M \in \mathbb{N}[X]$ . Whenever  $M$  returns control to the execution,  $T_M$  is updated by  $T_M := T_M + \frac{n}{c_M(\eta)}$  where  $n$  is the number of steps  $M$  did in its last activation and  $\eta$  is the security parameter.*

Local time functions are needed to model unsynchronized local clocks, only loosely synchronized clocks, or too fast or too slow clocks. Each machine  $M$  can request its local time by sending a (time) request to the execution. EXEC then computes the local time of  $M$  by applying  $M$ 's local time function  $f_M$  to its current global time  $T_M$ .

The execution and the simulating machine in the internal simulation lemma (Lemma 2) need to be able to efficiently compute and invert the local time; thus, we require that the local time function be efficiently computable and efficiently invertible. We additionally capture low-precision local clocks by not requiring that the local time function is injective. We only require that the local time function is *pseudo invertible* in the following sense: the pre-image of a local time  $f_M(t)$  is a closed interval  $[T, T']$  from which the corresponding global time is randomly chosen.

**Definition 8.** *A function  $f : \mathbb{Q} \rightarrow \mathbb{Q}$  is pseudo-invertible if for every value  $x \in f[\mathbb{Q}]$  there is exactly one non-empty, closed or right-open interval  $\mathcal{I}$  (i.e.  $\mathcal{I} = [T, T']$  with  $T \leq T'$  or  $\mathcal{I} = [T, T')$  with  $T < T'$ ) such*

that  $\forall y \in \mathcal{I} : f(y) = x$  and  $\forall y' \in \mathbb{Q} \setminus \mathcal{I} : f(y') \neq x$ . We denote the interval  $\mathcal{I}$  as the pseudo-pre-image of  $x$ .

Given a pseudo-invertible function  $f$ , we denote with  $f^{(-1)}$  the pseudo-inverse of  $f$ , which, given a value  $x \in f[\mathbb{Q}]$ , returns its pseudo-pre-image  $f^{(-1)}(x) = \mathcal{I}$ .

With the definition of pseudo-invertible functions, we can now define local time functions.

**Definition 9.** The local time function  $f_M : \mathbb{Q} \rightarrow \mathbb{Q}$  of  $M$  is a monotonically increasing, efficiently computable and efficiently pseudo-invertible function that transforms the value of  $T_M$  to  $M$ 's local time  $f_M(T_M)$ .

We require the local time function to be pseudo-invertible as EXEC needs to invert the local time function in order to process delayed message sending, which is an option for protocol machines in the network and will be required in our constructions in Section 7. We make a worst case assumption and define the local time function of the environment ENV and the network adversary  $\mathcal{A}$  to be the identity function (see Figure 2).

Speed coefficients and local time functions are fixed once the respective machine is spawned. Formally, the speed coefficient and the local time function depend on the machine ID, i.e., on the party ID and the basename. In order to assign speed coefficients to dynamically created machines, we suitably extend our definition of protocol introduced earlier.

We require that the protocol  $\Pi$  consists of two functions: one function that maps machine IDs to distributions of speed coefficients and local time functions and one function that maps basenames to the protocol code (see Definition 22). The execution draws the speed coefficients and local time functions from these distributions whenever a new machines is created during runtime. In the real world, the environment and the adversary might consist of several machines that work in parallel. A natural way of modeling this strength is to represent the environment and the adversary as a set of parallel machines. While such a model is more accurate, we abstract this strength of the environment and the adversary by allowing both parties to make an arbitrary amount of computation steps per point in time, for the sake of simplifying proofs.<sup>2</sup>

**Definition 10.** A machine  $M$  is timeless if it does not have a speed coefficient and  $M$  itself tells the execution the time-difference by which its timer increases next time it returns control to the execution.

Note that by the notion of poly-time we introduce in Section 3.1.9 this still restricts both to at most a polynomial number of computation steps (in the security parameter  $\eta$ ) per activation.

### 3.1.4 Communication Model

We differentiate between inner party communication between parent and children nodes inside a party and network communication between peers using a notion of *ports*: we distinguish between *environment*, *subroutine*, and *network port*. Figure 1b illustrates this with thick lines for inner party communication and dashed lines for network communication.

**Definition 11.** A port  $p$  of a machine  $M$  is a set of one input tape  $p_{in}$  and one output tape  $p_{out}$  that is used by the execution to pass information to  $M$ . Each machine has one environment port  $\text{sid}(M).\text{ENV}$ , a network port  $\text{sid}(M)_{\text{net}}$  and a set of subroutine ports  $\mathcal{S}_M$ .

For communication over the network,  $M$  sends its messages over its network port, addressing the recipient using the recipient's machine id. All incoming messages are received through  $M$ 's network port as well. A machine  $M$  can only send messages over the network to another machine  $M'$  if either  $M'$  is a peer of  $M$  or  $M'$  is the network adversary.

$M$  uses its environment port  $\text{sid}(M).\text{ENV}$  to communicate with its parent, or if  $M$  is a root node, with the environment. The set of subroutine ports  $\mathcal{S}_M$  contains a unique port for each child of  $M$ . In case  $M$  wants to create a new machine  $M'$  as a child,  $M$  creates a new port  $p'$  in  $\mathcal{S}$  and addresses  $M'$  through this port. EXEC then recognizes that  $p'$  is not in use yet and creates a new machine  $M'$ , as detailed in Figure 3.

Inner party ports follow the naming convention  $\text{pid}.\text{sid}_1.\text{sid}_2$ . Here  $\text{pid}$  is the process ID of the party,  $\text{sid}_1$  is the session ID of the parent node  $M_p$ , and  $\text{sid}_2$  the session ID of the child node  $M_c$ . Note that

<sup>2</sup>For the completeness of the dummy adversary, the adversary needs to be able to forward message in a way that is unobservable for the protocol even though every message-forwarding costs time. For that situation, we make use of the timelessness of the dummy adversary and show that if the dummy adversary proceeds in exponentially small steps, message-forwards remains unobservable for the protocol (see Lemma 3).



<pre> <b>upon</b> <math>(m, id)</math> <b>on port</b> <math>net</math> <b>from</b> <math>M \in \mathcal{M}</math> <b>at time</b> <math>T_M</math> 1: <b>if</b> <math>id</math> is a peer of <math>id(M)</math> <b>then</b> 2:   <math>(r, m, T_m) \leftarrow \text{NET}(id(M), id, m, T_M)</math> 3:   <b>if</b> <math>r = \mathcal{A}</math> <b>then</b> 4:     put <math>(m, T_m, net)</math> into <math>Q_{\mathcal{A}_i}</math> of the correspond-        ing sub-adversary <math>\mathcal{A}_i</math>. 5:     activate <math>\mathcal{A}_i</math>. 6:   <b>else</b> 7:     put <math>(m, T_m, net)</math> into <math>Q_{id}</math> 8:     <b>if</b> <math>M_{id}</math> in <b>listen</b> state <b>then</b> 9:       <b>if</b> <math>T_M \leq \tilde{T}_{M_{id}}</math> <b>then</b> 10:        set <math>\tilde{T}_{M_{id}} := T_M</math> 11:        <b>activate_listen</b><math>(M_{id})</math> 12:       <b>else</b> 13:        activate <math>M_{id}</math> 14:     <b>else</b> 15:       return error to <math>M</math>  <b>upon</b> <math>(delay, m, t)</math> <b>on port</b> <math>q</math> <b>from</b> <math>M \in \mathcal{M}</math> 1: <b>if</b> <math>t \geq f_M(T_M)</math> <b>then</b> 2:   compute pseudo-inverse <math>\mathcal{I} = f_M^{(-1)}(t)</math> 3:   randomly draw <math>T \leftarrow \mathcal{I}</math> 4:   execute appropriate send message for <math>m</math> with        time stamp <math>T</math> for port <math>q</math> 5: <b>else</b> 6:   return error to <math>M</math> </pre>	<pre> <b>upon</b> <math>m</math> <b>on port</b> <math>p \neq net</math> <b>from</b> <math>M \in \mathcal{M}</math> <b>at time</b> <math>T_M</math> 1: <b>if</b> there is a machine with input port <math>p</math> <b>then</b> 2:   let <math>id</math> be the machine ID of the unique machine        with input port <math>p</math> 3:   put <math>(m, T_M, p)</math> into <math>Q_{M_{id}}</math> 4:   <b>if</b> <math>M_{id}</math> in <b>listen</b> state <b>then</b> 5:     <b>if</b> <math>T_M \leq \tilde{T}_{M_{id}}</math> <b>then</b> 6:       set <math>\tilde{T}_{M_{id}} := T_M</math> 7:       <b>activate_listen</b><math>(M_{id})</math> 8:     <b>else</b> 9:       activate <math>M_{id}</math> 10:  <b>else</b> 11:   <b>if</b> <math>p = \text{pid}(M).\text{sid}(M).\text{sid}' \wedge</math>        <math>\text{sid}'</math> proper extension of <math>\text{sid}(M)</math> <b>then</b> 12:     let <math>(\mathcal{S}, \mathcal{LT}) = \pi_p(\text{pid}(M))</math> 13:     let <math>cd = \pi_c(\text{basename}(\text{sid}'))</math> 14:     sample speed <math>c'</math> from <math>\mathcal{S}</math> 15:     sample local time function <math>f'</math> from <math>\mathcal{LT}</math> 16:     create a new machine <math>M'</math> with code <math>cd</math>, 17:     <math>\text{sid}(M') := \text{sid}'</math>; <math>c(M') := c'</math>; <math>lt(M') := f'</math> 18:     set up translation of environment port of <math>M'</math>        to <math>p</math> 19:     set <math>T_{M'} = T_M</math> 20:     put <math>(m, T_M, p)</math> into <math>Q_{M'}</math> 21:     activate <math>M'</math> 22:   <b>else</b> 23:     return error to <math>M</math> </pre>
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Figure 3: Communication methods in EXEC with machine set  $\mathcal{M}$  and protocol II

$M_c$  communicates to its parent via its environment ports. The execution therefore makes an implicit port translation between environment ports of children nodes and inner party communication ports as defined above. Through this, we realize a variant of what is introduced as *Caller ID Translation* in [29, Section 4]. The methods used for message passing inside EXEC are presented in Figure 3.

For the case that a machine waits for incoming messages, we introduce a listen-command: (**listen**,  $T$ ). As soon as this command is sent, the execution EXEC will not activate these machines until either (i) they receive a message, or (ii), they can no longer receive a message before time  $T$ . The machines also have the possibility to set  $T = \infty$  the machines then will not be activated unless they receive a message. Once the machine is activated, their timer is also updated, either to the time-stamp of the received message, or  $T$  if the machine is activated without a message.

**Timing in communication.** In contrast to other sequential activation models, messages in TUC are not directly delivered to the recipient because there might be another message from a yet not activated machine that has to arrive earlier. More formally, if  $M$  sends a message to  $M'$  and  $T_M > T_{M'}$ , then the message obviously should not reach  $M'$  until  $T_{M'} \geq T_M$ . The execution remedies this problem by utilizing time stamps.

**Definition 12.** *The time stamp of a message  $m$  sent by a machine  $M$  is the updated value of the timer  $T_M$  at the point when  $M$  sends the message.*

The execution attaches this time-stamp to each message before it is sent to the recipient. On the recipient's end we use time-ordered queues, called *input queues*, which organize all messages that still need to be received, and release them once the recipient has progressed far enough in time. It is crucial that we deliver all messages at once; otherwise it is not clear how to internally simulate several machines (see the proof of Lemma 2).

**Definition 13.** *The input queue  $Q_M$  of a machine  $M$  is a priority queue which receives all messages directed to  $M$  as input and uses their time-stamp as the keys which are sorted. On request with a time-stamp  $T$ ,  $Q_M$  returns all messages with time-stamp  $T_m \leq T$ .*

<pre> <b>activate_listen</b>(<math>M</math>) 1: <b>if</b> <math>\forall M' \in \mathcal{M} \setminus \{M\} : \tilde{T}_{M'} \geq \tilde{T}_M</math> <b>then</b> 2:   <b>if</b> <math>M</math> is timeless <b>then</b> 3:     set <math>T_M = \tilde{T}_M</math> 4:   <b>else</b> 5:     set <math>T_M = \lceil \frac{\tilde{T}_M \cdot c_M}{c_M} \rceil</math> 6:   <b>if</b> <math>Q_M</math> is not empty <b>then</b> 7:     Pull messages <math>(m_1, T_{m_1}, p_1), \dots, (m_l, T_{m_l}, p_l)</math>        with time-stamps <math>T_{m_i} \leq T_M</math> from <math>Q_M</math> 8:     activate <math>M</math> with <math>m_1, \dots, m_l</math> on ports        <math>p_1, \dots, p_l</math> 9:   <b>else</b> 10:    activate <math>M</math> without input 11: activate ACT with a activation request </pre>	<pre> <b>upon output</b> (<math>\text{listen}, T</math>) <b>from</b> <math>M \in \mathcal{M}</math> 1: set <math>\tilde{T}_M := T</math> 2: <b>activate_listen</b>(<math>M</math>)  <b>upon activation by</b> <math>M \in \mathcal{M}</math> <b>without output</b> 1: activate ACT with activation request  <b>upon input</b> (<math>\text{activate}, M</math>) <b>from</b> ACT 1: <b>if</b> <math>M</math> in listen state <b>then</b> 2:   call <b>activate_listen</b>(<math>M</math>) 3: <b>else</b> 4:   activate <math>M</math> </pre>
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Figure 4: Activation order methods in EXEC with machine set  $\mathcal{M}$  and activation strategy ACT

When delivering  $m$ , EXEC puts the tuple  $(m, T_m, p)$  into the input queue  $Q_{M'}$  of  $M'$ . Here  $p$  denotes the port through which  $M'$  receives the message  $m$ . Whenever  $M$  is in the listen state, i.e. listen for new messages, the execution retrieves all messages from  $Q_M$  with time-stamps  $T_m \leq T_M$  and forwards them to  $M$ . In case there are several messages in  $Q_{M'}$  that could be retrieved at time  $T_{M'}$ ,  $Q_{M'}$  simply returns all of them. All methods involved in message passing are presented in Figure 3.

### 3.1.5 Consistency Enforcing Scheduling

Other simulation-based frameworks use a sequential activation model: machines in the network directly activate each other by sending messages and the environment or the adversary decides which machine is activated in case no messages are sent. We call these decisions the *activation strategy* and the actual sequence of activated machines in one run the *activation order*. Keeping to this unrestricted sequential activation model, however, causes several problems as soon as time is introduced: messages from the past arrive at nodes which are already in the future or the activation order (which is usually represented by the adversary or the environment) can push machines arbitrarily far into the future. The example below shows how this can lead into problems with a timeout mechanism, assuming the adversary decides the activation order.

**Example 1: Inconsistencies with unrestricted sequential activation strategies.** Consider machines  $M$  and  $M'$  which go into a timeout state if they do not receive a message up to some point in time  $T^*$

- 1: ENV repeatedly activates machine  $M$  through  $\mathcal{A}$ , which causes  $M$  to activate for one step and then return to the listening state. This effectively pushes  $M$  to time  $T > T^*$  the future.
- 2:  $M$  goes into the timeout state, as it did not receive any message until time  $T^*$
- 3: ENV tells machine  $M'$  to send a message to  $M$  at time  $T_0$ . Including processing the command, the message is sent at time  $T'$
- 4:  $M$  receives a message from time  $T' < T^*$  at time  $T^*$ .

$M$  now erroneously went into the timeout state, even though  $M'$  sent a message to  $M$  before the timeout should have occurred. ◇

We avoid such inconsistencies as follows: we introduce a special  $(\text{listen}, T)$  state for the machines in the network, in which they have to be in order to receive messages. Furthermore, we deviate slightly from the traditional sequential activation model by discarding activation commands that do not satisfy the following consistency enforcing property on the activation order.

**Definition 14** (Consistency Enforcing). *An activation order of machines is consistency enforcing, if  $\forall M' \neq M : \tilde{T}_M \leq \tilde{T}_{M'}$  whenever  $M$  is activated from the listening state. Here  $\tilde{T}_M$  is the virtual time of the machine  $M$  defined as follows:*

- $\tilde{T}_M = \min\{T, T_m\}$ , where  $T_m$  is the smallest time-stamp of a message in  $Q_M$  (or  $\infty$  if no such message exists), if  $M$  is in the  $(\text{listen}, T)$  state
- $\tilde{T}_M = T_M$ , if  $M$  is not in the  $(\text{listen}, T)$  state

Consistency enforcing activation orders resolve inconsistencies regarding timing that might otherwise occur in decisions made by machines in the network: for example, a machine deciding to cause a time-out after not receiving messages up to some point in time  $T$  can be sure that it will not receive any messages “from the past” after doing so, contrary to above example.

**Corollary 2.** *Under consistency enforcing activation orders, whenever a machine  $M$  is activated from the listen state at time  $T$ ,  $M$  receives all messages  $m$  with time-stamp  $T_m \leq T$  and any messages not yet received were sent at a time  $T' > T$ .*

We modify the execution EXEC such that it is consistency enforcing for any activation strategy, as described in Section 3.1.6.

### 3.1.6 Activation strategies

We need an *activation strategy* to determine the machine to be activated next whenever a machine turns inactive after switching into the listen state. Under consistency enforcing activation orders, however, we cannot take the same approach as previous frameworks, in which the adversary decides the activation strategy: since the adversary is a time-sensitive component of the network and thus also affected by consistency enforcing activation orders, the network can end up in a deadlock situation where all machines can either not be activated, or are stuck in the listen state. To avoid this problem we introduce the activation strategy as a sub-machine of the execution EXEC.

**Definition 15.** *The activation strategy ACT is a probabilistic, poly-time TM, which, given the state of the network as input, determines the next machine to be activated.*

ACT does not have a timer and implements the activation order based on the current state of the network. EXEC enforces the consistency enforcing property by checking the required conditions whenever ACT wants to activate a machine. In Section 3.2.1, we show that all consistency enforcing activation orders are equivalent. The activation methods in EXEC are depicted in Figure 4.

### 3.1.7 Shared Memory

As in other simulation-based framework, our goal is to analyze complex protocols by simplifying them to ideal functionalities with additional capabilities. We capture these capabilities in form of shared memory between all ideal peers in the network. Access to shared memory is granted via a special port through which parties can request read/write actions on the memory.

**Definition 16.** *A shared memory MEM is a machine without a timer which, given the current state of the network, implements time-sensitive data exchange outside of message passing. The set of machines with access to MEM is denoted with  $\mathcal{M}_{\text{MEM}}$ .*

MEM maintains every version  $D_{[T_1, T_2]}$  of each data-set  $D$  with respect to time (organizing them through time-intervals  $[T_1, T_2]$  between changes) and on request at time  $T$  returns the version  $D_{[T_i, T_{i+1}]}$  of data set  $D$  with  $T \in [T_i, T_{i+1})$

Note that, similar to the activation strategy presented in the last section, shared memory does not have a timer, is therefore outside of time. Technically this means that both activation strategy as well as shared memory are part of the execution, which is the only part of our network model that is not time-sensitive. We however separate them from EXEC as sub-machines for a more modular definition. Similar to the activation orders, the concept shared memory causes consistency issues: For example, what happens if a party that lives in the past changes a data set a party living in the future already read (in the future)?

#### Example 2: Consistency issues in shared memory.

Consider two machines  $M$  (living at time  $T_M$ ) and  $M'$  (living at time  $T_{M'} < T_M$ ) which both access the shared memory MEM to read/modify a data-set  $d$ .

- 1: ENV tells  $M$  to read  $d$  from MEM
- 2:  $M$  reads  $d$  from MEM at time  $T_M + \delta$
- 3: ENV tells  $M'$  to modify  $d$  in MEM
- 4:  $M'$  modifies  $d$  in MEM at time  $T_{M'} + \delta' < T_M + \delta$

**Upon activation with input** (write,  $D, d, M, T_M$ ):

- 1: let  $D_{[T_i, \infty]}$  be the last version of  $D$
- 2: set  $D_{[T_i, T_M]} := D_{[T_i, \infty]}$
- 3: remove  $D_{[T_i, \infty]}$
- 4: set  $D_{[T_M, \infty]} := d$
- 5: return confirmation to  $M$

**Upon activation:**

- 1: **if** there is an input (lookup,  $D, M, T_M$ ) **then**
- 2:   put the request (lookup,  $D, M, T_M$ ) into the queue  $Q_L$
- 3: let  $T^*$  be the smallest timer value in  $\mathcal{M}_{\text{MEM}}$
- 4: **while**  $\exists$  lookup requests  $Q_L$  with time stamp  $T \leq T^*$  **do**
- 5:   remove request (lookup,  $D', M', T_{M'}$ ) with smallest time stamp  $T_{M'}$  from  $Q_L$
- 6:   return data set  $D_{[T_i, T_{i+1}]}$ ,  $T_M \in [T_i, T_{i+1})$  to requesting party  $M$

Figure 5: The shared memory MEM

$M$  now has read the value of  $d$  which is actually no longer correct, as  $M'$  modified  $d$  in the past after  $M$  read it.  $\diamond$

We solve these consistency issues by using a variant of consistency enforcing activation orders.

**Definition 17.** *Shared memory access is consistency enforcing if a lookup requests (lookup,  $D, M, T_M$ ) is only processed if  $\forall M' \in \mathcal{M}_{\text{MEM}} : T'_M \geq T_M$ .*

If this condition is not true, the shared memory puts the request together with its time stamp into a input queue  $Q_L$ , which sorts all unanswered requests by their time stamps. Upon every activation, MEM checks  $Q_L$  for unanswered lookup requests and retrieves the one with the smallest time stamp. MEM then checks the lookup request for validity (based on its time stamp), and processes it if it is. In case MEM cannot process any lookup requests, it finishes the execution without sending a confirmation message. Figure 5 gives a possible pseudo-code implementation of a shared-memory unit in the time-sensitive network model.

As a consequence of consistency enforcing memory access we get Corollary 3 which ensures consistency of shared memory entries read by machines in the network.

**Corollary 3.** *If a data set  $D$  is read by a party  $M$  from a consistency enforcing shared memory MEM at time  $T$ , then any changes to  $D$  will only happen at a point in time  $T' \geq T$ .*

### 3.1.8 Compromisation

An important part in the analysis of protocols is accurately modeling adversarial capabilities, which includes restricting the adversary’s access to the network, as well as differentiating between active and passive adversaries.

**Link corruption.** Previous composability frameworks assume a global adversary which intercepts all messages sent between parties over the network. This is a necessity for realization proofs between protocols which do not inherently leak information to the adversary. However, in the special case of anonymous communication (AC) protocols, a global adversary poses a problem: Tor, e.g., is not secure against global adversaries [56, 48, 45, 14], it is not even designed to be secure against global adversaries. Previous work on the analysis of Tor shows how partial compromisation of the network can be modeled by introducing special network functionalities  $\mathcal{F}_{\text{NET}}$ , which are used as a link between parties [3].  $\mathcal{F}_{\text{NET}}$  only leaks a message to the network adversary if the communication link they represent is compromised.

To simplify the analysis, especially with regard to AC protocols, we assume an initially uncompromised network. The environment ENV, however, can compromise network communication links between two machines by sending a compromise message to the execution EXEC indicating which link should be compromised. Afterwards, any communication on the compromised link is forwarded to the adversary  $\mathcal{A}$  (see Figure 6).

We assume that inner-party communication, i.e. communication between children and parent nodes inside a party  $P$ , cannot be intercepted without compromising the party: a party models a system that resides at one physical location.

<pre> <b>upon</b> (compromise, id<sub>1</sub>) <b>from parent of</b> M<sub>id<sub>1</sub></sub> 1: <b>if</b> M<sub>id<sub>1</sub></sub> ∉ C<sub>M</sub> <b>then</b> 2:   <b>if</b> protName(M<sub>id<sub>1</sub></sub>) is not ideal <b>then</b> 3:     replace M<sub>id<sub>1</sub></sub> code to cd<sub>comp</sub> 4:     C<sub>M</sub> := C<sub>M</sub> ∪ {M<sub>id<sub>1</sub></sub>} 5:   send (compromise) to M<sub>id<sub>1</sub></sub> 6: <b>else</b> 7:   return error to ENV </pre>	<pre> <b>upon input</b> (compromise, id<sub>1</sub>, id<sub>2</sub>) <b>from Env</b> 1: NET(compromise, id<sub>1</sub>, id<sub>2</sub>) 2: activate ENV with (compromised, id<sub>1</sub>, id<sub>2</sub>) as    input. </pre>
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Figure 6: Corruption Commands in EXEC for Adaptive Compromisation

<pre> <b>upon</b> (compromise, id<sub>1</sub>) <b>from parent of</b> M<sub>id<sub>1</sub></sub> 1: <b>if</b> M<sub>id<sub>1</sub></sub> ∉ C<sub>M</sub> ∧ M did not receive a message yet <b>then</b> 2:   <b>if</b> protName(M<sub>id<sub>1</sub></sub>) is not ideal <b>then</b> 3:     replace M<sub>id<sub>1</sub></sub>'s code to cd<sub>comp</sub> 4:     C<sub>M</sub> := C<sub>M</sub> ∪ {M<sub>id<sub>1</sub></sub>} 5:   send (compromise) to M<sub>id<sub>1</sub></sub> 6: <b>else</b> 7:   return error to ENV </pre>
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Figure 7: Machine Corruption in EXEC for Static Compromisation

In order to keep EXEC modular, we introduce a network topology sub-machine NET, which handles all requests regarding compromised links: compromisation requests from ENV are forwarded to NET, which internally maintains the corruption status of the network, and on request from EXEC, determines whether a message can be directly forwarded to the recipient, or is intercepted by the network adversary. As we discuss for future work in Section 8, NET can also be extended to include network latency and other parameters of the network.

**Party corruption.** The network machines themselves can also be completely compromised by the environment ENV. Upon receiving a compromisation message **compromise**, EXEC replaces the code executed by the receiving machine  $M$  with the following code of a compromised machine  $cd_{comp}$  and forwards the corruption message to  $M$ , which in turn responds with an answer to ENV containing the current state of  $M$  and from then on is under full control of the adversary (see Fig. 6).  $cd_{comp}$  is defined as follows: whenever  $M$  receives a message, it is forwarded to the adversary, who in turn instructs  $M$ .

Since the adversary is modeled as a network party, we cover passive as well as active adversaries: a passive adversary would simply forward all messages he intercepts, while an active adversary can send additional messages through the network as well as change intercepted messages.

The analysis of AC protocols usually differentiates between two important classes of Compromisation.

**Definition 18.** *An execution allows for static compromisation if machines and communication links can only be compromised at initialization. It allow for adaptive compromisation if the environment can compromise even during run-time.*

While the presentation of EXEC in Figure 9 works for the adaptive case, we need to make some changes for the static case: In the static case, a set of machines and links is already compromised at the beginning of the execution and corruption commands are no longer available for the environment during the execution.

Compromised machines however can still create new machines. These new machines should be compromisable before they start to interact with the rest of the network. We therefore allow for a modified machine compromisation method in the static case, which only forwards the **compromise** command if it is the first message the newly created machine receives (see Fig. 7).

### 3.1.9 Runtime Bounds

There has been extensive work on finding a correct and accurate notion for polynomial runtime in networks of machines [30, 12, 7, 29, 40]. We adopt the solution put forward in [29, Section 6]. We only give a

high level idea of the notion of a probabilistic polynomial time network and refer to the GNUC paper for a thorough discussion [29, Section 6].

The IITM-model uses a simpler notion of poly-time machines, which is based on a notion from Hofheinz, Müller-Quade, and Unruh [30]; this notion can be adopted to our communication model as well. As discussed in [29, Section 11.8] however, the notion used in the IITM-model does not imply poly-boundedness for the composition of poly-bounded machines, which the definition in GNUC does.

We require that each message sent through the network begins with the string  $1^\eta$ , where  $\eta$  is the security parameter used in the execution. If a machine is activated without a message, it receives the string  $1^\eta$  on a special activation input port. This ensures that on any activation, the activated machine has an input. We define polynomial time based on this input.

Run-time of machines in the network is defined based on the length of the messages they exchange, i.e. the *flow* between machines. To this end we denote with  $f_{ep}(\eta)$  the flow from the environment ENV to protocol machines  $\Pi$ , with  $f_{ea}(\eta)$  the flow from ENV to the adversary  $\mathcal{A}$ , and with  $f_{ap}(\eta)$  the flow from  $\mathcal{A}$  to  $\Pi$ .

We only consider environments which on each activation are polynomially bounded in their input and which additionally are well-behaved.

**Definition 19.** *An environment ENV is well-behaved if there is a polynomial  $p$  such that for every adversary and for all security parameters  $\eta$ ,*

$$f_e(\eta) = f_{ep}(\eta) + f_{ea}(\eta) \leq p(\eta)$$

With this, we can define probabilistic, poly-time protocols.

**Definition 20.** *Let  $T_\eta^\Pi[\Pi, \text{ENV}, \mathcal{A}]$  denote the accumulated number of steps done by all machines running  $\Pi$ . A protocol  $\Pi$  is probabilistic, poly time (PPT) if there exists a polynomial  $p$  such that for all every well-behaved environments ENV,  $T_\eta^\Pi[\Pi, \text{ENV}, \mathcal{A}]$  can be bounded by*

$$\Pr[T_\eta^\Pi[\Pi, \text{ENV}, \mathcal{A}] > p(f_{ep}(\eta) + f_{ea}(\eta))] \leq \text{negl}(\eta)$$

In order to get an overall poly-time execution, we also only allow bounded adversaries.

**Definition 21.** *An adversary  $\mathcal{A}$  is bounded for  $\Pi$  if  $\mathcal{A}$  is time-bounded for  $\Pi$ , i.e. there exists a polynomial  $p$  such that for all well-behaved environments ENV we have that*

$$\Pr[T_\eta^{\mathcal{A}}[\Pi, \text{ENV}, \mathcal{A}] > p(f_{ep}(\eta) + f_{ea}(\eta))] \leq \text{negl}(\eta)$$

and if  $\mathcal{A}$  is flow-bounded for  $\Pi$ , i.e. there exists a polynomial  $p'$  such that for all well behaved environments

$$\Pr[f_{ap}(\eta) > p'(f_{ea}(\eta))] \leq \text{negl}(\eta)$$

With these restrictions we get an execution EXEC which overall uses a polynomial number of steps in the security parameter  $\eta$ .

**Lemma 1.** *For all well-behaved environments ENV, all PPT protocols  $\Pi$ , all bounded adversaries  $\mathcal{A}$  for  $\Pi$  and all inputs  $x \in \{0, 1\}^{p(\eta)}$  the execution  $\text{EXEC}_\eta(\Pi, \mathcal{A}, \text{ENV})$  is probabilistic polynomial time in  $\eta$  with overwhelming probability.*

The lemma follows, on the one hand, from  $\Pi$  and  $\mathcal{A}$  being polynomially time-bounded in their incoming flow (with overwhelming probability),  $\mathcal{A}$  being flow bounded (with overwhelming probability), and ENV being well-behaved. On the other hand we have that EXEC only requires polynomial time to update the timers of each machine, compute their local time-functions, compute the input queues of each machine and check the condition for consistency enforcing activation orders. EXEC therefore overall only uses polynomial time.

**Invitations [29].** Note that we also inherit the notion of *invitations* from GNUC. This technical artifact is sometimes required for the construction of the simulators in proofs of secure realization (see 4), which often cannot wait until they receive a message from the environment before they interact with protocol machines.

With invitations, messages sent from sender  $S$  to recipient  $R$  through the network are of the form  $(1^\eta, m, i_1, \dots, i_n)$ , where  $i_1, \dots, i_n$  are the invitations  $R$  can utilize to send messages to  $S$  which do not count towards the flow boundedness-constraint (see Definition 20). The execution remembers these

<pre> <b>upon input</b> <math>((i, m, i_1, \dots, i_n), \text{id})</math> <b>on port</b> <i>net</i> <b>from</b> <b>machine</b> <math>M \neq \text{Env}, \mathcal{A}</math>   <b>if</b> <math>i</math> <b>is not empty</b> <b>then</b>     <b>if</b> <math>(i, M_{\text{id}}) \in \mathcal{I}_M</math> <b>then</b>       <b>remove</b> <math>(i, M_{\text{id}})</math> <b>from</b> <math>\mathcal{I}_M</math>     <b>else</b>       <b>return error to</b> <math>M</math>     <b>for</b> <math>j \in [1, n]</math> <b>do</b>       <math>\mathcal{I}_{M_{\text{id}}} = \mathcal{I}_{M_{\text{id}}} \cup \{(i_j, M)\}</math>     <b>proceed for</b> <math>(m' = (m, i_1, \dots, i_n), \text{id})</math> <b>in port</b> <i>net</i> </pre>	<pre> <b>upon input</b> <math>((m, i_1, \dots, i_n))</math> <b>on port</b> <i>env</i> <b>from ma-</b> <b>chine</b> <math>\mathcal{A}</math>   <b>for</b> <math>j \in [1, n]</math> <b>do</b>     <math>\mathcal{I}_{\text{Env}} = \mathcal{I}_{M_{\text{id}}} \cup \{(i_j, M)\}</math>   <b>proceed for</b> <math>(m' = (m, i_1, \dots, i_n))</math> <b>in port</b> <i>env</i> </pre>
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Figure 8: Invitations Methods in EXEC.

invitations for each machine  $M$  in the set  $\mathcal{I}_M = \{(i_j, M_j)\}$ , where  $M_j$  is the machine sending the invitation.

Invitations can only be sent from protocol machines running protocol  $\Pi$  to the network adversary  $\mathcal{A}$  and from  $\mathcal{A}$  to the environment  $\text{ENV}$ .

An *invited* message is of the form  $(i, 1^\eta, m, i_1, \dots, i_n)$ . Whenever a machine sends an invited messages with invitation  $i$  to a recipient  $R$ , EXEC checks if  $\{i, R\} \in \mathcal{I}_M$ , and only forwards the message if the invitation exists and is used for the correct recipient.

In order for invitations to be useful, we also require that each time a machine  $M$  is compromised,  $M$  also sends a list of invitations, one for each of its children nodes, to the network adversary  $\mathcal{A}$  (together with its current state). This allows  $\mathcal{A}$  to in turn compromise  $M$ 's children without violating the flow-boundedness constraint introduced above. This will prove to be essential in the proof of the Joint-State Theorem in Section 4. Figure 8 presents the method used in EXEC to manage the invitations.

Invited messages do not count towards the flow we bound, and therefore do not cause any problems with the poly-boundedness of machines in the network. Since the overall number of invitations that can be generated is polynomially bounded in  $\eta$ , the overhead of invited messages can be polynomially bounded as well. Lemma 1 therefore still holds in a system with invited messages.

We furthermore observe that the timing mechanisms we introduce in TUC do not cause any additional invitations to be generated by machines in the network. All properties shown in GNUC [29] which include the invitation mechanism therefore also hold in our framework as long as timing is handled properly.

### 3.1.10 Discussion

**Modeling asynchronous communication despite consistency enforcing scheduling.** In other simulation-based frameworks, such as UC, GNUC, RSIM, IITM, the environment (or sometimes the network adversary) decides which machines are activated next. Quantifying over all possible activation strategies in particular includes those scenarios in which a message transmission is arbitrarily delayed. Canetti argues that such an activation order is chosen for modeling asynchronous communication [12, page 28].

In spite of consistency enforcing activation orders, TUC can be used to model asynchronous communication by protocols that do not use their local clock.<sup>3</sup> Arbitrary networks delays for compromised links can be modeled in TUC since the network adversary can arbitrarily delay a message.

Beside the advantage that asynchronous communication can be modeled by arbitrary activation strategies, another effect of (traditional) arbitrary activation strategies is that they can model disabled or crashed network nodes by not activating the respective machine, but only if this machine would not have received any message (otherwise this message would have activated the machine). However, since this mechanism only covers machines that would not have received any messages, we believe that such crashes should rather be modeled explicitly, i.e. in the same way as node corruptions, and not via an activation strategy.

**Encoding time-sensitive adversaries in previous frameworks.** It might be possible to wrap each machine  $M$  in the network in a local wrapper  $W$  that performs the same actions as the execution EXEC

<sup>3</sup>Even the slightly stronger setting in which each party uses its local clock, but the clocks are completely unsynchronized can be modeled by unsynchronized local time functions (see Section 3.1.3).

in TUC.  $W$  would count the number of steps that  $M$  performs and divides these steps by the speed of that party to calculate  $M$ 's current time. This wrapper  $W$  would for each outgoing message from  $M$  add a timestamp and for each incoming remove the timestamp before forwarding it to the recipient. Moreover,  $W$  would order every incoming messages in a time-ordered input queue and only deliver those message to  $M$  for which  $M$  already proceeded far enough in time.

Such a network of locally wrapped machines  $W(M_1), \dots, W(M_n)$ , however, does not ensure the consistency enforcing property for activation orders, i.e., allows inconsistent activation orders (see Example 1). Since consistency enforcing is a global property the local wrappers  $W$  would have to synchronize their timer information to find the next, eligible party and activate this party (by sending some dummy message). As a consequence, the adversary can potentially annul time-out mechanisms by a specifically crafted activation order.

An adversary (i.e., an environment) that is modelled in this way is unrealistically powerful: it can decide at which point in time a party starts and continues a computation. This capability gives the adversary more power to influence the parties (and thereby get information about their activation times) than it will ever have in reality.

It might be possible to encode all the adjustments that we formalized in this work in previous frameworks: in addition to locally wrapped protocol machines, ideal functionalities have to be accessible in parallel (from different points in time), and the adversary (and the environment) have wrapped such that they do not see the time-stamp of a message. However, then it is still necessary to reprove universal composability, the completeness of the dummy adversary, and the Joint State Theorem (see Section 4.2) since only wrapped machines, including environments and adversaries, are considered in the quantifications. We estimate the amount of work and formalism that is needed for such an encoding to be comparable to this work. For the sake of presentation, we decided to devise a new framework from scratch in which we can directly incorporate time-sensitive adversaries.

### 3.2 Properties of EXEC

We present two properties which will be helpful for proving the security properties of TUC. We show that all non-trivial activation orders are equivalent. Trivial activation strategies trivial include strategies that cause a deadlock, e.g., as follows. In a program with at least two machines  $M, M'$  in which  $M$  at some point goes into a listen state, the activation strategy could always choose to activate  $M'$ . The consistency enforcing mechanism would from some point on refute the activation of  $M'$  because  $M'$  will have advanced farther than the time of  $M$ . This activation strategy produces a deadlock that is artificial and due to our model; hence, we call such an activation strategy trivial. Theorem 1 makes this notion of a non-trivial, or *deadlock-free*, activation strategy formal.

**Theorem 1.** *Let  $\text{EXEC}'_{k,S,t}(\Pi, \mathcal{A}, \text{ENV})$  denote the machine that executes  $\text{EXEC}_k(\Pi, \mathcal{A}, \text{ENV})$  with the activation strategy  $S$ . Moreover  $\text{EXEC}'_{k,S,t}(\Pi, \mathcal{A}, \text{ENV})$  records the internal states of all machines in the execution after each step together with the global time in which that machine was during that state. After the execution finished,  $\text{EXEC}'_{k,S,t}(\Pi, \mathcal{A}, \text{ENV})$  outputs the sequence of all states of all machines (including  $\mathcal{A}$  and  $\text{ENV}$ ) up to the time  $t$ . Let  $\text{reach}(S, t)$  denote the following property: the probability that for all  $\mathcal{A}$  and all  $\text{ENV}$  and all  $x \in \{0, 1\}^*$  in  $\text{EXEC}'_{k,S,t}(\Pi, \mathcal{A}, \text{ENV})$  all machines reach a time  $> t$  is overwhelming in  $k$ .*

*Let  $A$  be a (potentially timeless) machine and let  $S_1, S_2$  be any two activation strategies, i.e., any two machines that, given all the information that the execution  $\text{EXEC}$  has, (adaptively) determine which machine shall be activated next. For all sets of machines  $P := \{P_1, \dots, P_n\}$  ( $n \in \mathbb{N}$ ),  $S_1$  is indistinguishable from  $S_2$ , i.e., for all points in time  $t \in \mathbb{Q}$  and for all distinguisher machines  $D$  there is a negligible function such that:*

$$\begin{aligned} \text{reach}(S_1, t) \text{ and } \text{reach}(S_2, t) &\implies \\ \left| \Pr[b \leftarrow \langle D \mid \text{EXEC}'_{\eta, S_1, t}(\Pi, \mathcal{A}, \text{ENV}) \rangle : b = 1] \right. \\ &\quad \left. - \Pr[b \leftarrow \langle D \mid \text{EXEC}'_{\eta, S_2, t}(\Pi, \mathcal{A}, \text{ENV}) \rangle : b = 1] \right| \\ &\leq \mu(\eta) \end{aligned}$$

*Proof.* First, we prove the following statement.

**Claim 1:** For two activation strategies  $S_1$  and  $S_2$ ,  $\text{EXEC}'_{\eta, S_1, t}(\Pi, \mathcal{A}, \text{ENV})$  is indistinguishable from  $\text{EXEC}'_{\eta, S_2, t}(\Pi, \mathcal{A}, \text{ENV})$  if and only if the sequence of results of all listen-commands, i.e., the input message with which the respective machine is activated, is indistinguishable in both scenarios.



*Proof of Claim 1.* By the definition of EXEC all the information that  $A$  can observe is independent of the activation order except of the results of the pull-queries to the input queue  $Q_A$ . Hence, it is indistinguishable whether the strategy  $S_1$  or  $S_2$  has been used. The reverse direction holds because every machine can use the result from the `listen`-command to distinguish the two settings.  $\diamond$

Let  $\text{EXEC}'_{\eta, S_1, t, i}(\Pi, \mathcal{A}, \text{ENV})$  be the execution (with the activation strategy  $S_1$ ) that stops after the  $i$ th activation of a machine that is in the `listen` state. Let  $\text{EXEC}'_{\eta, S_2, t, i}(\Pi, \mathcal{A}, \text{ENV})$  be defined analogously.

We perform a proof by induction over the activation of machines that are in the `listen` state.

Induction basis ( $i = 0$ ): By Claim 1 the two activation strategies are indistinguishable.

Induction step ( $i > 0$ ): We know that for all  $i - 1$ th `listen` activations the two activation strategies  $S_1$  and  $S_2$  are indistinguishable. Thus, the results of all  $i - 1$  `listen`-commands are indistinguishable for both activation strategies  $S_1$  and  $S_2$ . Thus, by Claim 1, it remains to show that also seeing the result of the  $i$ th `listen`-command is indistinguishable.

Let  $M$  be the  $i$ th machine that is activated in the `listen` state. If no machine sent a message to  $M$  since the  $i - 1$ th `listen`-command, the statement follows by induction hypothesis. Assume that at least one message has been sent to  $M$  since the  $i - 1$ th `listen` activation. Let  $T$  be the global time of  $M$  when it issue the `listen`-command. By consistency enforcing property of the activation order, we know that all machines are at least in time  $T' \geq T$  when  $M$  is activated in its `listen` state for any activation strategy  $S$  that lets all machines reach the (global) time  $T$ . By the definition of EXEC, we know that after  $M$  is activated (upon a `listen`-command) no machine can send a message  $M$  that will get a timestamp  $\leq T$ . Hence, the input of the `listen` activation, i.e., the inputs from the input queue  $Q_M$ , are independent from the activation strategy. Hence, by Claim 1, the statement follows.  $\square$

### 3.2.1 Simplified activation strategy

It turns out that under consistency enforcing all activation strategies are equivalent to the following activation strategy SAS, as long as no deadlocks occur: the next machine to be activated is selected based on each machine's timer  $T$  by randomly selecting a machine with the lowest timer value.

Theorem 1 directly implies that the activation strategy SAS is indistinguishable from any other activation strategy as long as no deadlock occurs.

**Corollary 4** (SAS is equivalent to all deadlock-free activation strategies). *The activation strategy SAS is indistinguishable from any other, deadlock-free activation strategy (in the sense of Theorem 1).*

### 3.2.2 Internal Simulation of Multiple Machines

Here we show that a finite number  $n$  of timed machines  $M_1, \dots, M_n$  in our network can be simulated by a single machine  $M^*$  which shows same behavior as the  $n$  separate machines. While the internal simulation is clearly not a problem if  $M^*$  is timeless (even if some of the simulated machines are timeless), this is not clear for the case where  $M^*$  is a timed machine with a timer that automatically progresses in time whenever  $M^*$  does a computation step.

The following lemma will allow us to simplify large parts of the theorems we show regarding the security definition we introduce in Section 4.

**Lemma 2.** *For any set of  $n$  timed machines  $M_1, \dots, M_n$  in the network there exists a single timed machine  $M^*$  that shows the same behavior as  $M_1, \dots, M_n$  for consistency enforcing activation orders.*

*Proof.* We construct and show that  $M^*$  sends the same messages as  $M_1, \dots, M_n$ , together with the same time-stamps, into the network.

**Construction:** We set the speed coefficient of  $M^*$  to  $c_{M^*} = n \cdot c_{M'} + O(n)$ , where  $M' \in \{M_1, \dots, M_n\}$  is the machine with the highest speed coefficient. The local time function of  $M^*$  is the identity.<sup>4</sup> This allows  $M^*$  to do at least one computation with every internally simulated machine without progressing in time further than  $M'$  (since each simulated computation step also causes one computation step in  $M^*$ ).

$M^*$  inherits all ports of the machines it simulates. Thus all messages intended for the internal machines are first received by  $M^*$ , who then forwards them appropriately.

In particular this implies that  $M^*$  has more than one network port:  $M^*$  has  $n$  network ports, each of which are identified with the identifiers of  $M_1, \dots, M_n$ . This is necessary since  $M^*$  needs to be able

<sup>4</sup>This is important for recomputing the execution EXEC.

to differentiate between the recipients of messages coming into  $M_1, \dots, M_n$  and the senders of messages going out of  $M_1, \dots, M_n$ . Since EXEC automatically identifies network ports based in machine ID, messages targeted at machines inside  $M^*$  are automatically forwarded to  $M^*$ .

In principle,  $M^*$  simply emulates the network execution for the machines it simulates, managing message passing from the machines to the real execution EXEC and the activation order. As shown in Lemma 1,  $M^*$  can adopt the strategy of activating the machine with the lowest timer value.

In particular,  $M^*$  now works as follows: on regular activation,  $M^*$  simulates each machine  $M_1, \dots, M_n$  for one computation step at a time, each time activating a machine  $M'$  with the lowest timer  $T_{M'}$  (randomly choosing one if more than one machine is at time  $T_{M'}$ ).  $M^*$  stops the simulation if all machines with the lowest timer value are either in the listening state or returned control to the execution.  $M^*$  sends each message sent out by the simulated machines using the delayed sending command (see Figure 3), using the appropriate time-stamps.

$M^*$  performs idle steps until its own timer matches the lowest timer value  $T'$ . Then,  $M^*$  turns into the listening state of the network port of machine  $M_i$  if there is a machine  $M_i$  at  $T'$  that is in the listening state (randomly choosing one if there is more than one), or simply returning control to EXEC otherwise.

If activated from the listening state,  $M^*$  automatically receives all messages on its ports with time-stamps smaller than  $T_{M^*}$ . These messages are forwarded to the message queues of the machines inside  $M^*$ , with  $T_{M^*}$  as a time-stamp, and each machine is in turn activated for one computation step each.  $M^*$  then continues with the usual activation order as described above.

Upon a command  $(\text{listen}, T)$  from a party,  $M^*$  computes the virtual time  $\tilde{T}_M$  for each machine  $M$  and handles the  $(\text{listen}, T)$  command as EXEC.  $M^*$  can compute for a machine  $M$  the virtual time  $\tilde{T}_M$  since it knows the speed coefficient of  $M$  and the content of the input queue of  $M$ .

We show by induction over the activations of  $M^*$  that  $M^*$  shows the same behavior towards the rest of the network as  $M_1, \dots, M_n$ :

**Activation 1:** On initial activation, all timers are the initial value, and no machine is in the listening state.  $M^*$  will in turn simulate each internal machine until all machines with the lowest timer  $T'$  either sent a message or went into the listening state.

Due to our selection of the speed coefficient  $c_{M^*}$  of  $M^*$ , we have that at this point  $T_{M^*} < T'$ , with enough time for  $M^*$  to forward all messages sent by the simulated machines using the delayed sending command, and turning into the listen state at time  $T'$ . Since all messages until  $T'$  were sent with the right time stamp, and the simulated machines are activated in the same order as they would have been activated from the execution (due to consistency enforcing scheduling), the behavior of  $M^*$  is the same as the behavior of each single machine  $M_1, \dots, M_n$ .

**Activation  $i \rightarrow i + 1$ :**  $M^*$  is either gets activated regularly or from a listening state. The former case is the same as for the initial activation above, hence we only consider the activation from the listening state.

Being activated from a listening state means there are machines with timer value  $T_{M^*}$  simulated in  $M^*$ , which also are in the listening state. Due to consistency enforcing scheduling, each of these machines receive all messages they would have received in the regular network as well (since all other machines in the network have progressed past  $T_{M^*}$ ).

Messages forwarded to machines which are not in the listening state at time  $T_{M^*}$  correctly receive these messages once they go into the listening state. The potentially changed time-stamp of these messages inside  $M^*$  does not influence the behavior of each machine as they do not receive the time-stamp of the message (it is only used by the network execution, which here is simulated by  $M^*$ ).

Hence all machines receive the same messages as in the regular network, at the same points in time, and thus will also produce the same output messages, at the same points in time, as well.  $\square$

We stress that Lemma 2 does not enforce the tree-restrictions of machines inside a party defined in Section 3.1.1. One should still mind these restrictions if composition is to be used on together with above construction.

As a corollary, we can show that a timeless machine can also internally simulate other timeless machines.

**Corollary 5.** *For any set of  $n$  machines  $M_1, \dots, M_n$  in the network there exists a single timeless machines  $M^*$  which shows the same behavior as  $M_1, \dots, M_n$  for consistency enforcing activation orders.*

*Proof.* The simulator is exactly constructed as in the proof of Lemma 2 except that the activation is slightly modified. All timed machines are only activated once in one point in time, but timeless machines are activated (in a round-robin manner) as often as they stay in that point in time. Moreover, if a simulated timeless machine  $M_i$  sends a message at time  $t$  and no other machine wants to perform a computation,  $M^*$  performs a `(listen, t)` command, where  $t$  is the minimal time of the next timed machine that could perform a step and a message that shall be sent or.  $\square$

### 3.3 Protocols

A protocol is a runtime library that assigns to each basename (i.e., protocol name, session parameter, and role) used in session IDs the respective program code to be executed by the respective machine and the speed of the machine that executes the code. A network has only one such function, i.e., one protocol. This function assigns to all parties the respective code, speed and local time functions for every single machines.

Speed coefficients and local time functions are fixed once the machine is spawned. Formally, the speed coefficient and the local time function depend on the machine ID, i.e, on the party ID and the basename. In order to assign speed coefficients to dynamically created machines, we require that the protocol  $\Pi$  consists of two functions: one function that maps machine IDs to distributions of speed coefficients and local time functions and one function that maps basenames to the protocol code. The execution draws the speed coefficients and local time functions from these distributions whenever a new machines is created during runtime.

In the definition below, we denote with  $Dist(X) \subset X \rightarrow [0, 1]$  the set of distributions over the natural numbers (without 0). Moreover, we denote with  $LTF$  the set of local time functions and with  $D$  the set of machine IDs.

**Definition 22 (Protocol).** For a set  $P$  of party IDs and a set of basenames  $D$ , a protocol is a pair of functions  $\pi := (\pi_p, \pi_c)$  consisting of a function  $\pi_p : P \times D \rightarrow Dist(\mathbb{N}[X]) \times Dist(LTF)$  from party IDs to an efficiently computable distribution of speed coefficients and an efficiently computable distribution of local-time functions local time functions and a function  $\pi_c : D \rightarrow \{0, 1\}^*$  from basenames to a code. A protocol  $\pi' = (\pi'_p, \pi'_c)$  is a subprotocol of  $\pi = (\pi_p, \pi_c)$  over domain  $D'$  if  $D' \subseteq D$  and  $\pi_c$  restricted to  $D'$  equals  $\pi'_c$  and  $\pi'_p = \pi_p$ .

A protocol  $\Pi$  also restricts the set of protocol-names  $\{d_1, \dots, d_l\} \subset D$  that a machine ID  $d \in D$  can call as subroutines. By the requirements listed in in [29, Section 5], these restrictions constitute an *acyclic call graph* on the protocol-names with a unique root  $r$ . We then call  $\Pi$  *rooted* at  $r$ . With this, the machine-trees representing a party in the network effectively are a *protocol-tree*, representing the different protocols and subprotocols used by a party for communication in the network.

**Example 3: Protocol.** Consider a network run by an execution EXEC with protocol  $\Pi$  and a machine  $M$  with machine ID  $id_M = (pid, ((main, x, empty)))$  wants to invoke a new TLS connection to another machine in the network.  $M$  would then address a new machine  $M'$  with machine ID  $id_{M'} = (pid, ((main, x, empty), (tls, x', empty)))$  over the port  $pid.((main, x, empty)).((main, x, empty), (tls, x', empty))$ . EXEC recognizes that  $M'$  does not yet exist and checks whether  $((main, x, empty), (tls, x', empty))$  is a proper extension of  $((main, x, empty))$  (i.e. `main` is allowed to invoke `tls` as a subprotocol). If the check succeeds, EXEC creates a new machine, queries  $(cd, \mathcal{S}, \mathcal{LT}) \leftarrow \Pi(id_M)$  and assigns the new machine  $cd$  as its code, a speed coefficient  $c'$  drawn from  $\mathcal{S}$  as its speed, and a local time function  $f$  drawn from  $\mathcal{LT}$ .  $\diamond$

#### 3.3.1 Composition

Composition of protocols is a useful tool for analyzing complex protocols by breaking them down into simpler to analyze, smaller sub protocols. In Section 4 we present the universal composability theorem which allows us to derive the security of a composed protocol from the security of its parts.

The following definitions are in line with the definitions for composition in GNUC [29, Section 5].

**Definition 23.** The sub-protocol  $\Pi' = \Pi|x$  of  $\Pi$  is the restriction of  $\Pi$  to  $D'$ , the set of all basenames reachable from the basename  $x$ .

We denote with  $\Pi \setminus x$  the protocol over all protocol names which are reachable from the root  $r$  without going through a node with basename  $x$ .

**Initialization:** All input tapes are set to empty, all timer-variables are set to the initial value and no links are compromised.

**Machine Activation:** Every time a party  $M \in \mathcal{M}$  gives control to the network execution, the current global time  $T_M$  for  $M$  is updated:  $T_M := T_M + \frac{n}{c(M)}$ , where  $n$  is the number of steps performed by  $M$  in its last activation, and  $c(M)$  is its speed coefficient.

```

upon  $(m, id)$  on port  $net$  from  $M \in \mathcal{M}$  at time  $T_M$ 
1: if  $id$  is a peer of  $ID(M)$  then
2:    $(r, m, T_m) \leftarrow NET(id(M), id, m, T_M)$ 
3:   if  $r = \mathcal{A}$  then
4:     put  $(m, T_m, net)$  into  $Q_{\mathcal{A}_i}$  of the corresponding
       sub-adversary  $\mathcal{A}_i$ .
5:     activate  $\mathcal{A}_i$ .
6:   else
7:     put  $(m, T_m, net)$  into  $Q_{id}$ 
8:     if  $M_{id}$  in listen state then
9:       if  $T_M \leq \tilde{T}_{M_{id}}$  then
10:        set  $\tilde{T}_{M_{id}} := T_M$ 
11:        activate_listen $(M_{id})$ 
12:       else
13:        activate  $M_{id}$ 
14:     else
15:     return error to  $M$ 

upon  $m$  on port  $p \neq net$  from  $M \in \mathcal{M}$  at time  $T_M$ 
1: if there is a machine with input port  $p$  then
2:   let  $id$  be the machine ID of the unique machine
       with input port  $p$ 
3:   put  $(m, T_M, p)$  into  $Q_{M_{id}}$ 
4:   if  $M_{id}$  in listen state then
5:     if  $T_M \leq \tilde{T}_{M_{id}}$  then
6:       set  $\tilde{T}_{M_{id}} := T_M$ 
7:       activate_listen $(M_{id})$ 
8:     else
9:       activate  $M_{id}$ 
10:  else
11:  if  $p = pid(M).sid(M).sid' \wedge$ 
        $sid'$  proper extension of  $sid(M)$  then
12:    let  $(\mathcal{S}, \mathcal{LT}) = \pi_p(pid(M))$ 
13:    let  $cd = \pi_c(basename(sid'))$ 
14:    sample speed  $c'$  from  $\mathcal{S}$ 
15:    sample local time function  $f'$  from  $\mathcal{LT}$ 
16:    create a new machine  $M'$  with code  $cd$ ,
17:     $sid(M') := sid'$ ;  $c(M') := c'$ ;  $lt(M') := f'$ 
18:    set up translation of environment port of  $M'$  to
        $p$ 
19:    set  $T_{M'} = T_M$ 
20:    put  $(m, T_M, p)$  into  $Q_{M'}$ 
21:    activate  $M'$ 
22:  else
23:  return error to  $M$ 

upon input (compromise,  $id_1, id_2$ ) from Env
1: NET(compromise,  $id_1, id_2$ )
2: activate ENV with (compromised,  $id_1, id_2$ ) as input.

upon (compromise,  $id_1$ ) from parent of  $M_{id_1}$ 
1: if  $M_{id_1} \notin \mathcal{C}_M$  then
2:   replace  $M_{id_1}$  code to  $cd_{comp}$ 
3:    $\mathcal{C}_M := \mathcal{C}_M \cup \{M_{id_1}\}$ 
4:   send (compromise) to  $M_{id_1}$ 
5: else
6:   return error to ENV

upon (delay,  $m, t$ ) on port  $q$  from  $M \in \mathcal{M} \setminus \{\text{Env}, \mathcal{A}\}$ 
1: if  $t \geq f_M(T_M)$  then
2:   compute global time  $T = f_M^{-1}(t)$ 
3:   execute appropriate send message for  $m$  with time
       stamp  $T$  for port  $q$ 
4: else
5:   return error to  $M$ 

upon input (time) from  $M \in \mathcal{M}$ 
1: retrieve  $T_M$ 
2: compute local time  $t_M := lt(M)(T_M)$ .
3: activate  $M$  with input  $t_M$  on the time tape.

before every input (cmd,  $t$ ) from  $M \in \{\mathcal{A}, \text{Env}\}$ 
1: if  $t > 0$  then
2:   set  $T_M := T_M + t$ 
3:   proceed with cmd
4: else activate  $M$  with error

upon output (listen,  $T$ ) from  $M \in \mathcal{M}$ 
1: set  $\tilde{T}_M := T$ 
2: activate_listen $(M)$ 

upon activation by  $M \in \mathcal{M}$  without output
1: activate ACT with scheduling request

upon input (activate,  $M$ ) from ACT
1: if  $M$  in listen state then
2:   call activate_listen $(M)$ 
3: else
4:   activate  $M$ 

activate_listen $(M)$ 
1: if  $\forall M' \in \mathcal{M} \setminus \{M\} : \tilde{T}_{M'} \geq \tilde{T}_M$  then
2:   if  $M$  is timeless then
3:     set  $T_M = \tilde{T}_M$ 
4:   else
5:     let  $k^* = \min\{k \in \mathbb{N} \mid \frac{k}{c_M} \geq \tilde{T}_M\}$ 
6:     set  $T_M = \frac{k}{c_M}$ 
7:   if  $Q_M$  is not empty then
8:     Pull messages  $(m_1, T_{m_1}, p_1), \dots, (m_l, T_{m_l}, p_l)$ 
       with time-stamps  $T_{m_i} \leq T_M$  from  $Q_M$ 
9:     activate  $M$  with  $m_1, \dots, m_l$  on ports  $p_1, \dots, p_l$ 
10:  else
11:  activate  $M$  without input
12: activate ACT with a scheduling request

```

Figure 9: The full description of the execution EXEC for the time-sensitive network execution with adaptive compromisation. The machine set  $\mathcal{M}$  denotes all machines, including environment and adversary,  $\mathcal{A}$  denotes all machines in the adversary party.

**Definition 24.** Let  $\Pi' = \Pi|x$  be a sub-protocol of  $\Pi$  and let  $\Pi'_1$  be a protocol rooted at  $x$ .  $\Pi'_1$  is substitutable for  $\Pi'$  if for all  $y \in D(\Pi \setminus x)$  it holds that  $\Pi(y) = \Pi'_1(y)$

We denote the substitution of  $\Pi'$  in  $\Pi$  as  $\Pi_1 = \Pi[\Pi'/\Pi'_1]$ . That is,  $\Pi_1|x = \Pi'_1$  and  $\Pi_1 \setminus x = \Pi \setminus x$ .

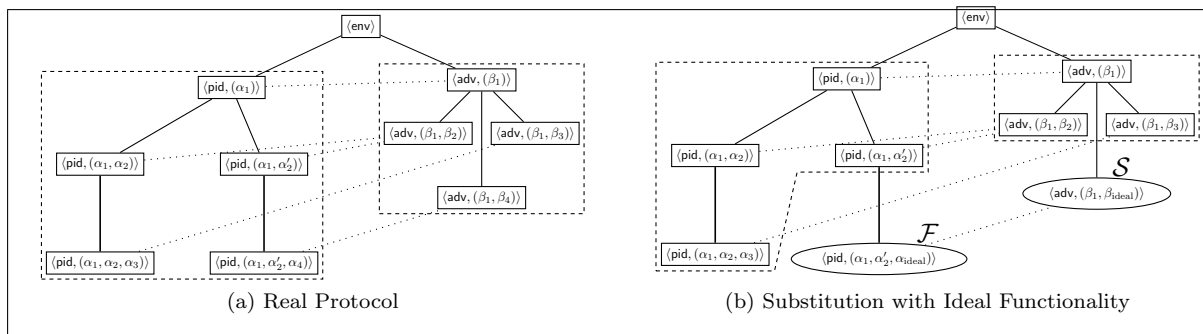


Figure 10: Substitution operation and the construction for proof of realization – a sub-protocol is substituted for an ideal functionality  $\mathcal{F}$  and its sub-adversary by the simulator  $\mathcal{S}$  used for realization.

Composition in our network model comes down to replacing sub-trees inside the machine-tree of a party. Figure 10 gives an example for such a substitution. On the protocol level, composition comes down to replacing the code provided for all basenames in a sub-tree of the *acyclic call graph* of basenames.

### 3.4 Ideal Functionalities

Typically, the notion of secure realization is used to prove that a protocol  $\Pi$  is as secure as a simpler protocol  $\pi$  that has some additional capabilities, such as a shared memory for all machines running  $\pi$ . Protocols that have such additional capabilities are called *ideal functionalities*.

An ideal functionality is a protocol, i.e., every party contains a copy of the ideal functionality in its protocol tree, and all of these copies share a common state (see Section 3.1.7). Since such a copy of the ideal functionality is part of the protocol tree, we allow the basenames of each machine to require ideal or real protocol code, instead of classifying ideal machines by their party IDs, as in GNUC [29].

In contrast to previous work, we furthermore allow ideal functionalities to have children. Whenever ideal machines use common routines, such as communication channels, it is very convenient to be able to formalize such a routine as a child, e.g., as an ideal functionality for communication channels.

We adopt the restriction from GNUC that ideal machines can only communicate with ideal peers in the network and that ideal machines upon a *compromise*-message do not reveal their entire internal state to the network adversary  $\mathcal{A}$  but can see the *compromise*-message in plain, i.e., as a normal message. Depending on the code of the ideal functionality protocol, the ideal machine then, e.g., just marks a party as compromised or sends sensitive information to the network adversary  $\mathcal{A}$ .

Previous work [12, 29, 40] models ideal functionalities as a single, separate machine that has a direct connection to the rest of the protocol via a so-called dummy nodes that solely forwards messages between the ideal functionality and the parent protocol.

In a time-sensitive setting, however, an ideal functionality has to abstract several machines, each of which has their own timer. It is therefore much more natural to consider distributed ideal functionalities than having a central ideal functionality: in the centralized setting the ideal functionality would have to manage the timers of each machine it replaced (each of which was in different parties in the network), as well as manage the additional delay the dummy nodes create. In the distributed setting, on the other hand, each instance of the ideal functionality is a separate machine with its own timer, allowing for a much simpler construction.

In the following, we therefore use distributed ideal functionalities, in particular for our abstraction of the onion routing protocol used in Tor (see Section 6).

#### 3.4.1 Central vs. Distributed Ideal Functionalities

A *central ideal functionality* is a machine without parents that is a peer of so-called *dummy nodes*. Similar to previous work, a dummy node is linked to a central ideal functionality  $\mathcal{F}$  (see below). Upon receiving a messages from its parent node, a dummy node forwards this message to the machine  $\mathcal{F}$ . Analogously, upon receiving a message from the machine  $\mathcal{F}$ , the dummy node forwards this message to its parent node. A dummy node can not have any children. Similar to functionality nodes, dummy node can not be compromised.

For the sake of convenience, we treat dummy nodes as re-wirings, i.e., reroutings. Formally, dummy nodes live at all points in time at once, we say they are *omni-time* machines. Upon receiving a message at time  $t$  they also forward the message at time  $t$ , without any delay. It would be possible use normal

**Machine Activation:** Every time a party  $M \in \mathcal{M}$  gives control to the network execution, the current global time  $T_M$  for  $M$  is updated:  $T_M := T_M + \sum_{j=1}^n C(M)(\text{instr}_j)/c(M)$ , where  $C(M)$  is the clock cycle function of the machine model of  $M$ ,  $\text{instr}_1, \dots, \text{instr}_n$  are the instructions that have been performed by  $M$  in its last activation and  $c(M)$  is the speed coefficient of  $M$ .

<p><b>upon</b> <math>m</math> <b>on port</b> <math>p \neq \text{net}</math> <b>from</b> <math>M \in \mathcal{M}</math> <b>at time</b> <math>T_M</math></p> <p>1: <b>if</b> there is a machine with input port <math>p</math> <b>then</b></p> <p>2:   let <math>id</math> be the machine ID of the unique machine with input port <math>p</math></p> <p>3:   put <math>(m, T_M, p)</math> into <math>Q_{M_{id}}</math></p> <p>4:   <b>if</b> <math>M_{id}</math> in listen state <b>then</b></p> <p>5:     <b>if</b> <math>T_M \leq \tilde{T}_{M_{id}}</math> <b>then</b></p> <p>6:      set <math>\tilde{T}_{M_{id}} := T_M</math></p> <p>7:      <b>activate_listen</b><math>(M_{id})</math></p> <p>8:    <b>else</b></p> <p>9:      activate <math>M_{id}</math></p> <p>10: <b>else</b></p> <p>11:   <b>if</b> <math>p = \text{pid}(M).\text{sid}(M).\text{sid}' \wedge</math>            <b>sid' proper extension of sid}(M) <b>then</b></b></p>	<p>12:   let <math>((\text{BI}, cl), \mathcal{S}, \mathcal{LT}) = \pi_p(\text{pid}(M))</math></p> <p>13:   let <math>cd = \pi_c(\text{basename}(\text{sid}'))</math></p> <p>14:   sample speed <math>c'</math> from <math>\mathcal{S}</math></p> <p>15:   sample local time function <math>f'</math> from <math>\mathcal{LT}</math></p> <p>16:   create a new machine <math>M'</math> with code <math>cd</math> with the machine model <math>(\text{BI}, cl)</math>,</p> <p>17:   <math>\text{sid}(M') := \text{sid}'</math>; <math>c(M') := c'</math>; <math>lt(M') := f'</math>;           <math>C(M') := cl</math></p> <p>18:   set up translation of environment port of <math>M'</math> to <math>p</math></p> <p>19:   set <math>T_{M'} = T_M</math></p> <p>20:   put <math>(m, T_M, p)</math> into <math>Q_{M'}</math></p> <p>21:   activate <math>M'</math></p> <p>22: <b>else</b></p> <p>23:   return error to <math>M</math></p>
---	---

Figure 11: Modification to the execution EXEC for generalized machine models

machines as dummy nodes, but then the central ideal functionalities would have to compensate the time the dummy nodes need for forwarding messages.

In contrast to a central ideal functionality, we call an ideal functionality as considered in our framework, i.e., that consists of several nodes with the same code and a shared memory, a *distributed ideal functionality*.

We define for every central ideal functionality a corresponding distributed ideal functionality. Each replica uses the same code of the central ideal functionality but we replace each memory access with an access to the shared memory. Analogously, we define for every distributed ideal functionality a central ideal functionality by using the one code that all machines share and replacing each access to the shared memory with access to the local memory.<sup>5</sup>

**Corollary 6.** *Let  $\mathcal{F}_r$  be a distributed ideal functionality without children and  $\mathcal{F}_c$  be the corresponding central ideal functionality. Then,  $\mathcal{F}_r \geq_t \mathcal{F}_c$  and  $\mathcal{F}_c \geq_t \mathcal{F}_r$ .*

*Proof.* This lemma directly follows the internal simulation lemma (Lemma 2) and from Corollary 3, i.e., from the consistency enforcing scheduling of a shared memory: every write operation is scheduled before any other read operations that take place by parties that are already in the future.  $\square$

**A note on adding omni-time dummy nodes.** Extending our framework with such dummy nodes that are omni-time machines, still preserves all previously proven statements. The crucial difference is in the internal simulation lemma (Lemma 2 and Corollary 5). The internal simulation applies almost verbatim with the difference that dummy nodes are not internally executed but rather understood as re-wiring instructions: given a machine  $M$  that has a dummy nodes child that in turn is connected to an ideal functionality  $\mathcal{F}$ , in the internal simulation the port to the dummy node child is directly connected to the corresponding port in  $\mathcal{F}$ . With this construction almost by the same arguments Lemma 2 and Corollary 5 hold, which in turn implies the completeness of the dummy adversary (Lemma 3) and the composition theorem (Theorem 2).

<sup>5</sup>Formally, we actually require that the states of the Turing machine of the central ideal functionality are the cartesian product  $S^k$  of the states  $S$  of the Turing machine of the distributed ideal functionality, where  $k$  is the number of parties. Otherwise, moving the reading head of the program tape does not take the same amount of steps for the central ideal functionality.

### 3.5 More realistic machine models

For the sake of a simpler presentation, we assume in this work Turing machines as the underlying machine model. As soon as timing information is taken into account, Turing machines do not properly model real-world systems: e.g., a Turing machine needs in the worst case linear time (in the size of the already written cells) for a memory access, whereas a real-world system performs a memory access in constant time. Such a behavior is much more accurately modelled by other machine models, such as random access machines (RAMs) [15].

It is possible to extend our model to more realistic machine models. Interpreting a single step of a machine as a clock cycle, we can formalize more realistic machine models by mapping different basic instructions, such as addition and multiplication, to different amounts of clock cycles. We can even formalize architectures with caches by mapping the internal state and a basic instruction to some amount of clock cycles.

**Definition 25** (Machine model). *A state space is a set  $s \in S \subseteq 0, 1^*$ . A function  $f : S \rightarrow S$  from a state to a state is a basic instruction. A function  $cl : \text{BI} \times S \rightarrow \mathbb{N}$  from basic instructions and states to natural numbers is a clock cycle function. A pair  $(\text{BI}, cl)$  consisting of a set of basic instructions  $\text{BI} \subset \{f \mid f : S \rightarrow S\}$  and a clock cycle function  $cl$  is a machine model.*

With such a modular treatment of the machine models, we can, moreover, capture scenarios in which different parties in the network use different machine architectures. Similar to the distribution of the speed coefficients and the local time functions, we require that the machine model is determined by the machine ID, i.e, party ID and the basename. We generalize the notion of a protocol in the following definition. In the definition below, we denote with  $\text{Dist}(X) \subset X \rightarrow [0, 1]$  the set of distributions over the natural numbers (without 0), with  $LTF$  the set of local time functions and with  $D$  the set of machine IDs, and with  $\text{MM}$  the set of all machine models.

**Definition 26** (Protocols for generalized machine models). *For a set  $P$  of party IDs and a set of basenames  $D$ , a protocol is a pair of functions  $\pi := (\pi_p, \pi_c)$  consisting of a function  $\pi_p : P \times D \rightarrow \text{MM} \times \text{Dist}(\mathbb{N}[X]) \times \text{Dist}(LTF)$  from party IDs to an efficiently computable distribution of speed coefficients and an efficiently computable distribution of local-time functions local time functions and a function  $\pi_c : D \rightarrow \{0, 1\}^*$  from basenames to a code. A protocol  $\pi' = (\pi'_p, \pi'_c)$  is a subprotocol of  $\pi = (\pi_p, \pi_c)$  over domain  $D'$  if  $D' \subseteq D$  and  $\pi_c$  restricted to  $D'$  equals  $\pi'_c$  and  $\pi'_p = \pi_p$ .*

Accordingly, the execution EXEC has be adjusted such that the clock cycle functions are maintained, and instead of increasing the time of a party by 1 for each instruction EXEC increases the time by the sum of cycles given by the clock cycle function. In Figure 11, we show how the execution has to modified to take into account the generalized machine models. We stress that all the results about our framework are independent of the machine model; hence, our results hold for generalized machine models as well as defined in Definition 25.

This concludes the presentation of the time-sensitive network model used in TUC on which we want to base our time-sensitive analysis of anonymous communication protocols. The next section presents the security notion we will use for this analysis.

## 4 Secure Realization

We present the notion of secure realization adopted in TUC and show that important properties of secure realization such as the completeness of the dummy adversary and universal composability hold.

### 4.1 Security Definition

In the same spirit as in other simulation-based frameworks, we adopt the notion of *secure realization*. A protocol  $\pi$  is compared to a simplified protocol  $\rho$  and is shown to be at least as secure:  $\pi$  securely realizes  $\rho$ , if every attack against  $\pi$  is also possible against  $\rho$ . More formally we require that the output distribution of the execution running the protocol  $\pi$ , an adversary  $\mathcal{A}$  and an environment  $\text{ENV}$  is indistinguishable from the output distribution of the execution running the simplified protocol  $\rho$  with a simulator  $\mathcal{S}$  and the same environment  $\text{ENV}$ . We define the indistinguishability of different execution as follows. This definition is a reformulation of the indistinguishability of binary random variable ensembles in [11].

**Definition 27** (Indistinguishability). *Two ensembles  $(\text{EXEC}_\eta(\Pi, \mathcal{A}, \text{ENV}))_{\eta \in \mathbb{N}}$  and  $(\text{EXEC}_\eta(\Pi', \mathcal{A}', \text{ENV}'))_{\eta \in \mathbb{N}}$  are indistinguishable, denoted*

$$\text{EXEC}(\Pi, \mathcal{A}, \text{ENV}) \approx \text{EXEC}(\Pi', \mathcal{A}', \text{ENV}'),$$

*if for every  $c \in \mathbb{N}$  there is a  $\eta_0 \in \mathbb{N}$  such that for all security parameters  $\eta > \eta_0$  we have that*

$$\begin{aligned} &|Pr[\text{EXEC}_\eta(\Pi, \mathcal{A}, \text{ENV}) = 1] \\ &- Pr[\text{EXEC}_\eta(\Pi', \mathcal{A}', \text{ENV}') = 1]| < \eta^{-c} \end{aligned}$$

Using this definition, we can now formalize secure realization.

**Definition 28.** *A protocol  $\pi$  securely realizes another protocol  $\rho$ , written  $\pi \geq_t \rho$ , if for all PPT adversaries  $\mathcal{A}$  there is a PPT simulator  $\mathcal{S}$  such that for all PPT environments  $\text{ENV}$*

$$\text{EXEC}(\pi, \mathcal{A}, \text{ENV}) \approx \text{EXEC}(\rho, \mathcal{S}, \text{ENV})$$

We stress that typically, the (distribution of the) speed coefficient of the realized protocol  $\rho$  depends on the (distribution of the) speed coefficient of the realizing protocol  $\pi$ .

As the notion of secure realization is based on the notion of indistinguishability, we get as a direct consequence the transitivity and reflexivity of  $\geq_t$ .

**Corollary 7.**

$$\Pi \geq_t \Pi \text{ and } \Pi_1 \geq_t \Pi_2 \wedge \Pi_2 \geq_t \Pi_3 \implies \Pi_1 \geq_t \Pi_3$$

*Proof.* The theorem follows directly from the transitivity and reflexivity of  $\approx$ , on which we based our definition of  $\geq_t$ : Due to  $\Pi_1 \geq_t \Pi_2$ , there is a simulator  $\mathcal{S}_1$  such that  $\text{EXEC}(\Pi_1, \mathcal{A}_d, \text{ENV}) \approx \text{EXEC}(\Pi_2, \mathcal{S}_1, \text{ENV})$ . But since  $\Pi_2 \geq_t \Pi_3$ , there exists a simulator  $\mathcal{S}_2$  for  $\mathcal{S}_1$  such that  $\text{EXEC}(\Pi_2, \mathcal{S}_1, \text{ENV}) \approx \text{EXEC}(\Pi_3, \mathcal{S}_2, \text{ENV})$  holds. Due to the transitivity of  $\approx$  we therefore get as required

$$\forall \text{ENV} : \text{EXEC}(\Pi_1, \mathcal{A}_d, \text{ENV}) \approx \text{EXEC}(\Pi_3, \mathcal{S}_2, \text{ENV}).$$

Reflexivity can be shown similarly. □

## 4.2 Properties of Secure Realization

In order to simplify the analysis of complex protocols, traditional composability frameworks depend on central properties of secure realization in their frameworks.

The most important design decisions with regard to showing these properties include making  $\text{ENV}$  and  $\mathcal{A}$  timeless (see Section 3) as well as having machines run with polynomially bounded speed coefficients (see Section 3.1.3). The proofs for the following results are slightly adjusted compared to their counterparts in classic composability frameworks, such as presented in [29], in order to account for timing.

### 4.2.1 Completeness of the Dummy Adversary

The definition of secure realization quantifies over all possible adversaries for the realizing protocol. In order to simplify this, we show that it is sufficient to only consider the dummy adversary  $\mathcal{A}_d$ , which just forwards all messages (with timing information) between environment and network parties. Furthermore, whenever  $\mathcal{A}_d$  is activated without a message, it turns into the  $(\text{listen}, \infty)$  state and waits until it receives a message.

While the central construction for the proof are the same as in classic proofs, where the original adversary  $\mathcal{A}$  is coupled with the dummy adversary  $\mathcal{A}_d$  before they are split, we need to be careful with the additional delay  $\delta$  the dummy adversary  $\mathcal{A}_d$  induces whenever it forwards a message. We deal with this delay  $\delta$  by setting it to be exponentially small, and using the fact that polynomially bounded speed coefficients of regular protocol machines also induce an at least polynomially large gap between two activations of protocol machines: This creates a tunnel in which  $\mathcal{A}_d$  can delay messages without affecting the rest of the network. The rest of the Lemma then follows from Corollary 5.

Before we can show the completeness of the dummy adversary however, we require the following insight, which shows that in a network of machines with polynomial speed coefficient (i.e. make at most a polynomial number of steps per second), there is always an at least polynomial gap time-gap between two activations of machines.



**Corollary 8.** *Given two polynomials  $A, B \in \mathbb{N}[X]$ , there exists a polynomial  $p \in \mathbb{N}[X]$  such that,  $\forall k, l \in \mathbb{N}. \exists n_0. \forall \eta \in \mathbb{N}, \eta > n_0$ . if*

$$\frac{k}{A(\eta)} - \frac{l}{B(\eta)} > 0$$

then

$$\frac{k}{A(\eta)} - \frac{l}{B(\eta)} \geq \frac{1}{p(\eta)}$$

*Proof.*

$$\begin{aligned} \frac{k}{A(\eta)} - \frac{l}{B(\eta)} &\geq \frac{1}{p(\eta)} \\ \Leftrightarrow p(\eta) &\geq \frac{A(\eta)B(\eta)}{kB(\eta) - lA(\eta)} \end{aligned}$$

Since both  $A(\eta)$  and  $B(\eta)$  are natural valued, we know that for all  $\eta > n_0$  we have  $1 \leq kB(\eta) - lA(\eta)$ . Thus, for all  $\eta > n_0$

$$\frac{A(\eta)B(\eta)}{1} \geq \frac{A(\eta)B(\eta)}{kB(\eta) - lA(\eta)}$$

Choosing  $p(\eta) := A(\eta)B(\eta)$  proves the claim.  $\square$

Given Corollary 8 we can now proof the completeness of the dummy adversary.

**Construction of the dummy adversary.** The dummy adversary merely forwards messages from the protocol to the adversary and vice versa.

**$\mathcal{A}_d$ : upon a message  $m$  from a protocol  $\Pi$**

request time from EXEC; wait for result  $t_{\mathcal{A}_d}$   
send  $m$  to ENV at time  $t + 1/2^\eta$   
listen until activated

**$\mathcal{A}_d$ : upon a message  $(m, P, t)$  from the environment ENV**

send  $m$  to  $P$  at time  $t$   
listen until activated

**Lemma 3** (Completeness of the dummy adversary). *If there exists an adversary  $\mathcal{S}$  for a protocol  $\Pi$  such that for all environments ENV,*

$$\text{EXEC}(\Pi', \mathcal{A}_d, \text{ENV}) \approx \text{EXEC}(\Pi, \mathcal{S}, \text{ENV})$$

then  $\Pi' \geq_t \Pi$ .

*Proof.* Take any adversary  $\mathcal{A}$  for  $\Pi$ . We substitute  $\mathcal{A}$  with the dummy adversary  $\mathcal{A}_d$ , and put  $\mathcal{A}$  together with ENV into the new environment  $\text{ENV}'$ , which emulates the execution for ENV and  $\mathcal{A}$ .

The internal simulation of ENV and  $\mathcal{A}$  works as in the proof of the internal simulation lemma (Lemma 2). Since  $\text{ENV}'$  is timeless, it has to decide how to proceed in time. The timer of  $\text{ENV}'$  always matches the minimum of the timers of ENV and  $\mathcal{A}$ , and  $\text{ENV}'$  is in the listen state if the machine with the lowest timer is in the listen state as well. Whenever  $\text{ENV}'$  is activated without a message, ENV internally activates the machine with the lowest timer (independent of state) and then continues to activate the machine with the lowest timer until this machine is in the listen state at time  $T$ . During this process,  $\text{ENV}'$  adds the factor  $\delta$  to  $T_{\mathcal{A}}$  whenever it evaluates which timer is lower, where  $\delta = 1/2^\eta$  is the time the dummy adversary progresses in time on each activation, e.g., whenever  $\text{ENV}'$  decides in which . This off-set is necessary in order to correctly synchronize the dummy adversary and  $\mathcal{A}$ . As a consequence, whenever  $\text{ENV}'$  returns control to EXEC, we have that  $T_{\text{ENV}'} = \min\{T_{\text{ENV}}, T_{\mathcal{A}} + \delta\}$ .

Whenever  $\mathcal{A}$  in  $\text{ENV}'$  sends a message  $m$  to a protocol party  $P$  at time  $t$ ,  $\text{ENV}'$  instructs the dummy adversary  $\mathcal{A}_d$  to send the message at time  $t' := \max\{T_{\text{ENV}}' + \delta, t - \delta\}$  (with the message  $(m, P, t')$ ).

It remains to show that this scenario is indistinguishable for the original network adversary  $\mathcal{A}$ , the original environment ENV, and the protocol  $\Pi$ .

**Claim 1.** *Let  $\text{EXEC}_{\mathcal{A}, \eta}$  be defined as  $\text{EXEC}_\eta$  except that it outputs the sequence of internal states (i.e., the contents of all tapes of all machines) of  $\mathcal{A}$  during the execution. Then,  $\text{EXEC}_{\mathcal{A}, \eta}(\Pi, \mathcal{A}, \text{ENV})$  is indistinguishable from  $\text{EXEC}_{\mathcal{A}, \eta}(\Pi, \mathcal{A}_d, \text{ENV}')$ .*

*Proof.* Recall that the dummy adversary is activated at time  $T$  when a message arrives from the protocol  $\Pi$ . Thus, the environment  $\text{ENV}'$  receives the message at time  $T + \delta$ . Since  $\text{ENV}'$  keeps in its internal simulation a  $\delta$ -gap to the timer of  $\mathcal{A}$  and then directly forwards the messages to  $\mathcal{A}$ ,  $\mathcal{A}$  thinks it receives the messages at time  $T$ .  $\square$

**Claim 2.** Let  $\text{EXEC}_{\text{ENV},\eta}$  be defined as  $\text{EXEC}_\eta$  except that it outputs the sequence of internal states (i.e., the contents of all tapes) of  $\text{ENV}$  during the execution. Then,  $\text{EXEC}_{\text{ENV},\eta}(\Pi, \mathcal{A}, \text{ENV})$  is indistinguishable from  $\text{EXEC}_{\text{ENV},\eta}(\Pi, \mathcal{A}_d, \text{ENV}')$ .

*Proof.* Messages from subroutine ports of regular machines are forwarded to  $\text{ENV}$  with the current time of  $\text{ENV}'$  as (the internally simulated) time-stamp. Since  $T_{\text{ENV}} \geq T_{\text{ENV}'}$ , these messages timely arrive at  $\text{ENV}$ .  $\square$

**Claim 3.** Let  $\text{EXEC}_{\Pi,\eta}$  be defined as  $\text{EXEC}_\eta$  except that it outputs the sequence of internal states (i.e., the contents of all tapes of all machines) of  $\Pi$  during the execution. Then,  $\text{EXEC}_{\Pi,\eta}(\Pi, \mathcal{A}, \text{ENV})$  is indistinguishable from  $\text{EXEC}_{\Pi,\eta}(\Pi, \mathcal{A}_d, \text{ENV}')$ .

*Proof.* It suffices to show the following two properties: (i) messages from the environment are indistinguishable; (ii) messages from the network are indistinguishable.

By the same arguments as in the proof of Lemma 2, messages from the environment are indistinguishable since  $\text{ENV}'$  perfectly simulates  $\text{ENV}$ .

Next, we show that all messages from the dummy adversary  $\mathcal{A}_d$  arrive at a time  $T'$  that is indistinguishable from the time  $T$  at which the network adversary  $\mathcal{A}$  sends the messages. Recall that the time gap of that the dummy adversary for sending a message is  $\delta = 1/2^\eta$ .

We show by induction the  $i$ th message  $\tilde{m}$  received by the dummy adversary  $\mathcal{A}_d$  that  $\mathcal{A}_d$  forwards a message from the adversary  $\mathcal{A}$  either

- 1.) at the time  $T_{\mathcal{A}} = T_{\mathcal{A}_d}$  or
- 2.) at a time  $T_{\mathcal{A}_d}$  such that  $T_\pi + k/2^\eta \geq T_{\mathcal{A}_d} \geq T_{\mathcal{A}} = T_\pi + \delta'$  where  $\delta'$  is some time induced by  $\mathcal{A}$ ,  $k$  is polynomially bounded, and  $T_\pi$  is a protocol time, i.e., a time in which some protocol party can be.

In the base case,  $\mathcal{A}_d$  is by construction in time 0 and listens until it receives a message. Observe that the dummy adversary only receives messages from the adversary  $\mathcal{A}$  and not from  $\text{ENV}$ . Thus, the environment  $\text{ENV}'$  sends the message from the  $\mathcal{A}$  at time  $\max\{\delta, T_{\mathcal{A}} - \delta\}$ . Since the speed coefficient of regular machines are polynomials (see Section 3.1.3), we know by Corollary 8 that the gap between two protocol times is at least  $1/p(\eta)$ , for some polynomial  $p$ . Hence, any protocol machine that is in a listen state will receive messages up to a time  $\geq 1/p(\eta) > \max\{\delta, T_{\mathcal{A}} - \delta\}$ .

In the case that the dummy adversary  $\mathcal{A}_d$  already received previous messages, we distinguish two cases. First, the last message  $m$  that  $\mathcal{A}_d$  received came from the protocol  $\Pi$ . Second, the last message  $m$  that  $\mathcal{A}_d$  received came from  $\mathcal{A}$  (via  $\text{ENV}'$ ).

In the first case, let  $\tilde{T}_{\mathcal{A}_d}$  be the time at which  $\mathcal{A}_d$  received the message  $m$  from the protocol. By construction,  $\mathcal{A}_d$  immediately forwarded the message, hence sent it to  $\mathcal{A}$  (via  $\text{ENV}'$ ) at time  $T_\pi + \delta = T_\pi + 1/2^\eta$ . By construction of  $\text{ENV}'$ ,  $\mathcal{A}$  receives the response  $\tilde{m}$  of  $\mathcal{A}$  at time  $\tilde{T}_{\mathcal{A}_d} + \delta$  and sends it at time  $\tilde{T}_{\mathcal{A}_d} + 2 \cdot \delta$ .

Assume we have by induction hypothesis that for the last message  $m'$  (at some time before  $m$ ) from  $\mathcal{A}$  we have  $T_\pi + k/2^\eta \geq T'_{\mathcal{A}_d} \geq T'_A = T_\pi + \delta'$  where  $T_\pi$  is some protocol time and  $\delta'$  is some gap that is induced by  $\mathcal{A}$ . Hence,  $\mathcal{A}_d$  was able to receive the message  $m$  from the protocol at a time  $\tilde{T}_{\mathcal{A}_d} = T_{\mathcal{A}_d}$  such that  $T_\pi + k/2^\eta \geq T'_{\mathcal{A}_d} \geq T_{\mathcal{A}} = T_\pi + \delta'$ . Thus, we get that  $\mathcal{A}_d$  forwards the response  $\tilde{m}$  of  $\mathcal{A}$  at time  $T_\pi + k + 2 \cdot \delta = T_\pi + k + 2/2^\eta$ .

If we have by induction hypothesis that  $T'_A = T'_{\mathcal{A}_d}$ , then the message is sent at time  $T'_{\mathcal{A}_d} + 2 \cdot \delta = T_{\mathcal{A}_d} + 2 \cdot \delta$ , which satisfies the statement.

In the second case, let  $\tilde{T}_{\mathcal{A}_d}$  be the time at which  $\mathcal{A}_d$  received  $m$ . Let  $m'$  be the  $i - 1$ th message and  $T'_{\mathcal{A}_d}$  be the time-stamp of  $\mathcal{A}_d$  for sending  $m'$  and  $T'_A$  be the time-stamp of  $\mathcal{A}$  for sending  $m'$ . If we have by induction hypothesis that  $T'_A = T'_{\mathcal{A}_d}$  for the time-stamp of  $m'$ , we again distinguish two cases: first,  $\mathcal{A}$  sent the message  $\tilde{m}$  at a time  $T$  such that  $T \geq \tilde{T}_{\mathcal{A}_d} + \delta$ ; second,  $\mathcal{A}$  sent the message  $\tilde{m}$  at a time  $T$  such that  $T < \tilde{T}_{\mathcal{A}_d} + \delta$ . In the first case,  $\mathcal{A}_d$  sends  $\tilde{m}$  at time  $T$  and we have  $T_{\mathcal{A}} = T_{\mathcal{A}_d}$ . In the second case,  $\mathcal{A}_d$  sends  $\tilde{m}$  at time  $T$  and we have  $T_{\mathcal{A}} = T_{\mathcal{A}_d}$ .

If we have by induction hypothesis that for  $m'$  we have  $T_\pi + k/2^\eta \geq T'_{\mathcal{A}_d} \geq T'_A = T_\pi + \delta'$  where  $T_\pi$  is some protocol time, then by the same argumentation as before we get two cases: (i) if  $\mathcal{A}$  sent the

message  $\tilde{m}$  at a time  $T$  such that  $T \geq \tilde{T}_{\mathcal{A}} + k \cdot \delta = \tilde{T}_{\mathcal{A}} + k + 1/2^\eta$ , then  $T_{\mathcal{A}} = T_{\mathcal{A}_d}$ ; if  $\mathcal{A}$  sent the message  $\tilde{m}$  at a time  $T$  such that  $T < \tilde{T}_{\mathcal{A}} + \delta$ , then  $T_\pi + k + 1/2^\eta \geq T'_{\mathcal{A}_d} \geq T'_{\mathcal{A}} = T_\pi + \delta'$  holds.  $\diamond$

By the statement above, we know that either  $\mathcal{A}_d$  sends the messages at the same time as  $\mathcal{A}$ , or  $T_\pi + k/2^\eta \geq T_{\mathcal{A}_d} \geq T_{\mathcal{A}} = T_\pi + \delta'$  for some polynomially bounded  $k$ . In the second case, we use Corollary 8 implies that for any pair of protocol times  $T_p$  and  $T'_p$  the probability that  $|T_\pi - T'_\pi| > 1/p(\eta)$  for any polynomial is negligible in  $\eta$ . Hence, upon a listen command, any machine in the protocol  $\Pi$  receives the same messages in  $\text{EXEC}_{\Pi, \eta}(\Pi, \mathcal{A}, \text{ENV})$  and  $\text{EXEC}_{\Pi, \eta}(\Pi, \mathcal{A}_d, \text{ENV}')$ .  $\square$

As  $\text{ENV}'$  generates the same output as  $\text{ENV}$  and does not generate any additional delays, both situations with  $\text{ENV}'$  and  $\text{ENV}$  generate the same behavior.

Finally, we are in a situation with the environment  $\text{ENV}'$ , the dummy adversary  $\mathcal{A}_d$  and the protocol  $\Pi$ . By assumption we can replace  $\Pi$  with  $\Pi'$  and  $\mathcal{A}_d$  with  $\mathcal{S}$  and remain indistinguishable.

Reverting  $\text{ENV}'$  to  $\text{ENV}$  and  $\mathcal{A}'$  and combining  $\mathcal{S}$  with  $\mathcal{A}'$  to a new simulator  $\mathcal{S}'$  then gives us the theorem. Note that in this step we can again remove all methods in  $\text{ENV}'$  we used to remove the time gap between the dummy adversary  $\mathcal{A}_d$  and the regular adversary  $\mathcal{A}$  since both  $\mathcal{S}$  and  $\mathcal{A}$  now reside in the same machine.  $\mathcal{S}'$  therefore only has to translate all messages from  $\mathcal{S}$  to the environment to network messages to  $\mathcal{A}$ .  $\square$

#### 4.2.2 Composition Theorem

The central building block of simulation-based security is the notion of *composability*: the composition of secure protocols is secure as well. The construction in the proof is exemplified in Figure 10.

**Theorem 2.** *Let  $\Pi$  be a protocol and  $\Pi' = \Pi|x$  a sub-protocol of  $\Pi$  rooted at  $x$ . Suppose that  $\Pi'_1$  is a protocol rooted at  $x$  such that  $\Pi'_1 \geq_t \Pi'$ . Then*

$$\Pi[\Pi'/\Pi'_1] \geq_t \Pi.$$

*Proof.* We construct a simulator  $\mathcal{S}$  for  $\Pi$ , which tries to simulate the interaction between  $\Pi_1$  and  $\mathcal{A}_d$ . By the completeness of the dummy adversary, this is enough to show realization.

**Game 1:** Original network consisting of parties running protocol  $\Pi$ , environment  $\text{ENV}$  and the dummy adversary  $\mathcal{A}_d$ .

**Game 2:** We split the sub-protocol  $\Pi'$  from the protocol  $\Pi$  and the dummy-sub-adversary  $\mathcal{A}_d$  that belongs to  $\Pi'$  from  $\mathcal{A}_d$ . We do not create separate entities, but just see that these are different machines with the following property: All intercepted messages from  $\Pi'$  go directly to  $\mathcal{A}'_d$ .

As the network is otherwise the same as in Game 1, Game 2 and Game 1 are both indistinguishable.

**Game 3:** We define a new environment  $\text{ENV}'$  which internally simulates  $\text{ENV}$ , all protocol parties not running  $\Pi'$ , which we denote as  $\Pi \setminus \Pi'$ , and part of the dummy adversary that does not communicate with  $\Pi'$ , which we denote with  $\mathcal{A}_d \setminus \mathcal{A}'_d$ .

By Lemma: 2,  $\text{ENV}'$  can simulate the interaction between  $\text{ENV}$ ,  $\Pi \setminus \Pi'$  and  $\mathcal{A}_d \setminus \mathcal{A}'_d$  as in the regular network, without introducing additional delays.

**Game 4:** In Game 3 we have the situation where parties running protocol  $\Pi'$  communicate with the corresponding dummy adversary  $\mathcal{A}'_d$  and the environment  $\text{ENV}'$ . Using our assumption, we can now replace  $\Pi'$  with  $\Pi'_1$  and  $\mathcal{A}'_d$  with the simulator  $\mathcal{S}'$  which is constructed in the realization proof of  $\Pi'$  being realized by  $\Pi'_1$ , while remaining indistinguishable from Game 3.

**Game 5:** We now split  $\text{ENV}'$  and recombine  $\Pi \setminus \Pi'$  with  $\Pi'_1$  to get  $\Pi_1$  and  $\mathcal{A}_d \setminus \mathcal{A}'_d$  with  $\mathcal{S}'$  to get the simulator  $\mathcal{S}$ . Due to what  $\text{ENV}'$  simulated, Game 5 is indistinguishable from Game 4.

Using the transitivity of the indistinguishability, we get the claim.  $\square$

#### 4.2.3 Joint State Theorem

A Joint State Theorem [13, 29] simplifies the analysis of a multi-session protocol  $\Pi$  which uses several instances of a single-session protocol  $\pi$  with a joint state between the multiple instances. For example, consider the situation where you give several sub-processes on your machine access to a key exchange process which always uses the same private-/public-key-pair. Here, the Joint State Theorem allows for the reduction of the security analysis of  $\Pi$  to the analysis of a single session ideal functionality  $\mathcal{F}$  which is realized by  $\pi$ .

The Joint State Theorem also holds in our time-sensitive communication model. While the proof is along the lines of the proof in [29, Section 9], we have to carefully consider the timing information the adversary has access to in our communication model (as for the composition theorem).

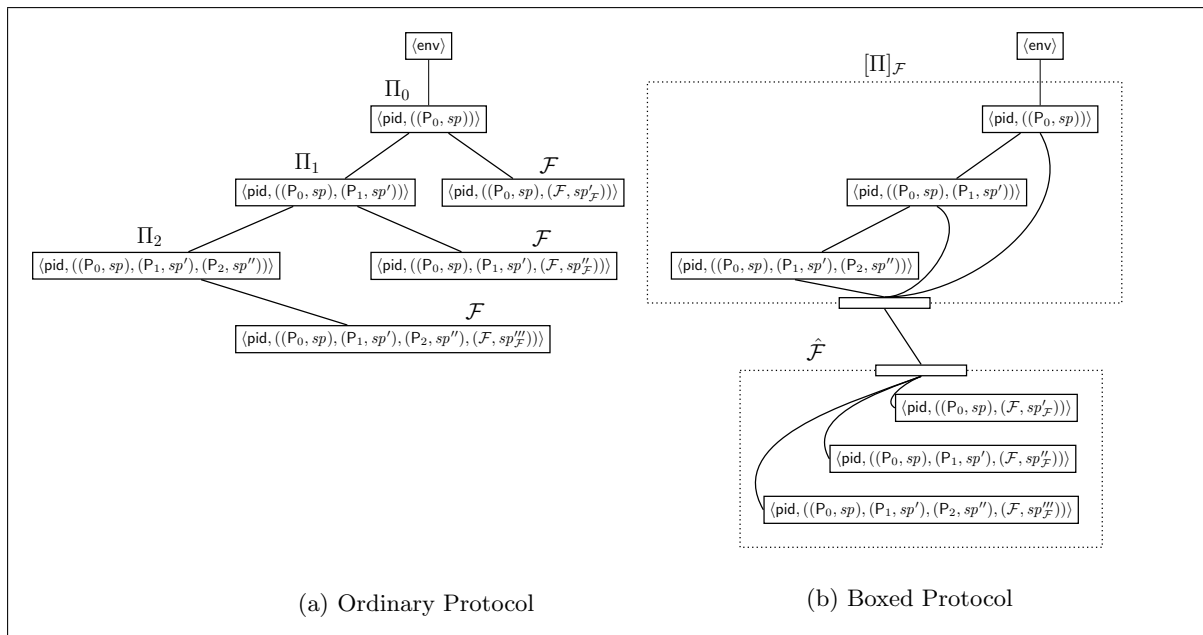


Figure 12: Boxed Protocol  $[\Pi]_{\mathcal{F}}$  and Multi-Session Functionality  $\hat{\mathcal{F}}$

**Multi-session functionality.** In order to analyze multi-session protocols with a joint-state, we need to combine several instances of a single session functionality  $\mathcal{F}$  into a single instance. The multi-session functionality  $\hat{\mathcal{F}}$  collects instances of  $\mathcal{F}$  in the protocol tree of a single party and combines them with a single interface to their callers. This interface filters and distributes incoming messages to the single instances of  $\mathcal{F}$  based on virtual session IDs called *vsid*.

To this end,  $\hat{\mathcal{F}}$  requires messages to be of the form  $(m, vsid)$ . The message  $m$  is then forwarded to the instance  $F$  of  $\mathcal{F}$  with session id *vsid*. Any message  $m'$  going out from an instance  $F'$  of  $\mathcal{F}$  is again brought into the form  $(m', vsid')$ , where *vsid'* is the corresponding session id of  $F'$ .

In the regular network model without time,  $\hat{\mathcal{F}}$  can be realized by a single machine which internally simulates the instances of  $\mathcal{F}$ , as done in GNUC [29]. Consistency enforcing scheduling (Section 3.1.5) allows us to also do this in our time-sensitive communication model as well (see Lemma 2). Thus we can adopt a similar construction, only amending the speed coefficient of the multi-session machine to also account for session ID translation.

**Boxed protocols.** Unfortunately we get following problem as soon as we introduce multi-session protocols and functionalities: As these can be used by many different nodes in the protocol tree of a single party, above construction is not compatible with our construction for a multi-session machine developed in Section 3.2.2, which only allows for a single environmental port. Furthermore, the single caller rule that we adopt from GNUC [29] is no longer enforced. As this rule is essential for the notion of composition and the universal composition theorem, we need to circumvent this problem by introducing so-called *boxed protocols*, as introduced in [29]: the protocol tree outside of the instances of  $\mathcal{F}$  is boxed inside a single TM  $M$ , which simulates every node in the tree. Additionally, all messages sent to a multi-session protocol or functionalities are modified to come from  $M$ . Messages received from the multi-session instance are distributed to the nodes in the tree by  $M$  based in the session ids used in the messages: a message  $m$  incoming on input port  $\text{pid.sid}_1.\text{sid}_2$  is then forwarded as  $(m, \text{sid}_2)$  to the interface of  $\hat{\mathcal{F}}$ , which receives this message on its environmental port. On the other hand, a message  $(m, \text{sid})$  received from  $\hat{\mathcal{F}}$  is forwarded as message  $m$  through the unique output port  $\text{pid.sid}_1.\text{sid}_2$ , where  $\text{sid}_2 = \text{sid}$ . From now on, we denote with  $[\Pi]_{\mathcal{F}}$  the boxed protocol which consists of the  $\mathcal{F}$ -hybrid protocol  $\Pi$ , which is virtually boxed by the TM  $M$  as described above and interacts with the multi-session variant  $\hat{\mathcal{F}}$  of  $\mathcal{F}$ . Using the boxing technique described above gives multi-session functionalities and protocols an interface through which they communicate with only a single caller, allowing us to keep the tree-like structure of parties in the network and use the same notions of composition we also use for single session functionalities and protocols. Figure 12 exemplifies the construction for the boxed protocol  $[\Pi]_{\mathcal{F}}$  together with a multi-session functionality  $\hat{\mathcal{F}}$ .

In contrast to classic constructions, our boxing technique allows us to keep the time-independence of each single session instance of a multi-session protocol. This can be used to more accurately model real world multi-session protocols, where for example different sessions are executed on different machines.

The classic construction would imply that all sessions are parallelized on the same machine and progress through time at the same pace. This can still be captured in our model by requiring that a set of sessions run on the same machine in  $\hat{\mathcal{F}}$ . This requires a small modification in the proof below, where set of instances  $F$  of  $\mathcal{F}$  that are on the same machine in  $\hat{\mathcal{F}}$  are delayed by the simulator by the same amount whenever a single instance  $F_i \in F$  is activated.

We have to impose one restriction to the Joint State Composition Theorem: the protocol has to have an a priori bounded number of protocol machines. The reason can be seen in the proof of internal simulation lemma (Lemma 2). The machine, say  $M$ , that internally runs all other machines has to be sufficiently fast. For an unbounded number of protocol machines, there is always a number of machines such that  $M$  is not sufficiently fast to simulate all internal machines.

With this construction we can now formulate the Joint State Composition Theorem.

**Theorem 3.** *Let  $\mathcal{F}$  be a poly-time ideal functionality and  $\Pi$  be a poly-time,  $\mathcal{F}$ -hybrid protocol with a priori bounded number of protocol machines. Then,  $[\Pi]_{\mathcal{F}} \geq_t \Pi$ .*

*Proof.* We need to construct a simulator  $\mathcal{S}$  such that following holds:

$$\text{EXEC}(\Pi, \mathcal{S}, \text{ENV}) \approx \text{EXEC}([\Pi]_{\mathcal{F}}, \mathcal{A}_d, \text{ENV})$$

As shown in Lemma 2 and since the number of protocol machines is a priori bounded, both machines, the multi-sessions functionality  $\hat{\mathcal{F}}$  and the boxed protocol  $[\Pi]_{\mathcal{F}}$ , simulate their internal machines as in the regular network. We only have to add additional computation time for the translation to and from virtual sessions IDs between  $[\Pi]_{\mathcal{F}}$  and  $\hat{\mathcal{F}}$ . Since this translation is possible in polynomial time we can achieve this by simply accelerating the simulating machines by the appropriate factor.

Corruption needs to be handled separately: While in  $[\Pi]_{\mathcal{F}}$  the whole protocol machine is compromised with one corruption command,  $\mathcal{S}$  needs to compromise each machine in a party separately after the root node has been compromised. This will require the usage of invited messages (see Section 3.1.9).  $\mathcal{S}$  can then use these invitations to compromise all machines in the machine-tree and keep parity with the boxed case.

The only exception that remains is  $\mathcal{S}$  having to handle irregular virtual session IDs which could be sent to  $\hat{\mathcal{F}}$  through a corrupted  $[\Pi]_{\mathcal{F}}$ :  $\Pi$ -impossible virtual session IDs have a basename which specify an instance of  $\mathcal{F}$ , but have a session ID prefix which is invalid with regard to  $\Pi$ .  $\hat{\mathcal{F}}$  cannot distinguish such session IDs from regular ones, hence  $\mathcal{S}$  needs to catch them and return an error to ENV.  $\mathcal{S}$  cannot do so directly however, but needs to simulate a run with a message from ENV to the network containing such a  $\Pi$ -impossible sessions ID in order to find the right time to send the response to ENV. But as  $\mathcal{S}$  is timeless and the simulation of the network is possible in poly-time,  $\mathcal{S}$  can do so without a problem.  $\square$

This concludes the introduction of secure realization in TUC and the presentation of its properties. The next sections show how to formalize the onion routing protocol that underlies Tor [54] in TUC and how to prove this formalization secure in TUC.

## 5 Time Sensitive Analysis of the Onion Routing Protocol

In this section we take the framework we presented in Sections 3 and 4 and exemplify its use in the time-sensitive analysis of the anonymous communication protocol Tor [54]. Anonymous communication protocols as provided by the Tor network are an increasingly popular way for users to protect their privacy by hiding the user's location. The Tor network is currently used by hundreds of thousands of users around the world [53].

Section 5.1 defines the onion routing (OR) protocol as a protocol  $\Pi_{\text{OR}}$  in the TUC framework. Section 5.2 presents our abstraction of the OR protocol by defining the ideal functionality  $\mathcal{F}_{\text{OR}}$ . Finally, Section 5.3 shows that  $\Pi_{\text{OR}}$  securely realizes  $\mathcal{F}_{\text{OR}}$ , i.e., we show that the abstraction  $\mathcal{F}_{\text{OR}}$  is sound in the sense that every attack against  $\Pi_{\text{OR}}$  can also be mounted against  $\mathcal{F}_{\text{OR}}$ .

Considering a time-sensitive adversary imposes new challenges on the analysis of complex, cryptographic communication protocols. As in previous work [3], we required cryptographic properties from the onion algorithms and the key exchange that ensure authenticity, integrity, secrecy and unlinkability. Against an adversary that can measure the time of a computation, however, we have to additionally

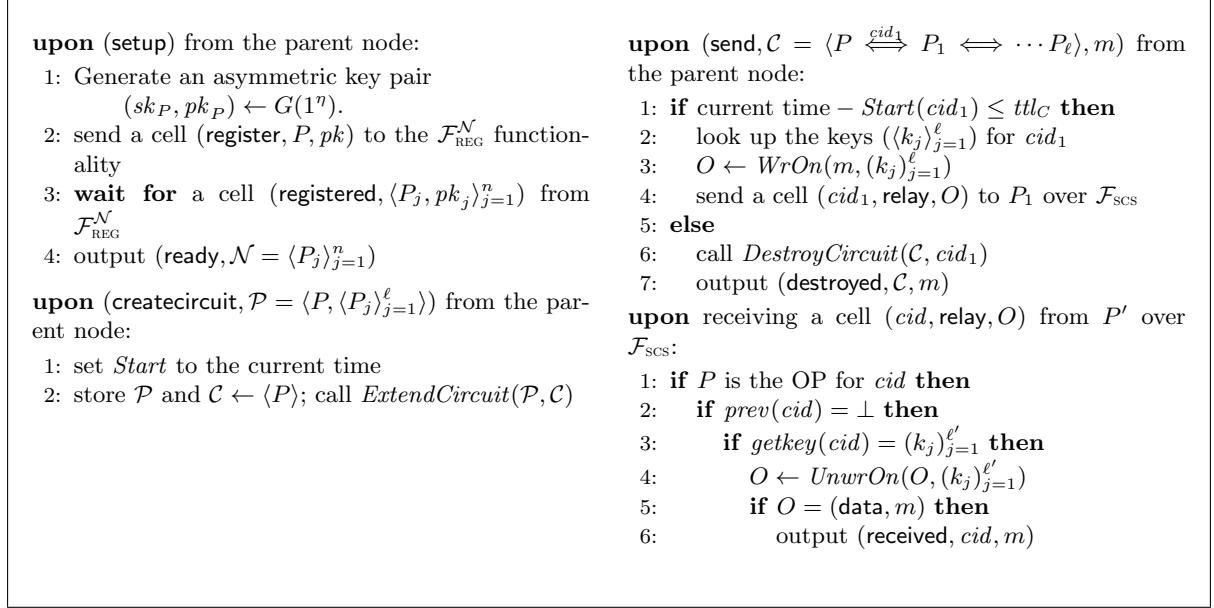


Figure 13:  $\Pi_{\text{OR}}$ : Client Protocol for Machine  $P$

require that the computation time of an encryption does not leak anything about the plaintext message, and we have to require that the DDH exponentiation does not leak anything about the exponents. We rigorously formalize these requirements in Section 5.3.1.

## 5.1 The Onion Routing Protocol

The core idea behind Tor is that, instead of directly communicating with the target, the user reroutes his traffic over a sequence of three onion routers. Smart use of cryptography then ensures that each participant in this chain only knows about his predecessor and successor, thus enabling anonymous communication.

Tor centrally organizes and validates available OR nodes and distributes their public keys to users. After the initial set up in which public keys of the onion routers (OR) are distributed, Tor works in two phases: In the first phase, the user establishes temporary symmetric keys with each of the three chosen onion routers, using the public keys of the ORs in a one-way authenticated key-exchange (1W-AKE) [25].<sup>6</sup> The sequence of ORs together with these symmetric keys is called a *circuit*. The exchanged keys are only used for one session, which typically lasts 10 minutes; then, fresh keys are established and the old keys are securely erased.<sup>7</sup>

In the second phase, the user performs a layered encryption of each message block, and sends the ciphertext, called *onion*, through the established circuit, where each OR decrypts one layer of encryption to learn where the onion should be sent next.

Using the same TCP stream that the last onion router in the circuit opened, the recipient can respond: in this case, each onion router adds a layer of encryption and the user removes all layers.

As presented in [3], the onion routing protocol used in Tor can be formalized by the protocol  $\Pi_{\text{OR}}$  presented in Figures 13, 15 and 14.  $\Pi_{\text{OR}}$  closely follows the Tor specification [18] and (for simplicity reason) assumes a fixed number  $\mathcal{N}$  of protocol participants. We further assume that every party can be both user as well as onion router. We denote the subprotocol of the user as *client protocol*.

In contrast to the presentation in [3], we do not need to approximate the *time-to-live* (denoted as  $ttl_C$ ) of a circuit  $\mathcal{C}$  as the number of messages a user can send through  $\mathcal{C}$ : since the network model we introduce further down includes the notion of time,  $ttl_C$  can directly give the time for which a circuit can live before it is torn down (e.g. 10 minutes).

The protocol  $\Pi_{\text{OR}}$  uses several cryptographic algorithms in order to realize its different tasks: For the 1W-AKE,  $\Pi_{\text{OR}}$  uses the three algorithms *Initiate*, *Respond* and *ComputeKey*, which we introduce

<sup>6</sup>Tor is currently migrating to a more efficient and more secure 1W-AKE scheme (the *ntor* protocol). Recent work (the *Ace* scheme [6]) further improves on *ntor*.

<sup>7</sup>Temporary keys enable immediate forward secrecy: after a session is dead (and its temporary key and its communication transcripts is securely erased) even compromised parties do not leak anything about previous communications.

<p><i>ExtendCircuit</i>(<math>\mathcal{P} = \langle P_j \rangle_{j=1}^\ell, \mathcal{C} = \langle P \xleftrightarrow{cid_1, k_1} P_1 \xleftrightarrow{k_2} \dots P_{\ell'} \rangle</math>):</p> <ol style="list-style-type: none"> <li>1: determine the next node <math>P_{\ell'+1}</math> from <math>\mathcal{P}</math> and <math>\mathcal{C}</math></li> <li>2: <b>if</b> <math>P_{\ell'+1} = \perp</math> <b>then</b></li> <li>3:   output (<b>created</b>, <math>\langle P \xleftrightarrow{cid_1} P_1 \xleftrightarrow{\dots} P_{\ell'} \rangle</math>)</li> <li>4: <b>else</b></li> <li>5:   <math>X \leftarrow \text{Initiate}(pk_{P_{\ell'+1}}, P_{\ell'+1})</math></li> <li>6:   <b>if</b> <math>P_{\ell'+1} = P_1</math> <b>then</b></li> <li>7:     <math>cid_1 \leftarrow \{0, 1\}^k</math></li> <li>8:     send a cell (<math>cid_1, \text{create}, X</math>) to <math>P_1</math> over <math>\mathcal{F}_{\text{SCS}}</math></li> <li>9:   <b>else</b></li> <li>10:    <math>O \leftarrow \text{WrOn}(\{\text{extend}, P_{\ell'+1}, X\}, (k_j)_{j=1}^{\ell'})</math></li> <li>11:    send a cell (<math>cid_1, \text{relay}, O</math>) to <math>P_1</math> over <math>\mathcal{F}_{\text{SCS}}</math></li> </ol>	<p><i>DestroyCircuit</i>(<math>\mathcal{C}, cid</math>):</p> <ol style="list-style-type: none"> <li>1: <b>if</b> <math>\text{next}(cid) = (P_{\text{next}}, cid_{\text{next}})</math> <b>then</b></li> <li>2:   send a cell (<math>cid_{\text{next}}, \text{destroy}</math>) to <math>P_{\text{next}}</math> over <math>\mathcal{F}_{\text{SCS}}</math></li> <li>3: <b>else if</b> <math>\text{prev}(cid) = (P_{\text{prev}}, cid_{\text{prev}})</math> <b>then</b></li> <li>4:   send a cell (<math>cid_{\text{prev}}, \text{destroy}</math>) to <math>P_{\text{prev}}</math> over <math>\mathcal{F}_{\text{SCS}}</math></li> <li>5: discard <math>\mathcal{C}</math> and all streams</li> </ol>
---	---

Figure 14: Subroutines of  $\Pi_{\text{OR}}$  for Party  $P$

further below.<sup>8</sup> For adding and removing encryption layers to the payload (plaintext or onion), i.e. as principal onion algorithms,  $\Pi_{\text{OR}}$  uses the two algorithms  $\text{WrOn}$  and  $\text{UnwrOn}$ .  $\text{WrOn}$  creates a layered encryption of the payload, given an ordered list of  $\ell$  session keys for  $\ell \geq 1$ .  $\text{UnwrOn}$  removes  $\ell$  layers of encryptions from an onion to output the payload, given an input onion and an ordered list of  $\ell$  session keys for  $\ell \geq 1$ .

We consider two kinds of messages in the description of  $\Pi_{\text{OR}}$ : network messages and user inputs. Network messages are used by the protocol to exchange *cells* between the parties. These are used for protocol level interactions such as creating a circuit or relaying a message. Input messages are sent by a user to his onion proxy in order to initiate a circuit construction or the sending of a message.

**Circuits in  $\Pi_{\text{OR}}$ .** A circuit  $\mathcal{C}$  is represented in  $\Pi_{\text{OR}}$  by a sequence of *circuit ids* ( $cid \in \{0, 1\}^*$ ), each of which is known only to two consecutive nodes in the circuit  $\mathcal{C}$ . At a node  $P_i$  we denote an established circuit using the terminology  $\mathcal{C} = P_{i-1} \xleftrightarrow{cid_i, k_i} P_i \xleftrightarrow{cid_{i+1}} P_{i+1}$ . Here,  $P_{i-1}$  and  $P_{i+1}$  are the predecessor and successor of  $P_i$  in the circuit  $\mathcal{C}$  and  $k_i$  is the session key established between  $P_i$  and the OP (who initiated this circuit). The absence of  $k_{i+1}$  indicates that the session key between  $P_{i+1}$  and the OP is not known to  $P_i$ . The functions  $\text{prev}$  and  $\text{next}$  on  $cid$  correspondingly give information about the predecessor or successor of the current node with respect to  $cid$ ; e.g.,  $\text{next}(cid_i)$  returns  $(P_{i+1}, cid_{i+1})$  and  $\text{next}(cid_{i+1})$  returns  $\perp$ .

In the next section we go into detail about the different messages exchanged in  $\Pi_{\text{OR}}$ .

### 5.1.1 User inputs

In this section, we present the commands that a user can send: a initialization command (**setup**), a command for circuit creation (**createcircuit**), and a command for sending message (**send**).

**Key registration.** Upon an input (**setup**), an OR node computes its long-term keys ( $sk, pk$ ) and registers these keys.

In  $\Pi_{\text{OR}}$  the key registration and distribution is modeled as an ideal functionality  $\mathcal{F}_{\text{REG}}^{\mathcal{N}}$ , which is defined as in [11] with the exception that  $\mathcal{F}_{\text{REG}}^{\mathcal{N}}$  rejects all parties not in  $\mathcal{N}$  and only distributes the public keys after all parties in  $\mathcal{N}$  have registered with  $\mathcal{F}_{\text{REG}}^{\mathcal{N}}$ . As soon as all parties have registered, each of them receives the message (**registered**,  $\langle P_j, pk_j \rangle_{j=1}^n$ ), which contains a list of all valid OR nodes, together with their public keys.

**Circuit creation.** Upon an input **createcircuit** (see Figure 13), the OP starts the circuit creation process, which consists of the 1W-AKE for establishing the session key, and the actual circuit creation: The OP, as the initiator, runs the *Initiate* algorithm to draw new key-exchange information and sends this to the first node of the circuit inside a **create** cell (see Figure 14). The first node then runs the

<sup>8</sup>Tor currently uses the TAP protocol and is going to switch to the more efficient and secure **ntor** protocol. [25]

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upon receiving an input  $(\text{exit}, \text{sid}, m)$  from the parent
1: obtain  $\mathcal{C} = \langle P' \xleftrightarrow{\text{cid}, k} P \rangle$  for sid
2:  $O \leftarrow \text{WrOn}(m, k)$ 
3: send a cell  $(\text{cid}, \text{relay}, O)$  to  $P'$  over  $\mathcal{F}_{\text{SCS}}$ 
upon receiving a cell  $(\text{cid}, \text{create}, X)$  from  $P'$  over  $\mathcal{F}_{\text{SCS}}$ :
1:  $\langle Y, k_{\text{new}} \rangle \leftarrow \text{Respond}(pk_P, sk_P, X)$ 
2: store  $\mathcal{C} \leftarrow \langle P' \xleftrightarrow{\text{cid}, k_{\text{new}}} P \rangle$ 
3: send a cell  $(\text{cid}, \text{created}, Y, t)$  to  $P'$  over  $\mathcal{F}_{\text{SCS}}$ 
upon receiving a cell  $(\text{cid}, \text{created}, Y, t)$  from  $P'$  over  $\mathcal{F}_{\text{SCS}}$ :
1: if  $\text{prev}(\text{cid}) = (P', \text{cid}', k')$  then
2:    $O \leftarrow \text{WrOn}(\text{extended}, Y, t, k')$ 
3:   send a cell  $(\text{cid}', \text{relay}, O)$  to  $P'$  over  $\mathcal{F}_{\text{SCS}}$ 
4: else if  $\text{prev}(\text{cid}) = \perp$  then
5:    $k_{\text{new}} \leftarrow \text{ComputeKey}(pk_i, Y, t)$ 
6:   update  $\mathcal{C}$  with  $k_{\text{new}}$ ; call  $\text{ExtendCircuit}(\mathcal{P}, \mathcal{C})$ 
upon receiving a cell  $(\text{cid}, \text{destroy})$  from  $P'$  over  $\mathcal{F}_{\text{SCS}}$ :
1: call  $\text{DestroyCircuit}(\mathcal{C}, \text{cid})$ 
upon receiving a cell  $(\text{cid}, \text{relay}, O)$  from  $P'$  over  $\mathcal{F}_{\text{SCS}}$ :
1: if  $\text{prev}(\text{cid}) = \perp$  then
2:   if  $\text{getkey}(\text{cid}) = (k_j)_{j=1}^{\ell'}$  then
3:      $O \leftarrow \text{UnwrOn}(O, (k_j)_{j=1}^{\ell'})$ 
4:   if  $(\text{type}, m) \neq O$  then abort
5:   else if  $\text{prev}(\text{cid}) = (P'', \text{cid}', k')$  then
6:     /* a backward onion */
7:      $O \leftarrow \text{WrOn}(O, k')$ ;  $\text{type} \leftarrow \text{default}$ 
8:   switch ( $\text{type}$ )
9:   case extend:
10:     $(P_{\text{next}}, X) \leftarrow m$ ;  $\text{cid}_{\text{next}} \leftarrow \{0, 1\}^{\kappa}$ 
11:    update  $\mathcal{C} \leftarrow \langle P' \xleftrightarrow{\text{cid}, k} P \xleftrightarrow{\text{cid}_{\text{next}}} P_{\text{next}} \rangle$ 
12:    send a cell  $(\text{cid}_{\text{next}}, \text{create}, X)$  to  $P_{\text{next}}$  over  $\mathcal{F}_{\text{SCS}}$ 
13:   case extended:
14:    get  $\langle Y, t \rangle$  from  $m$ 
15:    let  $P_{\text{ex}}$  be the first party in  $\mathcal{P}$  without a key in  $\mathcal{C}$ 
16:     $k_{\text{ex}} \leftarrow \text{ComputeKey}(pk_{\text{ex}}, Y, t)$ 
17:    update  $\mathcal{C}$  with  $(k_{\text{ex}})$ ; call  $\text{ExtendCircuit}(\mathcal{P}, \mathcal{C})$ 
18:   case data:
19:    if  $P$  is the OP for  $\text{cid}$  then output  $(\text{received}, \text{cid}, m)$ 
20:    else if  $m = (P'', m')$ 
21:      //  $P$  is the exit node for  $\text{cid}$ 
22:      generate or lookup the unique sid for  $\text{cid}$ 
23:      output  $(\text{exit}, (P'', (\text{sid}, m')))$  to parent
24:   case corrupted: /*corrupted onion*/
25:     call  $\text{DestroyCircuit}(\mathcal{C}, \text{cid})$ 
26:   case default: /*encrypted forward/backward onion*/
27:     if  $\text{prev}(\text{cid}) = \perp$  then  $(P'', \text{cid}') = \text{next}(\text{cid})$ 
28:     send a cell  $(\text{cid}', \text{relay}, O)$  to  $P''$  over  $\mathcal{F}_{\text{SCS}}$ 

```

Figure 15:  $\Pi_{\text{OR}}$  for Machine  $P$ : Network Messages for an Onion Router.

*Respond* algorithm and responds with a created cell. After receiving this response, the OP runs the *ComputeKey* algorithm to compute the session key.

For extending a circuit past the first node, the OP runs the *Initiate* algorithm and sends an extend relay cell, which causes the currently last node of the circuit to send a create cell to the next node and so on.

**Sending messages.** Communication in the forward direction is initiated by a send message from the user to his OP, while communication in the backward direction is initiated by a network message to the exit node (from the recipient).

### 5.1.2 Network Messages

In Tor, each pair of onion routers establishes a TLS connection for ensuring the integrity of onions and for hiding the circuit identifiers from a network observer. In  $\Pi_{\text{OR}}$ , we abstract such a TLS connection by a functionality  $\mathcal{F}_{\text{SCS}}$  as proposed by Canetti [11].<sup>9</sup>

Communication between servers (outside of the Tor network) and exit nodes (i.e., the last OR in the circuit) is synchronized using TCP streams.  $\Pi_{\text{OR}}$  abstracts from these streams by introducing a session identifier sid.

**Relay cells.** relay cells are used for tunneling commands such as data, extend and extended through an established (part of a) circuit. Communication between the OP and the exit node in the forward direction is implemented via a *WrOn* call with all session keys exchanged during the circuit creation, and a series of *UnwrOn* calls at each of the ORs in the circuit with the individual session keys they know. In contrast to previous work, an exit node does not send the message over the network but rather outputs the message to the environment with a exit string as prefix to mark the message as an exit message. This exit messages are important for being able to apply the countermeasure.

<sup>9</sup>The leakage function  $l$  for  $\mathcal{F}_{\text{SCS}}$  we use here is  $l(m) := |m|$ .



<p><b>upon input (setup):</b></p> <ol style="list-style-type: none"> <li>1: draw a fresh handle <math>h</math>; set <code>registered_flag</code> <math>\leftarrow</math> <code>true</code></li> <li>2: store <math>lookup(h) \leftarrow (\text{dir}, \text{registered}, \mathcal{N})</math></li> <li>3: lookup the current time <math>t</math>; let <math>t' \leftarrow d_1</math></li> <li>4: send <math>(h, \text{register}, P)</math> to the network at time <math>t + t'</math></li> <li>5: <b>wait for</b> a msg <math>(\text{dir}, \text{registered}, \mathcal{N})</math> via a handle</li> <li>6: lookup the current time <math>t</math></li> <li>7: let <math>t' \leftarrow d_2</math></li> <li>8: output <math>(\text{ready}, (P_j)_{j=1}^n) = (\text{ready}, \mathcal{N})</math> at time <math>t + t'</math></li> </ol> <p><b>upon input (createcircuit, <math>\mathcal{P} = \langle P, P_1, \dots, P_\ell \rangle</math>)</b></p> <ol style="list-style-type: none"> <li>1: set <math>Start</math> to the current time</li> <li>2: store <math>\mathcal{P}</math> and <math>\mathcal{C} \leftarrow \langle P \rangle</math></li> <li>3: let <math>t \leftarrow d_3</math></li> <li>4: <math>ExtendCircuit(\mathcal{P}, \mathcal{C}, t)</math></li> </ol>	<p><b>upon receiving a handle <math>(P', P, h)</math> from the network</b></p> <ol style="list-style-type: none"> <li>1: <b>if</b> <math>(P, cid, \text{relay}, (\text{data}, m)) = lookup(h)</math> and <math>P</math> is the OP for <math>cid</math> <b>then</b></li> <li>2: lookup the current time <math>t</math>; let <math>t' \leftarrow d_6</math></li> <li>3: <b>if</b> <math>prev(cid) = \perp</math> <b>then</b></li> <li>4: output <math>(\text{received}, cid, m)</math> at time <math>t + t'</math></li> </ol> <p><b>upon input (send, <math>\mathcal{C} = \langle P \xleftrightarrow{cid_1} P_1 \iff \dots P_\ell \rangle, m</math>)</b></p> <ol style="list-style-type: none"> <li>1: <b>if</b> current time <math>- Start(cid_1) \leq ttl_C</math> <b>then</b></li> <li>2: let <math>t \leftarrow d_4</math></li> <li>3: <math>SendMessage(P_1, cid_1, \text{relay}, (\text{data}, m), t)</math></li> <li>4: <b>else</b></li> <li>5: <math>DestroyCircuit(\mathcal{C}, cid_1)</math></li> <li>6: lookup the current time <math>t</math></li> <li>7: let <math>t' \leftarrow d_5</math></li> <li>8: output <math>(\text{destroyed}, \mathcal{C}, m)</math> at time <math>t + t'</math></li> </ol>
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Figure 16: The ideal functionality  $\mathcal{F}_{\text{OR}}^{\mathcal{N}}$  (short  $\mathcal{F}_{\text{OR}}$ ) for Machine  $P$ : Client

In the backward direction communication is implemented using a series of *WrOn* calls by the ORs in the network with the individual session keys, and finally a *UnwrOn* call at the OP.

**Tearing down a circuit.** To tear down a circuit (e.g. if a session expires after  $ttl_C$  time), an OR or OP sends the `destroy` cell to the neighboring nodes in the circuit along with the corresponding  $cid$  (see Figure 14). Upon receiving a `destroy` cell, the node frees resources associated with the corresponding circuit. Once the `destroy` cell has been processed, the node ignores all future cells from the corresponding circuit.

A `destroy` cell is also in that situation sent through the circuit if an integrity check fails during an *UnwrOn* call. A failed integrity check means that the adversary somehow tinkered with the onion that was being processed, and  $\Pi_{\text{OR}}$  counters this by dropping the affected circuit and creating a new one.

This concludes the presentation of the onion routing protocol. We will use it in Section 5.3, where we show secure realization of our time-sensitive abstraction of Tor we present in the next section.

## 5.2 Time-sensitive Abstraction of OR

Tor is a low latency communication protocol and hence is prone to all kinds of traffic pattern analyses, such as traffic confirmation attacks or website fingerprinting attacks. As in previous work, we want to accurately model all weaknesses of the OR protocol. As a consequence, our anonymous channel functionality has to leak all these communication patterns, while still abstracting from all cryptographic operations, thereby allowing to accurately capture the leakage of the OR protocol and the capabilities of a time-sensitive adversary.

Our abstraction goes along the lines of previous work [3]. However we additionally have to compensate for computation time differences that appear in the ideal functionality: the ideal functionality does not perform any cryptographic operations and therefore often has less computation steps to perform than the real protocol. In the functionality we use the delayed sending commands presented in Figure 3 to compensate for these differences.

### 5.2.1 Review of $\mathcal{F}_{\text{OR}}$

The ideal functionality  $\mathcal{F}_{\text{OR}}$ , as presented in Figure 16, 17, and 18, is close to the OR protocol  $\Pi_{\text{OR}}$  presented in Section 5.1. Due to the similarities of  $\Pi_{\text{OR}}$  and  $\mathcal{F}_{\text{OR}}$ , we concentrate on highlighting the differences between them.

The major difference between  $\Pi_{\text{OR}}$  and  $\mathcal{F}_{\text{OR}}$  is that  $\mathcal{F}_{\text{OR}}$  does not use any cryptography: the session keys, the onion methods *WrOn* and *UnwrOn*, and 1W-AKE methods *Initiate*, *Respond*, and *ComputeKey* are absent in  $\mathcal{F}_{\text{OR}}$ .

<p><b>upon</b> receiving (compromise) from <math>\mathcal{A}</math>:</p> <ol style="list-style-type: none"> <li>1: set <math>compromised \leftarrow true</math>; delete local information</li> </ol> <p><b>upon</b> receiving <math>(-, P, h, [corrupt, T(\cdot)])</math> from the network:</p> <ol style="list-style-type: none"> <li>1: <math>msg \leftarrow lookup(h)</math></li> <li>2: <b>if</b> <math>corrupt = true</math> <b>then</b></li> <li>3:   <math>msg \leftarrow T(msg)</math></li> <li>4:   set <math>corrupted(msg) \leftarrow true</math></li> <li>5: lookup the current time <math>t</math>; let <math>t' \leftarrow d_{11}</math></li> <li>6: proceed with <math>msg</math> at time <math>t + t'</math></li> </ol> <p><b>upon</b> receiving an input (response, sid, <math>m</math>) from the parent</p> <ol style="list-style-type: none"> <li>1: obtain <math>\mathcal{C} = \langle P' \xleftrightarrow{cid} P \rangle</math> for sid</li> <li>2: lookup the current time <math>t</math>; let <math>t' \leftarrow d_{15}</math></li> <li>3: <math>SendMessage(P', cid, relay, (data, m), t + t')</math></li> </ol> <p><b>upon</b> receiving a handle <math>(-, P, h)</math> from the network:</p> <ol style="list-style-type: none"> <li>1: lookup the current time <math>t</math>; let <math>t' \leftarrow d_6</math></li> <li>2: proceed with <math>msg \leftarrow lookup(h)</math> at time <math>t + t'</math></li> </ol> <p><b>upon</b> receiving a msg <math>(P', cid, create)</math> through a handle:</p> <ol style="list-style-type: none"> <li>1: store <math>\mathcal{C} \leftarrow \langle P' \xleftrightarrow{cid} P \rangle</math></li> <li>2: let <math>t \leftarrow d_7</math></li> <li>3: <math>SendMessage(P', cid, created, t)</math></li> </ol> <p><b>upon</b> receiving <math>(P', cid, created)</math> through a handle:</p> <ol style="list-style-type: none"> <li>1: <b>if</b> <math>prev(cid) = (P'', cid')</math> <b>then</b></li> <li>2:   let <math>t \leftarrow d_8</math></li> <li>3:   <math>SendMessage(P'', cid', relay, extended, t)</math></li> <li>4: <b>else if</b> <math>prev(cid) = \perp</math> <b>then</b></li> <li>5:   let <math>t \leftarrow d_9</math></li> <li>6:   <math>ExtendCircuit(\mathcal{P}, \mathcal{C}, t)</math></li> </ol>	<p><b>upon</b> receiving <math>(P', cid, destroy)</math> through a handle:</p> <ol style="list-style-type: none"> <li>1: let <math>t \leftarrow d_{14}</math>; <math>DestroyCircuit(\mathcal{C}, cid, t)</math></li> </ol> <p><b>upon</b> receiving <math>(P', cid, relay, O)</math> through a handle:</p> <ol style="list-style-type: none"> <li>1: <b>if</b> <math>prev(cid) = \perp</math> <b>then</b> <math>(type, m) \leftarrow O</math></li> <li>2: <b>else</b> <math>(P'', cid') \leftarrow prev(cid)</math>; <math>type \leftarrow default</math></li> <li>3: <b>switch</b> (type)</li> <li>4: <b>case</b> extend:</li> <li>5:   <math>P_{next} \leftarrow m</math>; <math>cid_{next} \leftarrow \{0, 1\}^k</math></li> <li>6:   update <math>\mathcal{C} \leftarrow \langle P' \xleftrightarrow{cid} P \xleftrightarrow{cid_{next}} P_{next} \rangle</math></li> <li>7:   let <math>t \leftarrow d_{10}</math></li> <li>8:   <math>SendMessage(P_{next}, cid_{next}, create, t)</math></li> <li>9: <b>case</b> extended:</li> <li>10:   update <math>\mathcal{C}</math> with <math>P_{ex}</math></li> <li>11:   <math>ExtendCircuit(\mathcal{P}, \mathcal{C})</math></li> <li>12: <b>case</b> data:</li> <li>13:   <b>if</b> <math>(P = OP)</math> <b>then</b> output (received, <math>cid, m</math>)</li> <li>14:   <b>else if</b> <math>m = (P'', m')</math></li> <li>15:     generate or lookup the unique sid for <math>cid</math></li> <li>16:     output (exit, <math>(P'', (sid, m'))</math>) to parent</li> <li>17: <b>case</b> corrupt: /*corrupted onion*/</li> <li>18:   let <math>t \leftarrow d_{12}</math></li> <li>19:   <math>DestroyCircuit(\mathcal{C}, cid, t)</math></li> <li>20: <b>case</b> default: /*encrypted forward/backward onion*/</li> <li>21:   <b>if</b> <math>prev(cid) = \perp</math> <b>then</b> <math>(P'', cid') = next(cid)</math></li> <li>22:   let <math>t \leftarrow d_{13}</math></li> <li>23:   <math>SendMessage(P'', cid', relay, O, t)</math></li> </ol>
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Figure 17: The ideal functionality  $\mathcal{F}_{OR}^N$  (short  $\mathcal{F}_{OR}$ ) for Machine  $P$ : Network messages for an onion router

In fact,  $\mathcal{F}_{OR}$  does not need any cryptography: Instead of relying on the security of onion algorithms, messages are exchanged via shared memory: shared memory is an additional abstraction added to  $\mathcal{F}_{OR}$ , which allows all parties running  $\mathcal{F}_{OR}$  to exchange messages “off-band”.

Now if party  $P$  wants to send a message  $m$  to party  $P_{next}$ ,  $P$  creates a fresh handle  $h$ , saves  $m$  in the shared memory under this handle and sends  $\langle P, P_{next}, h \rangle$  over the network.

$\mathcal{F}_{OR}$  also does not require  $\mathcal{F}_{REG}^N$  for the initial distribution of public keys (it does not really need any public keys at all): instead, on input (setup), the party  $P$  notes its registration in the shared memory, and, as soon as all other parties in the network also noted their registration, outputs a successful registration to the caller.

**Compromising parties.** A party running  $\mathcal{F}_{OR}$  cannot be compromised: instead, upon receiving a compromise message from the adversary, the respective party sets its  $compromised$  variable to  $true$ . Then, all input or network messages that are visible to the compromised entity are forwarded to the adversary. In principle, the adversary runs that entity and can send messages from that entity.

**Explicit leakage: visible subpaths.** For proving secure realization for  $\mathcal{F}_{OR}$ , we require a special behavior by compromised parties. In case the adversary manages to compromise an entire subpath  $S$  of a circuit, the first node in  $S$  needs to leak all information that would have been leaked by each node in  $S$  individually in the real world: the simulator constructed for the realization proof in [3] does not learn about circuits constructed in the network and neither about the messages transmitted through the network. But the simulator would need this information for correctly simulating the behavior of the real parties (running  $\Pi_{OR}$ ), if it only had the individual leakage of the parties in the compromised subpath.

<pre> ExtendCircuit(<math>\mathcal{P} = (P_j)_{j=1}^{\ell}, \mathcal{C} = \langle P \xleftrightarrow{cid_1} P_1 \iff \dots P_{\ell'} \rangle</math>, [time]): 1: determine the next node <math>P_{\ell'+1}</math> from <math>\mathcal{P}</math> and <math>\mathcal{C}</math> 2: <b>if</b> <math>P_{\ell'+1} = \perp</math> <b>then</b> 3:   output (created, <math>\mathcal{C}</math>) 4: <b>else</b> 5: <b>if</b> <math>P_{\ell'+1} = P_1</math> <b>then</b> 6:   <math>cid_1 \leftarrow \{0, 1\}^{\kappa}</math> 7:   <math>SendMessage(P_1, cid_1, create</math>, [time]) 8: <b>else</b> 9:   <math>SendMessage(P_1, cid_1, relay, (extend, P_{\ell'+1})</math>, [time]) DestroyCircuit(<math>\mathcal{C}, cid</math>, [time]): 1: <b>if</b> <math>next(cid) = (P_{next}, cid_{next})</math> <b>then</b> 2:   <math>SendMessage(P_{next}, cid_{next}, destroy</math>, [time]) 3: <b>else if</b> <math>prev(cid) = (P_{prev}, cid_{prev})</math> <b>then</b> 4:   <math>SendMessage(P_{prev}, cid_{prev}, destroy</math>, [time]) 5: discard <math>\mathcal{C}</math> and all streams </pre>	<pre> SendMessage(<math>P_{next}, cid_{next}, cmd</math>, [relay-type], [data], [time]): 1: draw a fresh handle <math>h</math> 2: set <math>lookup(h) \leftarrow (P_{next}, cid, cmd</math>, [relay-type], [data]) 3: <b>if</b> <math>compromised = true</math> <b>then</b> 4:   let <math>P_{last}</math> be the last node in the complete contiguous visible subpath starting <math>P_{next}</math> 5:   <b>if</b> (<math>P_{last} = OP</math>) or <math>P_{last}</math> is the exit node and <math>data \neq \perp</math> <b>then</b> 6:     <math>msg' \leftarrow lookup(h')</math> 7:     lookup the current time <math>t</math>; send the entire message <math>\langle P, P_{next}, \dots, P_{last}, cid_{next}, cmd, data \rangle</math> to <math>\mathcal{A}</math> at time <math>t[+time]</math> 8:   <b>else</b> send <math>\langle P, P_{next}, \dots, P_{last}, cid_{next}, cmd, h \rangle</math> to <math>\mathcal{A}</math> 9: <b>else</b> send <math>\langle P, P_{next}, h \rangle</math> to the network </pre>
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Figure 18: Subroutines of  $\mathcal{F}_{OR}$  for Party  $P$

We therefore have the *visible subpath* computation in the *SendMessage* function in Figure 18. Parties running  $\mathcal{F}_{OR}$  share their *compromised*-status over the shared memory and based on this leak the required information to the adversary.

**Messages through a handle.** Figure 17 considers messages  $m$  that are retrieved *through a handle*. As described above,  $\mathcal{F}_{OR}$  uses shared memory in order to transmit messages through the network. A party  $P$  receives a message through a handle  $h$  if  $P$  found this message after looking up  $h$  in the shared memory.

**Corrupted messages.** While the adversary might corrupt or replay messages in  $\Pi_{OR}$ , these active attacks will be detected by the recipient due to the presence of a secure and authenticated channel between any two communicating parties. The interesting case is when the adversary manages to compromise an onion router in the circuit: the adversary can then propagate corrupted messages, which in  $\Pi_{OR}$  are only detected during *UnwrOn* calls at the OP or the exit node.

This fact is captured in  $\mathcal{F}_{OR}$  by using *corrupted* flags for each message sent through the network. If the adversary wants to modify a message, this flag is set to *true* and propagated until it reaches the last node  $P_{last}$  in the circuit.

The adversary also provides a message transformation function  $T(\cdot)$ , which is applied to the message in the shared memory in order to change it.

### 5.2.2 Our Modifications to $\mathcal{F}_{OR}$

In order to correctly capture the notion of time in our abstraction, we modify some aspects of  $\mathcal{F}_{OR}$  which did not allow for a direct translation into a time-sensitive abstraction.

Similar to the adjusted OR protocol  $\Pi_{OR}$ , we change the time-to-live ( $tll_C$ ) of a circuit to an actual time-interval (e.g., 10 minutes) instead of a bounding the number of messages that can be transmitted through the same circuit.

A major problem we face after introducing time is that  $\Pi_{OR}$  and  $\mathcal{F}_{OR}$  take a different number of steps for executing specific commands (due to the differences in their code). This results in parties in different worlds (real and ideal) advancing in time with different paces. In order to still be able to show secure realization for our abstraction, we therefore need to adjust the pace in which parties running  $\mathcal{F}_{OR}$  advance in time.

We achieve this by introducing a *delay vector*  $\underline{d} = (d_1, \dots, d_{15})$  with which we parameterize  $\mathcal{F}_{OR}$ . Each entry of  $v$  is a delay-distribution, which is inserted at specific points in the code of  $\mathcal{F}_{OR}$  (see Figure 16).  $\mathcal{F}_{OR}$  then draws the number of steps it should delay at these specific points whenever this piece of code is executed.

With this, we make sure that in the abstraction  $\mathcal{F}_{\text{OR}}$  as well as in the protocol  $\Pi_{\text{OR}}$  the parties progress in time at roughly the same rate.

Special care has to be taken whenever we add delay for a function with a run-time which is not constant, e.g. if we add delay for the various encryption and decryptions methods from  $\Pi_{\text{OR}}$ . The delay can then depend on input provided by  $\mathcal{F}_{\text{OR}}$ . We explain this in more detail in Section 5.3.

In Section 3.1.3 we described how the speed coefficients for newly created machines in the network are determined (i.e. by drawing the speed coefficient from a distribution specific to the protocol role of the new machine). We have to account for these variable speeds by suitably varying the delay vector: the initial delay vectors are defined for fixed speed coefficient for ideal ( $c_i$ ) and real ( $c_r$ ) machines. After drawing the coefficient  $c$  for the newly crated machine, all delay vector entries are stretched by the factor  $\frac{c_r}{c}$ , then multiplied by a factor  $b$  which makes all entries integer, and increased by the factor  $(\frac{c_r b}{c} - 1)s_i$ , where  $s_i$  is the number of steps the ideal machine does on the activation until this specific delay vector entry kicks in. The new base speed coefficient for the ideal machine will be  $c_i \cdot b$  and uses the previously computed delay vector.

We also need to adjust the visible subpath computation in the *SendMessage* function: in the original  $\mathcal{F}_{\text{OR}}$  functionality the visible subpath was leaked before the message arrived the observed part of the network. We adjusted *SendMessage()* such that messages are only leaked after the compromised of the network is actually reached.

We stress that  $\mathcal{F}_{\text{OR}}$  basically resembles the protocol except for the cryptographic operations. Instead of ciphertexts and group elements, the  $\mathcal{F}_{\text{OR}}$  merely sends freshly drawn handles over the network. The predecessor of this  $\mathcal{F}_{\text{OR}}$  has been used for analyzing the anonymity guarantees of the OR protocol, since all cryptographic operations are abstract away in a provably secure way [4].

### 5.3 Abstracting Tor in TUC

We show that  $\mathcal{F}_{\text{OR}}$  is indeed an accurate abstraction of the onion routing protocol  $\Pi_{\text{OR}}$ : we show that  $\Pi_{\text{OR}}$  securely realizes  $\mathcal{F}_{\text{OR}}$  in TUC, which was already shown by Backes et al. [3] for the standard UC-framework. This gives us the secure realization for the abstraction.

#### 5.3.1 Assumptions

In order to prove the following theorems, we need to make certain assumptions about the cryptographic primitives used in  $\Pi_{\text{OR}}$ . These assumptions were already presented in [3], but we require them to also hold against an adversary with timing information. We present these assumptions here and use them later in the proofs.

**1W-AKE.** We assume that the key exchange that happens whenever a new circuit is created uses a 1W-AKE-protocol as introduced in [25]. From these we need the property of *key secrecy*: for an adversary, which observes the public parts of the key exchange, the generated key is indistinguishable from a randomly chosen one.

We assume that the encryption and decryption algorithms used in the onion routing protocol  $\Pi_{\text{OR}}$  to be secure, i.e. they satisfy following four properties, as presented in [3]. As we consider a time sensitive network model, we assume that these assumptions also hold against time sensitive adversaries:

**Onion correctness.** The first property of secure onion algorithms is *onion correctness*. It states that honest wrapping and unwrapping results in the same message. Moreover, the correctness states that whenever the unwrapping algorithm has a fake flag, it does not care about integrity, because for  $m \in M(\eta)$  the integrity measure is always added, as required by the end-to-end integrity. But for  $m \notin M(\eta)$  but of the right length, the wrapping is performed without an integrity measure. The fake flag then causes the unwrapping to ignore the missing integrity measure. Then, we also require that the state transition is independent from the message or the key.

**Definition 29** (Onion correctness). *Let  $M(\eta)$  be the message space for the security parameter  $\eta$ . Let  $\langle k_i \rangle_{i=1}^\ell$  be a sequence of randomly chosen bitstrings of length  $\eta$ .*

<pre> <b>(setup, <math>\ell'</math>)</b>   if <math>initiated = false</math> then     for <math>i = 1</math> to <math>\ell'</math> do       <math>k_i \leftarrow \{0, 1\}^\eta</math>; <math>cid_i \leftarrow \{0, 1\}^\eta</math>       <math>initiated \leftarrow true</math>; store <math>\ell'</math>       send <math>cid</math>  <b>(compromise, <math>i</math>)</b>   <math>initiated \leftarrow false</math>; erase the circuit   <math>compromised(i) \leftarrow true</math>; run <b>setup</b>;   for <math>j</math> with <math>compromised(j) = true</math> do     send <math>(cid_j, k_j)</math> for all  <b>(send, <math>m</math>)</b>   <math>O \leftarrow WrOn(m, \langle k_i \rangle_{i=1}^{\ell'})</math>   send <math>O</math> </pre>	<pre> <b>(unwrap, <math>O, cid</math>)</b>   look up the key <math>k</math> for <math>cid</math>   <math>O' \leftarrow UnwrOn(O, k)</math>   send <math>O'</math>  <b>(respond, <math>m</math>)</b>   <math>O \leftarrow WrOn(m, k_{\ell'})</math>   send <math>O</math>  <b>(wrap, <math>O, cid</math>)</b>   look up the key <math>k</math> for <math>cid</math>   <math>O' \leftarrow WrOn(O, k)</math>   send <math>O'</math>  <b>(destruct, <math>O</math>)</b>   <math>m \leftarrow UnwrOn(O, \langle k_i \rangle_{i=1}^{\ell'})</math>   send <math>m</math> </pre>
--	--

Figure 19: The Honest Onion Secrecy Challenger OS-Ch<sup>0</sup>: OS-Ch<sup>0</sup> only answers for honest parties

**Forward:**  $\Omega_f(m)$

```

 $O_1 \leftarrow WrOn(m, \langle k_i \rangle_{i=1}^\ell)$ 
for  $i = 1$  to  $\ell$  do
   $O_{i+1} \leftarrow UnwrOn(O_i, k_i)$ 
 $x \leftarrow O_{\ell+1}$ 

```

**Backward:**  $\Omega_b(m)$

```

 $O_\ell \leftarrow WrOn(m, k_\ell)$ 
for  $i = \ell - 1$  to  $1$  do
   $O_i \leftarrow WrOn(O_{i+1}, k_i)$ 
 $x \leftarrow UnwrOn(O_1, \langle k_i \rangle_{i=1}^\ell)$ 

```

Let  $\Omega'_f$  be the defined as  $\Omega_f$  except that  $UnwrOn$  additionally uses the fake flag. Analogously,  $\Omega'_b$  is defined. We say that a pair of onion algorithms  $(WrOn, UnwrOn)$  is correct if the following three conditions hold:

- (i)  $\Pr[x \leftarrow \Omega_d(m) : x = m] = 1$  for  $d \in \{f, b\}$  and  $m \in M(\eta)$ .
- (ii)  $\Pr[x \leftarrow \Omega'_d(m) : x = m] = 1$  for  $d \in \{f, b\}$  and all  $m \in M'(\eta) := \{m' | \exists m'' \in M(\eta). |m'| = |m''|\}$ .
- (iii) For all  $m \in M'(\eta)$ ,  $k, k' \in \{0, 1\}^\eta$  and  $c, s \in \{0, 1\}^*$  such that  $c$  is a valid onion and  $s$  is a valid state

$$\Pr[(c', s') \leftarrow WrOn(m, k, s), \\ (m', s'') \leftarrow UnwrOn(c, k', s) : s' = s''] = 1$$

- (iv)  $WrOn$  and  $UnwrOn$  are polynomial-time computable and randomized algorithms.

**Synchronicity.** The second property is synchronicity. In order to achieve replay resistance, we have to require that once the wrapping and unwrapping do not have synchronized states anymore, the output of the wrapping and unwrapping algorithms is indistinguishable from randomness. For the following definition we use the modified challenger OS-Ch<sup>0'</sup>, which results from modifying OS-Ch<sup>0</sup> such that along with the output of the adversary also the state of the challenger is output. The resulting challenger OS-Ch<sup>0'</sup> can, moreover, optionally get a state  $s$  as input.

**Definition 30** (End-to-end integrity). *Let  $S(O, cid)$  be the machine that sends a  $(destruct, O)$  query to the challenger and outputs the response. Let  $Q'(s)$  be the set of answers to construct queries from the challenger to the adversary. Let the last onion  $O_{\ell'}$  of an onion  $O_1$  be defined as follows:*

```

Last( $O_1$ ):
  for  $i = 1$  to  $\ell' - 1$  do
     $O_{i+1} \leftarrow UnwrOn(O_i)$ 

```

Let  $Q(s) := \{Last(O_1) \mid O_1 \in Q'(s)\}$  be the set of last onions answers to the challenger. We say a set of onion algorithms has end-to-end integrity if for all PPT adversaries  $\mathcal{A}$  the following is negligible in the security parameter  $\eta$

$$\Pr[(O, s) \leftarrow \mathcal{A}(1^\eta)^{OS-Ch^{0'}}, (m, s') \leftarrow S(O, cid)^{OS-Ch^{0'}(s)} \\ : m \in M(\eta) \wedge P_{\ell'} \text{ is honest} \wedge O \notin Q(s')].$$

<pre> <b>(setup, <math>\ell'</math>)</b> do the same as OS-Ch<sup>0</sup> additionally <math>k_S \leftarrow \{0, 1\}^\eta</math>  <b>(compromise, <math>i</math>)</b> do the same as OS-Ch<sup>0</sup>  <b>(send, <math>m</math>)</b> <math>q(st_f^1) \leftarrow m</math> look up the first visible subpath <math>(cid_1, \langle k_i \rangle_{i=1}^j)</math> <b>if</b> <math>j = \ell'</math> <b>then</b> <math>m' \leftarrow q(st_f^1)</math> <b>else</b> <math>k_{j+1} \leftarrow k_S; j \leftarrow j + 1; m' \leftarrow 0^{ q(st_f^1) }</math> <math>((O_i)_{i=0}^j, s') \leftarrow WrOn^j(m, \langle k_i \rangle_{i=1}^j, st_f^1)</math> update <math>st_f^1 \leftarrow s'</math> store <math>onions(cid_j) \leftarrow O_1</math>; send <math>O_j</math>  <b>(unwrap, <math>O, cid_i</math>)</b> look up the forward v.s. <math>\langle k_i \rangle_{i=u}^j</math> for <math>cid_i</math> <math>O' \leftarrow onions(cid_i)</math> <math>T \leftarrow M(O, O')</math>; <math>q(st_f^i) \leftarrow T(q(st_f^i))</math> <b>if</b> <math>j = \ell'</math> <b>then</b> <math>m \leftarrow q(st_f^i)</math> <b>else</b> <math>k_{j+1} \leftarrow k_S; j \leftarrow j + 1; m \leftarrow 0^{ q(st_f^i) }</math> <math>((O_i)_{i=u-1}^j, s') \leftarrow WrOn^{j-u+1}(m, \langle k_i \rangle_{i=u}^j, st_f^i)</math> update <math>st_f^i \leftarrow s'</math> store <math>onions(cid_j) \leftarrow O_u</math>; send <math>O_j</math> </pre>	<pre> <b>(respond, <math>m</math>)</b> <math>q(st_b^{\ell'}) \leftarrow m</math> look up the last visible subpath <math>\langle k_i \rangle_{i=u}^{\ell'}</math> <b>if</b> <math>u = 1</math> <b>then</b> <math>m \leftarrow q(st_b^{\ell'})</math> <b>else</b> <math>k_{u-1} \leftarrow k_S; u \leftarrow u - 1; m \leftarrow 0^{ q(st_b^{\ell'}) }</math> <math>((O_i)_{i=u-1}^j, s') \leftarrow WrOn^{j-u+1}(m, \langle k_i \rangle_{i=u}^j, st_b^{\ell'})</math> update <math>st_b^{\ell'} \leftarrow s'</math> store <math>onions(cid_u) \leftarrow O_u</math>; send <math>O_j</math>  <b>(wrap, <math>O, cid_i</math>)</b> look up the backward v.s. <math>\langle k_i \rangle_{i=u}^j</math> for <math>cid_i</math> <math>O' \leftarrow onions(cid_i); T \leftarrow M(O, O')</math> <math>q(st_b^i) \leftarrow T(q(st_b^i))</math> get <math>\langle k_i \rangle_{i=u}^j</math> for <math>cid</math> <b>if</b> <math>u = 1</math> <b>then</b> <math>m \leftarrow q(st_b^i)</math> <b>else</b> <math>k_{u-1} \leftarrow k_S; u \leftarrow u - 1; m \leftarrow 0^{ q(st_b^i) }</math> <math>((O_i)_{i=u-1}^j, s') \leftarrow WrOn^{j-u+1}(m, \langle k_i \rangle_{i=u}^j, st_b^i)</math> update <math>st_b^i \leftarrow s'</math> store <math>onions(cid_u) \leftarrow O_u</math>; send <math>O_j</math>  <b>(destruct, <math>O, cid</math>)</b> <math>m \leftarrow UnwrOn(k_1, st_b^1)</math> <math>O' \leftarrow onions(cid_1); T \leftarrow M(O, O')</math> <math>q(st_b^1) \leftarrow T(q(st_b^1))</math> <b>if</b> <math>m \neq \perp</math> <b>then</b> send <math>q(st_b^1)</math> </pre>
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Figure 20: The Faking Onion Secrecy Challenger OS-Ch<sup>1</sup>: OS-Ch<sup>1</sup> only answers for honest parties.  $st_f^i, st_b^i$  is the current forward, respectively backward, state of party  $i$ .  $((O_i)_{i=u-1}^j, s') \leftarrow WrOn^{j-u+1}(m, \langle k_i \rangle_{i=u}^j, st)$  is defined as  $O_{u-1} \leftarrow m$ ; **for**  $i = u$  **to**  $j$  **do**  $(O_i, s') \leftarrow WrOn(O_{i-1}, k_{j+u-i}, st)$

**End-to-end integrity.** The third property that we require is *end-to-end integrity*, i.e., the adversary is not able to produce an onion that successfully unwraps unless it compromises the exit node. For the following definition, we modify OS-Ch<sup>0</sup> such that, along with the output of the adversary, also the state of the challenger is output. In turn, the resulting challenger OS-Ch<sup>0'</sup> can optionally get a state  $s$  as input. In particular,  $(a, s) \leftarrow A^B$  denotes in the following definition the pair of the outputs of  $A$  and  $B$ .

**Definition 31** (Synchronicity). *For a machine  $\mathcal{A}$ , let  $\Omega_{l,\mathcal{A}}$  and  $\Omega_{r,\mathcal{A}}$  be defined as follows:*

<p><b>Left:</b> <math>\Omega_{l,\mathcal{A}}(\eta)</math></p> $(m_1, m_2, st) \leftarrow \mathcal{A}(1^\eta)$ $k, s, s' \leftarrow \{0, 1\}^\eta$ $O \leftarrow WrOn(m_1, k, s)$ $O' \leftarrow UnwrOn(O, k, s')$ $b \leftarrow \mathcal{A}(O', st)$	<p><b>Right:</b> <math>\Omega_{r,\mathcal{A}}(\eta)</math></p> $(m_1, m_2, st) \leftarrow \mathcal{A}(1^\eta)$ $k, s, s' \leftarrow \{0, 1\}^\eta$ $O \leftarrow WrOn(m_2, k, s)$ $O' \leftarrow UnwrOn(O, k, s')$ $b \leftarrow \mathcal{A}(O', st)$
--	---

For all PPT machines  $\mathcal{A}$  the following is negligible in  $\eta$ :

$$|\Pr[b \leftarrow \Omega_{l,\mathcal{A}}(\eta) : b = 1] - \Pr[b \leftarrow \Omega_{r,\mathcal{A}}(\eta) : b = 1]|$$

**Predictably malleable onion secrecy.** The fourth property that we require is *predictably malleable onion secrecy*, i.e. for every modification to a ciphertext the challenger is able to compute the resulting changes for the plaintext. This even has to hold for faked plaintexts. Note that this property is a stateful and weaker variant of what was introduced as Homomorphic-CCA-Security in [51].

In detail, we define a challenger OS-Ch<sup>0</sup> that provides, a wrapping, a unwrapping and a send and a destruct oracle. In other words, the challenger provides the same oracles as in the onion routing protocol except that the challenger only provides one single session. We additionally define a faking challenger OS-Ch<sup>1</sup> that provides the same oracles but fakes all onions for which the adversary does not control the final node.

For OS-Ch<sup>1</sup>, we define the maximal paths that the adversary knows from the circuit. A visible subpath of a circuit  $(P_i, k_i, cid_i)_{i=1}^\ell$  from an honest onion proxy is a minimal subsequence of corrupted parties  $(P_i)_{i=u}^s$  of  $(P_i)_{i=1}^\ell$  such that  $P_{i-1}$  is honest and either  $s = \ell$  or  $P_{s+1}$  is honest as well. The parties  $P_{i-1}$  and, if existent,  $P_{s+1}$  are called the guards of the visible subpath  $(P_i)_{i=u}^s$ . We store visible subpaths by the first  $cid = cid_u$ .

Figure 19 and 20 presents OS-Ch<sup>0</sup>, and OS-Ch<sup>1</sup>, respectively.<sup>10</sup>

**Definition 32** (Predictably malleable onion secrecy). *Let onionAlg be a pair of algorithms WrOn and UnwrOn. We say that the algorithms onionAlg satisfy predictably malleable onion secrecy if there is a negligible function  $\mu$  such that there is a efficiently computable function M such that for all PPT machines  $\mathcal{A}$  and sufficiently large  $\eta$*

$$\Pr[b \leftarrow \{0, 1\}, b' \leftarrow \mathcal{A}(1^\eta)^{\text{OS-Ch}^b} : b = b'] \leq 1/2 + \mu(\eta)$$

**Timed standard assumptions.** The assumptions above also require standard cryptographic assumptions such as CCA, CPA or the Decisional–Diffie–Hellman (DDH) assumption to hold when the adversary has access to timing information about e.g. how long it took to choose the exponents for the Diffie–Hellman key–exchange. We assume that these assumptions also hold in the timed setting.

**Encryption time.** There is another important aspect we need to consider when handling timing information: the running time of encryption and decryption functions depend on the message to be encrypted and the key used to encrypt the message. While the different running times alone are reason enough to include this aspect into the delay-vectors, previous work [37] has shown that this information can leak information about the key and/or the message. Thus we need to accurately capture these small delays in our delay vectors.

We require the following: Let  $f(x, m)$  denote the encryption (decryption) time needed to encrypt (decrypt) message  $m$  with the key  $x$ . Then the encryption (and decryption) times are indistinguishable with regards to the message, i.e. given following two events

$$\begin{aligned} M_1 : b = b^*; (m_0, m_1) \leftarrow \mathcal{A}, x \leftarrow \text{KeyGen}(1^\eta), \\ t \leftarrow f(x, m_b), b^* \leftarrow \mathcal{A}(t) \\ M_2 : b \neq b^*; (m_0, m_1) \leftarrow \mathcal{A}, x \leftarrow \text{KeyGen}(1^\eta), \\ t \leftarrow f(x, m_b), b^* \leftarrow \mathcal{A}(t) \end{aligned}$$

we have that

$$|\Pr[M_1] - \Pr[M_2]| < \text{negl}(\eta)$$

Note that this requirement is automatically fulfilled as soon as we assume the timed variant of the CPA assumption, as we could otherwise directly construct an adversary which breaks timed CPA from an adversary which distinguishes plain-texts from encryption times.

We give the above defined function  $f$  to the functionality in its delay-vector. The party  $P$  running  $\mathcal{F}_{\text{OR}}$  gives  $f$  a key and a message, and  $f$  returns the number of steps  $P$  should idle in order to mimic the correct encryption/decryption time (this in particular also takes into account the number of steps required to compute  $f$ ).<sup>11</sup>

Unfortunately, during the proofs presented below, we get the situation where the simulator uses a different key to do encryptions than were used in computing the delay. We therefore also have to make the assumption that the encryption (decryption) times are also indistinguishable with regards to the key, i.e. given the two events

$$\begin{aligned} K_1 : b = b^*; (x_0, x_1, m) \leftarrow \mathcal{A}, t \leftarrow f(x_b, m), b^* \leftarrow \mathcal{A}(t) \\ K_2 : b \neq b^*; (x_0, x_1, m) \leftarrow \mathcal{A}, t \leftarrow f(x_b, m), b^* \leftarrow \mathcal{A}(t) \end{aligned}$$

we again have that

$$|\Pr[K_1] - \Pr[K_2]| < \text{negl}(\eta).$$

<sup>10</sup>We stress that in Figure 20 the onion  $O_u$  denotes the onion from party  $P_j$  to party  $P_{j+1}$ .

<sup>11</sup>We feel that this assumption is only necessary due to proof we present below. It would be interesting to improve the proof such that this assumption is no longer necessary.

### 5.3.2 Secure Realization

The proof of secure realization that we present here is very close to the proof presented by Backes et al. [3] for the realization of  $\mathcal{F}_{\text{OR}}$  by  $\Pi_{\text{OR}}$  in the standard UC-framework. But we have to make some alteration to take timing properties into account. The main challenge was to avoid time drifting too far apart in the scenario with  $\mathcal{F}_{\text{OR}}$  compared to the scenario in which  $\Pi_{\text{OR}}$  is used.

**Theorem 4.**  $\Pi_{\text{OR}}$  securely realizes  $\mathcal{F}_{\text{OR}}$  in the  $\mathcal{F}_{\text{SCS}}, \mathcal{F}_{\text{REG}}^{\mathcal{N}}$ - hybrid model for some delay vector  $v$ .

*Proof.* We adopt the proof of secure UC-realization from [3]. That is, we define a sequence of games, for which we show that these are indistinguishable.

**Game 1:** This is the initial game in which  $\Pi_{\text{OR}}$  interacts with the adversary  $A_d$  and the environment ENV. Here, being in the  $\mathcal{F}_{\text{SCS}}, \mathcal{F}_{\text{REG}}^{\mathcal{N}}$ - hybrid model means that each party consists of a root node running the  $\Pi_{\text{OR}}$  code and two children nodes, each running the code for  $\mathcal{F}_{\text{SCS}}$  and  $\mathcal{F}_{\text{REG}}^{\mathcal{N}}$  respectively.

**Game 2:** In this game we replace the dummy adversary with a simulator  $\mathcal{S}_1$ .  $\mathcal{S}_1$  consists of a root node, which is the main simulator simulating the dummy adversary, and two children nodes, each of which simulate the functionalities  $\mathcal{F}_{\text{SCS}}$  and  $\mathcal{F}_{\text{REG}}^{\mathcal{N}}$ . That is, we move all the children nodes from compromised parties in the network to the simulator and simulate them inside  $\mathcal{S}_1$  (this also includes rewiring of all relevant ports, e.g. from the root node of a party to the  $\mathcal{F}_{\text{SCS}}$ - children node). Remember that  $\mathcal{S}_1$  is timeless, while the simulated nodes are all time-ful. Thus we need to be careful with our simulation, making sure that messages going out of the functionalities, back to the parties in the network or the environment, are forwarded at the right time. In order to achieve this,  $\mathcal{S}_1$  uses internal queues for all out-ports, marking each outgoing message with a time-stamp. These messages will then be sent out as soon as the timer of  $\mathcal{S}_1$  has the correct value. Note that it is enough to internally simulate the  $\mathcal{F}_{\text{SCS}}$  and  $\mathcal{F}_{\text{REG}}^{\mathcal{N}}$  nodes of compromised nodes, as ENV does not learn about uncompromised nodes and their behavior. Hence, **Game 2** and **Game 1** are indistinguishable.

**Game 3:** In this game, session keys are no longer generated by a key exchange protocol, but are just chosen randomly and saved in a common shared memory. In order to make sure that the timing remains correct, we first double the speed coefficient of each party in order to accommodate the additional computation (randomly choosing the key and saving it in the shared memory) and introduce idle commands in the code of  $\Pi_{\text{OR}}$ , making sure that all messages are still sent out at the same times as in **Game 2**. Due to the security of the 1W-AKE, no ppt machine can distinguish the randomly chosen key from the key generated by 1W-AKE, hence this game is indistinguishable from **Game 2**.

**Game 4:** In  $\text{Game}_4$ , we adopt the visible subpath computations from [3]. The onions do not contain the real messages anymore but only the constant zero bitstring.  $\Pi_4$  maintains a shared datastructure  $q$  in which the real messages are stored.

Recall that a visible subpath of a circuit  $(P_i, k_i, cid_i)_{i=1}^{\ell}$  from an honest onion proxy is a minimal subsequence of corrupted parties  $(P_i)_{i=u}^s$  of  $(P_i)_{i=1}^{\ell}$  such that  $P_{i-1}$  is honest and either  $s = \ell$  or  $P_{s+1}$  is honest as well. The parties  $P_{i-1}$  and, if existent,  $P_{s+1}$  are called the guards of the visible subpath  $(P_i)_{i=u}^s$ . In particular, the onion proxy is also a guard. Every circuit can be split into a sequence of visible subpaths and guards.  $\Pi_4$  stores for every circuit  $(P_i, k_i, cid_i)_{i=1}^{\ell}$  such a splitting into visible subpaths and guards. These splittings are updated upon each compromise command.

Upon receiving a send input or a response from a network,  $\Pi_4$  stores an input message  $m$  in a shared datastructure  $q$  as follows. For a guards  $P$ , let  $cid_P$  be the circuit id for which  $P$  knows the key. Let  $s$  the state of the wrapping algorithms of the sender before computing the onion. Then, we store  $q(cid_P, s) \leftarrow m$  for each  $P$ .

The adversary might be able to corrupt onions such that the contained plaintext is changed.  $\Pi_4$ , however, does not rely on the content of the onions anymore but rather looks up the message in the shared memory. Therefore,  $\Pi_4$  needs a way to derive the changes to the plaintext due to possible modifications of the ciphertexts. At this point our predictable malleability applies, and we use the algorithm  $D$  from the onion secrecy definition for computing the changes in the plaintext. However, for computing the changes in the plaintext, we need to store the onions that the receiving guard has to expect. Hence,  $\Pi_4$  maintains a shared datastructure *onions* indexed by the *cid* of the receiving guard that stores the expected onions.



$\Pi_4$  initially draws some distinguished random key  $k_S$ , which is later used for a distinguished last wrapping-layer of the constant zero bitstring. Whenever in  $\Pi_3$  a guard  $P$  that is neither the exit node nor the onion proxy would unwrap an onion  $O$  with key  $k$  and circuit id  $cid$ ,  $P$  looks up  $O' = \text{pending}(cid)$ . Then, it runs  $T \leftarrow S(O, O')$  and replaces the real message  $m \leftarrow q(cid, st)$  in the shared memory with  $T(m)$ , where  $st$  is the state of the onion algorithms in the forward direction. Then,  $P$  unwraps  $O$  with the fake flag, i.e.,  $(O'', st') \leftarrow \text{UnwrOn}(O, k_S, \text{fake}, st)$  instead of  $\text{UnwrOn}(O, k, st)$ . We set the fake flag, because the unwrapping has to skip the integrity check; otherwise a corrupted onion would already in the middle of the circuit be stopped in  $\Pi_4$ . However, instead of forwarding  $O''$ ,  $P$  constructs a new onion either for the adversary or for the next guard as follows.  $P$  looks up the adjacent visible subpath  $(P_i)_{i=u}^s$  in forward direction. If  $s = \ell$ , then  $P$  constructs the onion for the adversary.  $P$  reads the real message  $m \leftarrow q(cid, st)$  from the shared memory and sends a forward onion  $O_j$  for the subcircuit  $(P_i, k_i, cid_i)_{i=u}^\ell$  that contains the message  $m$  and is constructed as follows:

$$O_{u-1} \leftarrow m$$

**for**  $i = u$  **to**  $\ell$  **do**  $(O_i, st') \leftarrow \text{WrOn}(O_{i-1}, k_{j+u-i}, st)$

Only then,  $P$  updates the forward state  $st \leftarrow st'$ . Thereafter,  $P$  stores  $q(cid_{P_{j+1}}, st') \leftarrow O_u$ , where  $cid_{P_{j+1}}$  is the circuit id of the guard  $P_{j+1}$ .

If  $s < \ell$ ,  $P$  sends a forward onion for the subcircuit  $(P_i, k_i, cid_i)_{i=u}^{s+1}$  that contains  $0^{|m|}$  instead of  $m$ , where we replace for the last layer  $k_{s+1}$  by the distinguished key  $k_S$ . Again only then,  $P$  updates the forward state  $st \leftarrow st'$ . Analogously, guards that are onion proxies, i.e., construct an onion in forward direction, also only construct an onion for the adversary or the next guard.

Similar to the forward direction, guards that receive an onion  $O$  in backward direction do not wrap it further as in  $\Pi_3$  but first unwrap  $O$  with the fake flag and the distinguished key  $k_S$ , i.e.,  $O' \leftarrow \text{UnwrOn}(O, k_S, \text{fake})$ . Instead of wrapping  $O$  as in  $\Pi_3$ , the guard constructs an onion for the adjacent subpath in backward direction as follows. Since  $P$  is a guard for the circuit, also the onion proxy is honest, thus  $u > 1$ .  $P$  looks up the adjacent visible subpath  $(P_i)_{i=u}^s$  in backward direction. Let  $m \leftarrow q(cid, st)$  be the real message stored in the shared memory,  $cid$  be the circuit id for which  $P$  knows the key and  $s$  be the state of the onion algorithms in the backward direction. Then,  $P$  sends an onion  $(O, st') \leftarrow \text{UnwrOn}(0^{|m|}, \langle k_i \rangle_{i=u-1}^s, st)$ , where  $k_{u-1} := k_S$ . Thereafter, update the backward state  $st \leftarrow st'$ .

It might happen that the adversary compromised a node in the middle of the circuit and the exit node. Then the adversary sends a random message to an honest node  $P$ . In this case,  $P$  would honestly unwrap the message. Since the adversary controls the exit node the broken integrity is not realized. But from that point on the guard  $P$  is out of sync, i.e.,  $P$  has a different unwrapping state than the predecessor guards. Consequently, by the synchronicity of the onion algorithms all future messages that are sent from the onion proxy will be garbage. For guards that are out of sync, we only send randomly chosen messages of appropriate length.

Then, by a hybrid argument it follows that any adversary distinguishing **Game**<sub>3</sub> from **Game**<sub>4</sub> can be used for breaking onion secrecy or synchronicity, where the hybrids are indexed by the circuits of honest onion proxies in the order in which the circuits are initiated. Hence, **Game**<sub>3</sub> and **Game**<sub>4</sub> are indistinguishable.

While this visible subpath computation only changes the messages, but not the amount of messages sent through the network, our main concern is the additional computation done by each party. In order to accommodate this, we again accelerate the party machines, introducing the new code for the visible-subpath computations and additional idle commands, making sure that messages are sent out at the same time as in **Game** 3. We make use of shared memory in order to enable parties to compute the visible sub paths: compromised parties indicate in the shared memory that they are compromised, and parties doing the visible subpath computation get all necessary information from the shared memory. This work around is necessary as in the original model [3], there is only a single protocol machine  $P$  which internally simulates all participating parties, and does the visible subpath computations. This approach is not feasible in our model, as this would require  $P$  to live in several points in time simultaneously.

Due to onion secrecy and synchronicity of the used onion encryption algorithms, and as we make sure that the time stamps remain the same, no ppt adversary can distinguish between **Game** 3 and **Game** 4.

**Game 5:** In this games, each party internally simulates  $\mathcal{F}_{\text{OR}}$  for doing the visible-subpath computations. That is, every input from the environment is directly forwarded to  $\mathcal{F}_{\text{OR}}$ , which in turn returns the computed visible subpaths and messages to be sent through the network.

A major difference to  $\Pi_{\text{OR}}$  here is in the key registration. Upon input (**Register**,  $P$ ),  $\mathcal{S}_1$  internally simulates the interaction with the key registration functionality and makes sure that all required network messages are sent to ENV.

Other small differences are discussed in [3] and will be skipped here. Our main concern will be making sure that the time stamps of each message remain correct, and also that time variables of each party progress at the same rate as in **Game 4**. While compared to **Game 4**, we save code for the parties (from outsourcing the visible subpath computation), we still have to accommodate the simulation of  $\mathcal{F}_{\text{OR}}$  in our time budget. Again we accelerate our parties and introduce idle commands as required in order to make sure that messages are sent into the network at the right time.

As we make sure that the timestamps of all messages remain the same, the indistinguishability of **Game 4** and **Game 5** follows from the anonymity of the  $1W - AKE$  protocol as discussed in [3].

**Game 6:** Here, we replace the protocol code by the functionality  $\mathcal{F}_{\text{OR}}$ . In **Game 5**,  $\Pi_{\text{OR}}$  directly forwarded all inputs from ENV to  $\mathcal{F}_{\text{OR}}$ , hence the messages sent by  $\mathcal{F}_{\text{OR}}$  remain the same. As these are sent to the adversary,  $\mathcal{S}_1$  will receive them and can then compute the correct network messages by internally simulating  $\Pi_{\text{OR}}$ . This will now be our final simulator  $\mathcal{S}$ .

At this points,  $\mathcal{F}_{\text{OR}}$  will require much less time than  $\Pi_{\text{OR}}$  in **Game 5**. In order to close this gap, we correctly set all the delay values in  $\mathcal{F}_{\text{OR}}$ 's delay vector, by adding up all the idle commands added in the previous games, taking account of the accelerations and including the encoding-time-distribution functions whenever encryption would happen in the real world scenario.

As the timestamps therefore remain the same as in **Game 6**, and as  $\mathcal{S}$  correctly computes all network messages as in **Game 5**, **Game 5** and **Game 6** are indistinguishable.

□

## 5.4 A User Interface: the Wrapper $\Pi_{\text{wor}}$

We allow for the sake of modularity to let the environment, i.e., the parent node, to command the circuit and to choose the path. In many cases, however, this additional complexity becomes inconvenient. In this section, we present a wrapper  $\Pi_{\text{wor}}$  (see Figure 21) that performs the circuit construction, and splits messages and re-assembles the messages blocks. We present a wrapper that uses a uniform path selection; however, by adjusting the distribution `RandomParties`, any path selection can be used.

We note that such a  $\mathcal{F}_{\text{OR}}$  together with such a wrapper  $\Pi_{\text{wor}}$  (i.e.,  $\Pi_{\text{wor}\mathcal{F}_{\text{OR}}}$ ) give rise to an anonymous channel functionality, that is similar in spirit to the anonymous channel functionality suggested by Canetti [11]. However, such an anonymous channel functionality would have as much leakage as and give the adversary as much influence capabilities as  $\mathcal{F}_{\text{OR}}$ ; thus, we refrained from presenting it in this work.

**Re-assembling and splitting in  $\Pi_{\text{wor}}$ .** We assume a stateful routine `Reassemble( $m, s$ )` which expects as input a message block  $m$  (and a state  $s$ ) and outputs together with a new state  $s'$  either a dummy message `ready`, if a complete message could not be reassembled yet, or a re-assembled message  $m' \neq \text{ready}$ , if  $m$  and the state  $s$  allowed re-assembling a complete message. In the description of the protocol, we lookup the state of `Reassemble` and store it in the variable  $s$ . If this lookup fails, we assign to the variable  $s$  the empty state, i.e., the empty string. Dual to the re-assembling routing, we assume a splitting routine  $(m_1, \dots, m_{\lceil |m|/blockln \rceil}) \leftarrow \text{Split}(m)$  which splits the message into message blocks  $m_i$  of length `blockln` and pads the last block if necessary.

## 6 Timing Attacks in TUC

While previous frameworks only allowed modeling time-independent traffic features such as packets-counts or direction-changes, the communication model in TUC allows us to capture common timing features of traffic such as inter-packet delay, throughput and round-trip-times. Subsequently, we briefly discuss how these features are represented in TUC and how they can be exploited by the network adversary. Our analysis is inspired by the adaptive extension of the AnoA framework [5]. Due to space constraints, we use a simplified version of the anonymity notions presented therein.

<p><b>upon setup from parent</b></p> <ol style="list-style-type: none"> <li>1: send <b>setup</b> to <math>\rho</math>; wait for <math>(\text{ready}, \langle P_j \rangle_{j=1}^n)</math></li> <li>2: store <math>\langle P_j \rangle_{j=1}^n</math>; output <b>ready</b> to parent</li> </ol> <p><b>upon a message</b> (received, <math>(m, cid)</math>) <b>from</b> <math>\rho</math></p> <ol style="list-style-type: none"> <li>1: lookup the state <math>s</math> of Reassemble for <math>\text{sid}'</math></li> <li>2: call <math>(m', s') \leftarrow \text{Reassemble}(m, s)</math>; store <math>s'</math> for <math>\text{sid}'</math></li> <li>3: <b>if</b> <math>\text{ready} \neq m'</math> <b>then</b></li> <li>4:   send (received, <math>m'</math>) to parent</li> </ol> <p><b>upon a message</b> (exit, <math>(m, \text{sid})</math>) <b>from</b> <math>\rho</math></p> <ol style="list-style-type: none"> <li>1: lookup the state <math>s</math> of Reassemble for <math>\text{sid}'</math></li> <li>2: call <math>(m', s') \leftarrow \text{Reassemble}(m, s)</math>; store <math>s'</math> for <math>\text{sid}'</math></li> <li>3: <b>if</b> <math>\text{ready} \neq m' = (m'', \text{sid}'', a)</math> and <math>a</math> is a server <b>then</b></li> <li>4:   send (exit, <math>(m'', \text{sid}'', a), \text{sid}</math>) to parent</li> </ol> <p><b>upon input</b> (response, <math>(\text{sid}, m)</math>) <b>from</b> parent</p> <ol style="list-style-type: none"> <li>1: <math>m_1, \dots, m_q := \text{Split}(m)</math> (for <math>q = \lceil  m /\text{blockln} \rceil</math>)</li> <li>2: <b>for all</b> <math>i \in \{1, \dots, q\}</math> <b>do</b></li> <li>3:   send message (response, <math>\text{sid}, m_i</math>) to <math>\rho</math></li> </ol>	<p><b>upon input</b> (send, <math>m</math>) <b>from</b> parent</p> <ol style="list-style-type: none"> <li>1: <math>\mathcal{P} \leftarrow \text{PathSelection}(P_u)</math></li> <li>2: <b>if</b> there is no open circuit <math>cid</math> <b>then</b></li> <li>3:   send message (createcircuit, <math>\mathcal{P}</math>) to <math>P_u</math></li> <li>4:   wait for response (created, <math>\mathcal{C}</math>)</li> <li>5: <math>m_1, \dots, m_q := \text{Split}(m)</math> (for <math>q = \lceil  m /\text{blockln} \rceil</math>)</li> <li>6: <b>for all</b> <math>i \in \{1, \dots, q\}</math> <b>do</b></li> <li>7:   send message (send, <math>\mathcal{C}, m_i</math>) to <math>\rho</math></li> </ol> <p><b>upon a message</b> (destroyed, <math>cid, m</math>) <b>from</b> <math>\rho</math></p> <ol style="list-style-type: none"> <li>1: mark <math>cid</math> as closed</li> <li>2: proceed as in (send, <math>m</math>)</li> </ol> <p>PathSelection(<math>P_u</math>):</p> <ol style="list-style-type: none"> <li>1: <math>l \xleftarrow{R} \{1, \dots, n\}</math></li> <li>2: <math>N := \{1, \dots, n\}</math></li> <li>3: <b>for</b> <math>j = 1</math> to <math>l</math> <b>do</b></li> <li>4:   <math>i_j \xleftarrow{R} N</math></li> <li>5:   <math>N := N \setminus \{i_j\}</math></li> <li>6: <b>return</b> <math>(P_u, P_{i_1}, \dots, P_{i_l})</math></li> </ol>
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Figure 21: Wrapper  $\Pi_{\text{WOR}\rho}$  for client  $P_u$  and a sub-protocol  $\rho$

## 6.1 The Set-Up

We consider the class of environments that consist of two sub-machines, an environment adversary  $\mathcal{A}_{\text{ENV}}$  and the challenger Ch. The environment adversary  $\mathcal{A}_{\text{ENV}}$  is connected to the network adversary and the challenger is connected to the users and defines the security game. In the following, we will use the term adversary to denote the collaboration of the environment adversary and the network adversary. By the completeness of the dummy adversary (Lemma 3) it suffices to consider the network dummy adversary and the environment adversary.

Each party consists of a the  $\Pi_{\text{WOR}}$  protocol with the onion routing protocol as a child. By Theorem 4 , we directly consider the ideal functionality  $\mathcal{F}_{\text{OR}}$  instead of the onion routing protocol  $\Pi_{\text{OR}}$  ( $\mathcal{F}_{\text{OR}}$  is depicted in Figure 16).

This set-up is exemplified in Figure 22. It depicts three instances of  $\mathcal{F}_{\text{OR}}$  together with the wrapper  $\Pi_{\text{WOR}}$ , the shared memory MEM used by  $\mathcal{F}_{\text{OR}}$ , a challenger Ch, the network adversary and the environment adversary  $\mathcal{A}_{\text{ENV}}$ .

**Sender anonymity challenger SACH.** For illustrating attacks, we present a guessing-based sender anonymity game via a sender anonymity challenger SACH (which instantiates Ch in Figure 22). In this sender anonymity game the environment adversary  $\mathcal{A}_{\text{ENV}}$  has to determine the sender of a specific message-stream in the presence of noise, i.e., other message-streams.  $\mathcal{A}_{\text{ENV}}$  has to link at least one session to the correct sender address. Recall that  $\mathcal{A}_{\text{ENV}}$  can observe all compromised network links  $\mathcal{L}$  though the dummy adversary.

We model a scenario in which users are randomly assigned to addresses, and (in the case of the sender anonymity game) the adversary has to guess which user sits at which address. An address in our model is represented by a party, and a user is represented by a *user model*. For each server  $S \in \mathcal{S} := \{S_1, \dots, S_l\}$ , there is a user model  $\text{UM}_S$  that reactively creates messages for a (potentially interactive) communication with a server  $S$ . The user model in particular also decides, when a specific message is sent.

Technically, a user model  $\text{UM}_S$  is a randomized PPT machine that upon a server message  $r$  outputs a sequence  $((\text{msg}_1, t_1), \dots, (\text{msg}_a, t_a))$ , consisting of a client message  $\text{msg}_i$  and the time  $t_i$  at which  $\text{msg}_i$  shall be sent. Initially, it expects a distinguished message **fresh** to start a new session.

The sender anonymity challenger SACH allows the adversary to initially register a user model for every server. Then, SACH randomly assigns parties  $P_1, \dots, P_l$  (i.e., addresses) to servers  $S_1, \dots, S_l$  (i.e., to user models) and internally runs the user models  $\text{UM}_S$ . SACH forwards each message  $\text{msg}_i$  from  $\text{UM}_S$  as a **send**-command from  $P$  to  $S$  at time  $t_i$  and forwards any response  $r$  from  $S$  to  $P$  to the user model  $\text{UM}_S$  as input.

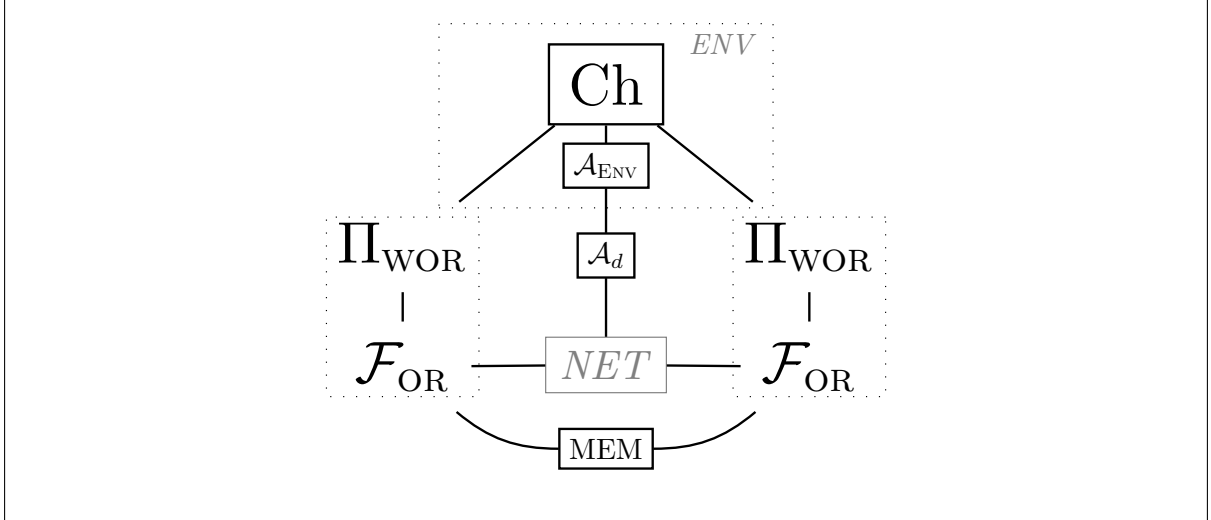


Figure 22: The Attack Scenario

Moreover, for the analysis of sender anonymity, we can assume that all servers are compromised. Hence, SACH forwards all messages from the servers to the environment adversary  $\mathcal{A}_{\text{ENV}}$ . Finally, upon an input  $(\text{guess}, (P', S'))$ , SACH checks whether the user  $u$  is assigned to the user model  $\text{UM}_S$  for the server  $S$ . If so, SACH outputs 1; otherwise it outputs 0.

**Onion routers  $O_i$ .** In addition to the regular users, i.e., protocol parties that are controlled through user models, we also assume protocol parties  $O_1, \dots, O_v$  that run the same protocol code but only serve as onion routers. We assume that users and onion routers are distinct, i.e.,  $\forall i, j. O_i \neq P_j$ .

## 6.2 Mounting Attacks that use Timing Features

In TUC timing-based traffic features can be measured by the adversary. Example 4 details how timing features can be measured in TUC. Subsequently, we discuss how timing based traffic analysis attacks from the literature can be mounted using these features [14, 20, 44, 32, 24, 31, 42, 45, 47, 48, 56, 33].

**Example 4: Observing timing features in TUC.** We examine how the time at which a message is sent from a party  $P$  correlates to  $P$ 's speed coefficient  $c_P$ . For simplicity, we assume that party  $P$  just created a new circuit before receiving the message from SACH. We further assume that both machines inside  $P$  have the same speed coefficient  $c_P$ .

- 1: SACH sends  $(\text{send}, m)$  to party  $P$  at time  $T$
- 2:  $\Pi_{\text{WOR}}$  in  $P$  receives  $(\text{send}, m)$  at  $T' = \frac{k}{c_P} \geq T$ , where  $T'$  is the first time after  $T$  where  $\Pi_{\text{WOR}}$  is in the listen state
- 3: Let  $n$  be the number of steps needed to split  $m$  into  $q := \lceil |m| / \text{block}n \rceil$  packets  $m_1, \dots, m_q$ .  
 $\Pi_{\text{WOR}}$  sends  $m_i$  at time  $T_i = T' + \frac{n+i}{c_P}$  to  $\mathcal{F}_{\text{OR}}$
- 4: Let  $n'$  be the time that  $\mathcal{F}_{\text{OR}}$  needs to send a message and  $d_4$  the delay for being synchronized with  $\Pi_{\text{OR}}$ .  
 $\mathcal{F}_{\text{OR}}$  forwards  $m_i$  at time  $T' + d_4 + \frac{n+1+i \cdot n'}{c_P} =: T' + g(c_P, i)$ , since messages  $m_2, \dots, m_q$  arrive at  $\mathcal{F}_{\text{OR}}$  before  $n'/c_P$  time has passed.

The *traffic pattern* a user model  $\text{UM}_S$  generates is the stream of message blocks that are generated if, for the sequence of messages  $((\text{msg}_1, t_1), \dots, (\text{msg}_a, t_a))$ , the message  $\text{msg}_i$  is sent at the instructed time  $t_i$ . Thus, the corresponding traffic pattern is preserved when  $\Pi_{\text{WOR}, \mathcal{F}_{\text{OR}}}$  sends its message blocks.

If the network links through which the messages, or in the case of  $\mathcal{F}_{\text{OR}}$  the message handles, are sent are compromised, the network adversary  $\mathcal{A}$  learns the time at which the messages cross the network. From this  $\mathcal{A}$  can determine different timing features of the traffic, e.g. he can determine the difference  $g(c_P, i+1) - g(c_P, i)$  between the two messages  $m_i$  and  $m_{i+1}$ , or  $\mathcal{A}$  can learn at which time how much throughput, i.e., how many message blocks per time, passed through the link. Thus, it learns the traffic patterns of the message stream generated by the user model.

Similarly to the function  $g$ , which estimates the delay created by creating a stream of cells from a message  $m_i$ , we can also determine a function  $h$  for the delay created by relaying a message through a onion router. This function  $h$  solely depends on the speed of the onion router and  $i$ .

Consider a circuit with the onion routers  $O_1, O_2, O_3$ . The time at which a message  $m_i$  is sent from an exit link  $O$  over the network to the server is  $g(c_P, i) + h(c_{O_1}, i) + h(c_{O_2}, i) + h(c_{O_3}, i) + d + w$  for some network delay  $d$  that is caused by other messages that the onion routers have to concurrently process and, optionally, for some watermarking delay  $w$  that the adversary deliberately introduces for recognizing traffic (as studied in previous work [20, 44, 32, 33]).

Let  $|\mathcal{L}|$  be the number of compromised links and  $M$  be the number of total links between the protocol parties. Then, the probability that the entry link from  $P$  to the entry node, i.e., to the first onion router, is compromised is  $|\mathcal{L}|/M$ . This is therefore the probability with which  $\mathcal{A}$  observes the links that belong to the same connection and can then try to correlate the traffic. The success of this correlation however depends on the methods used by  $\mathcal{A}$ .

For a passive network adversary, which does not introduce any watermarking delay, we get the following: if the traffic patterns of all user models are sufficiently well distinguishable and the network delay is sufficiently small (i.e., there is not much traffic on the onion routing network), then the traffic pattern of a user model is recognizable, i.e., the traffic can be correlated, for an adversary in TUC. For an active adversary, which does introduce watermarking delays, it is possible to compensate the network delay, and such an adversary can even recognize a stream if the user models produce exactly the same traffic patterns. Thus, an active adversary can recognize a user model as soon as it controls the entry, i.e., with more than  $|\mathcal{L}|/M$  probability.<sup>12</sup>  $\diamond$

**Inter-packet delay.** Inter-packet delay is the time difference in the time stamps of two consecutive packets sent through a connection. Example 4 illustrates how the time distance  $t_{i+1} - t_i$  between two messages  $((m_i, t_i), (m_{i+1}, t_{i+1}))$  from the user model is preserved in the sequence of message blocks that  $\Pi_{\text{WOR}, \mathcal{F}_{\text{OR}}}$  produces. Moreover, it illustrates how a delay  $g(c_P, i+1) - g(c_P, i)$  between two message blocks that belong to the same message is produced and that this delay depends on the speed coefficient of the party. These delays reflect the inter-packet delays used in traffic analysis attacks from the literature. We are aware that we abstract from the delays that, in the real world, are produced by low-level network protocols, such as TCP and IP and from machine specific hardware delays, e.g., induced by a machine’s network card. In principle, however, TUC and  $\mathcal{F}_{\text{OR}}$  allow for fine-grained modeling of these timing features by introducing the respective protocols as sub-protocols of the onion routing protocol.

As discussed in Example 4, if the network delay is small and the traffic patterns of the user models are distinguishable, the inter-packet delays in the traffic pattern of a user model can be correlated even for a passive adversary.

**Traffic watermarking attack.** In a traffic watermarking attack, the adversary deliberately causes a delay pattern, called a watermark, for a packet stream, e.g., at the entry link, and measures at the other links, e.g., at exit links, whether it recognizes such a watermarked message stream. We illustrated in Example 4 how such a traffic watermarking attack could be modeled in TUC.

Similar to recognizing watermarks and inter-packet delays, the adversary can measure other timing features such as throughput and round-trip-times in the network. For round-trip times, the speed coefficients of the onion routers potentially give a unique fingerprint if they are sufficiently different. For simplicity, we omit network latency on links. Network latency can however be modeled by refining the network topology NET in the execution EXEC.

The attack described above only acts passively as it collects messages sent through the network and evaluates them. But, especially for traffic correlation, active methods such as *traffic watermarking* are very popular in the literature [20, 44, 32, 33]. Here the adversary slightly modifies the traffic he intercepts, creating watermarks that are easier to spot if the same traffic is observed at some other point in the network. These kinds of attacks are also possible in our model, as we do allow active adversaries (we are only restricted to static corruption).

**Modeling website-fingerprinting attacks.** A website fingerprinting attack assumes that the adversary up-front possesses a list of fingerprints for each server, which characterizes connections to these

<sup>12</sup>For simplicity, we give a very coarse approximation of the probability that the adversary control the entry link. The adversary could additionally compromise onion routers and thereby increase its chance to control the entry point of a circuit.

<p><b>Both: upon (setup) from the parent</b></p> <p>1: send <code>setup</code> to <math>\rho</math>; wait for <code>ready</code>; send <code>ready</code> to parent</p> <p><b>Client: upon (req, address) from the parent</b></p> <p>1: send (<code>send</code>, <code>address</code>) to <math>\rho</math></p> <p><b>Client: upon <math>m</math> from <math>\rho</math></b></p> <p>1: output <math>m</math> to the parent</p>	<p><b>Exit node: upon (exit, <math>((m'', \text{sid}'', a), \text{sid}')</math>) from <math>\rho</math></b></p> <p>1: <b>if</b> <math>\text{bun}_{\text{sid}''} = \perp \wedge a</math> is a server <b>then</b></p> <p>2: lookup and store current time in <math>\text{reqStart}(\text{sid}')</math></p> <p>3: store <math>((m'', \text{sid}'', a), \text{sid}')</math> in <math>\text{bun}_{\text{sid}''}</math></p> <p>4: <b>while</b> <math>\exists \text{request}(\text{loc}, a)</math> in <math>\text{bun}_{\text{sid}''}</math> <b>do</b></p> <p>5:     remove <math>\text{request}(\text{loc}, a)</math> from <math>\text{bun}_{\text{sid}''}</math></p> <p>6:     send <math>(\text{loc}, (a, \text{sid}'))</math> over the network</p> <p>7:     store the response <math>\text{res}</math> in <math>\text{bun}_{\text{sid}''}</math></p> <p>8: lookup the smallest bucket size <math>n' \geq  \text{bun}_{\text{sid}''} </math> in <code>pageBuck</code></p> <p>9: pad <math>\text{bun}_{\text{sid}''}</math> to a size of <math>n'</math> and store it in <math>m'</math></p> <p>10: let <math>t \leftarrow t_{\text{buf}} + \text{reqStart}(\text{sid}')</math></p> <p>11: send (<code>response</code>, <math>(\text{sid}', m')</math>) to <math>\rho</math> at time <math>t</math></p>
--	---

Figure 23: The protocol  $\text{WFC}_\rho$  for party  $\text{pid}$ , where  $\text{sid}$  is its session ID

servers based on traffic features, e.g., direction changes in traffic, throughput, round-trip-times and inter-packet-delays.  $\mathcal{A}$  then only needs to listen to the entry links from users to the onion routing network and collect the messages (together with time-stamps) that go through this entry link. After collecting sufficiently many messages, he can then match the fingerprints he possesses to the traffic he intercepted. In the literature [9, 49, 28] several successful website-fingerprinting attacks are known.

While previous frameworks already allowed fingerprinting websites based on time-insensitive traffic features, such as overall size of traffic and direction changes in traffic, an adversary in TUC can utilize timing features of traffic for website-fingerprinting as well. In turn, proving the absence of attacks in TUC excludes the entire family of attacks that use time-sensitive traffic features, such as throughput in time and inter-packet delay. In the next section we propose a countermeasure against the class of website fingerprinting attacks against the onion routing protocol and prove this countermeasure to be secure.

## 7 Analyzing a Countermeasure against Website Fingerprinting

As a case study, we leverage our time-sensitive framework and our Tor abstraction to analyze a simple countermeasure against website fingerprinting and to prove it secure. The countermeasure achieves  $k$ -recipient anonymity for web pages without dynamic requests, such as Ajax.

The countermeasure protocol, called  $\text{WFC}$ , is plugged on top of the Tor protocol. At exit nodes,  $\text{WFC}$  performs all web page requests until the web page is fully loaded and returns the entire web page at once to the user. In order to remove size features of web pages, the response packet-stream are padded to a common denominator for all web pages.  $\text{WFC}$  additionally waits until a time buffer  $t_{\text{buf}}$  has passed in order to remove traffic-related timing features.

In order to improve performance, the countermeasure uses buckets for common web page sizes and pads the web pages up to the next larger bucket (instead of padding to a common size of all web pages). The data structure `pageBuck` contains a bucket for each target web page size and upon input of a size  $n$ , `pageBuck( $n$ )` returns the list of web pages that have size  $n$  or are padded to size  $n$ . The countermeasure protocol  $\text{WFC}$  is depicted in Figure 23.

We next describe the  $k$ -recipient anonymity challenger.

**The  $k$ -recipient anonymity challenger  $\text{RACH}$**  We consider following notion of recipient anonymity: an adversary that control all entry links of a party  $P$ , e.g., an ISP-level adversary, should not be able to determine the web pages that the party  $P$  visits. The set-up for recipient anonymity is exactly as for the sender anonymity game (See Section 6.1) except that the challenger  $\text{Ch}$  is replaced by the following  $k$ -recipient anonymity challenger  $\text{RACH}$ . This challenger  $\text{RACH}$  initially allows the environment adversary to define the page-buckets used in the game, but requires that each bucket in `pageBuck` contains exactly  $k$  web pages.

In contrast to the sender anonymity game,  $\text{RACH}$  does not allow the environment adversary to control the servers. Instead, we assume that the adversary solely controls all links connected to the parties representing users (i.e., controls entry links to the onion routing network).

<pre> <b>upon</b> (register-webpages, pageBuck) <b>from</b> <math>\mathcal{A}_{\text{ENV}}</math> 1: <b>for</b> all sizes <math>n</math> <b>do</b> 2:   <b>if</b> <math> \text{pageBuck}(n)  = k</math> <b>then</b> 3:     <b>for</b> all <math>(a, loc, pg) \in \text{pageBuck}(n)</math> <b>do</b> 4:       <b>if</b> <math>(a, loc)</math> is unregistered and <math>loc \neq</math>          challenge <b>then</b> 5:         send (register, <math>loc, pg</math>) to <math>\Pi_{\text{server}}</math> at            party <math>a</math> 6:       <b>else</b> 7:         return error <b>upon</b> (challenge, <math>n</math>) <b>from</b> <math>\mathcal{A}_{\text{ENV}}</math> </pre>	<pre> 1: draw a random <math>j \in \{1, \dots, k\}</math> 2: let <math>(a_j, loc_j, pg_j) \leftarrow \text{pageBuck}(n)</math> 3: <math>(a_{ch}, loc_{ch}) \leftarrow (a_j, loc_j)</math> <b>upon</b> (send, <math>(a, loc)</math>) <b>from</b> <math>\mathcal{A}_{\text{ENV}}</math> 1: in the first call: send setup to <math>\rho</math>; wait for ready 2: <b>if</b> <math>loc = \text{challenge}</math> <b>then</b> <math>(a, loc) \leftarrow (a_{ch}, loc_{ch})</math> 3: send <math>(a, loc)</math> to P <b>upon</b> (guess, <math>(a, loc)</math>) <b>from</b> the <math>\mathcal{A}_{\text{ENV}}</math> 1: <b>if</b> <math>(a, loc) = (a_{ch}, loc_{ch})</math> <b>then</b> output (guess, 1) 2: <b>else</b> (guess, 0) </pre>
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Figure 24:  $k$ -recipient anonymity challenger:  $\text{RACH}_k$

In order to strengthen the adversary, we allow it to choose the time at which a user sends a web page request to a server. Similar to the sender anonymity game the environment adversary has to guess the correct server/request pair.

In the following, we describe the code of the web servers.

**Web pages.** For our purposes it suffices to represent web pages as lists of *elements*, which are associated with *locations* on the web server and are returned upon requests for these locations. Elements are arbitrary bit-strings  $m$  that are marked as elements; we denote them as  $\text{element}(m)$ .

A page request consists of a pair of party ID  $a$  and a location  $loc$  (represented by a bit-string), which we denote as  $\text{request}(loc, a)$ . The pair  $(loc, a)$  can be understood as the url that is requested from the user, where  $a$  denotes the domain and  $loc$  denotes the path to a specific web page on the domain  $a$ . Note that the countermeasure WFC only provides recipient anonymity guarantees for web pages that do not dynamically load content upon user inputs, e.g., by using JavaScript techniques such as Ajax.

**Server protocol  $\Pi_{\text{server}}$ .** Web servers are modeled as a protocol  $\Pi_{\text{server}}$ , which can be thought of an abstraction of server software, e.g., Apache HTTP Server.<sup>13</sup>

The server protocol can register several locations on the machine it runs on. Formally, upon a message of the form  $(\text{register}, loc, pg)$  from its parent,  $\Pi_{\text{server}}$  registers a web page  $pg$  at the location  $loc$ . Upon a network message  $(loc, (a, \text{sid}'))$ , the server then responds with the web page  $pg$ .

The  $k$ -recipient anonymity for WFC follows from the composition theorem (Theorem 2), the realization theorem (Theorem 4), the definition of  $\mathcal{F}_{\text{OR}}$  and from the fact that all web-pages in the same bucket are padded to the same size and that WFC removes timing features from the traffic by introducing artificial delays.

**Lemma 4.** *Let  $\text{EXEC}'$  be defined as  $\text{EXEC}$  except that  $\text{EXEC}'$  outputs the bit  $b$  from the first output of the form  $(\text{guess}, b)$  by  $\text{RACH}$ . Let  $\langle \text{RACH}, \mathcal{A}_{\text{ENV}} \rangle$  denote the machine that contains  $\text{RACH}$  and  $\mathcal{A}_{\text{ENV}}$ , as described in Section 6.1. Let  $\langle \text{WFC}_{\Pi_{\text{WOR}}\Pi_{\text{OR}}}, \Pi_{\text{server}} \rangle$  denote the combined protocol with the countermeasure along with the wrapper and the onion routing protocol and the server protocol.*

*For any PPT environment adversary  $\mathcal{A}_{\text{ENV}}$  and a dummy network adversary  $\mathcal{A}_d$  that only compromises entry links or entry nodes, for sufficiently large  $t_{\text{buf}}$  and  $\eta$  and a negligible function  $\mu$  we have*

$$\Pr[\text{EXEC}'_{\eta}(\langle \text{WFC}_{\Pi_{\text{WOR}}\Pi_{\text{OR}}}, \Pi_{\text{server}} \rangle, \mathcal{A}_d, \langle \text{RACH}, \mathcal{A}_{\text{ENV}} \rangle) = 1] \leq 1/k + \mu(\eta)$$

*Proof.* By the realization theorem (Theorem 4) and the composability theorem (Theorem 2),  $\text{WFC}_{\Pi_{\text{WOR}}\Pi_{\text{OR}}}$  securely realizes  $\text{WFC}_{\Pi_{\text{WOR}}\mathcal{F}_{\text{OR}}}$ ; hence a network adversary against  $\text{WFC}_{\Pi_{\text{WOR}}\mathcal{F}_{\text{OR}}}$  (and hence also against  $\text{WFC}_{\Pi_{\text{WOR}}\Pi_{\text{OR}}}$ ) that only compromises the entry node of one client or the link to the entry node of one client only learns the *cids* and freshly drawn handles, which are both independent of the recipient. Hence, the pattern of the response is the same for all names that the environment input.

Since we assumed that the web servers wait until a fixed time  $t$  before they response, the response time of the web servers is always the same. We conclude that the adversary cannot learn more than the length of the requested web page. Since  $\text{RACH}$  has output  $(\text{guess}, 1)$ , the length of all possibly

<sup>13</sup>Technically,  $\Pi_{\text{server}}$  is the “server” role in the WFC protocol; otherwise they cannot be peers.

requested web pages is padded to  $\text{pageBuck}(k)$ . Hence, the length and the time pattern of all web pages in  $\text{pageBuck}(k)$  is the same.  $\square$

## 8 Conclusion and Future Work

In this work, we presented TUC, a formal framework for the analysis of complex multi-party protocols that includes a comprehensive notion of time, which is suitable for and tailored to the demands of analyzing AC protocols. Our framework provides all properties that allow for strong compositionality: a universal composability result, and the completeness of the dummy adversary. We apply this framework to the widely deployed Tor network and showed that a previous abstraction of the onion routing protocol [3] can be suitably extended to account for timing and that it is realized in TUC by a similarly extended onion routing protocol. We then leveraged this abstraction and our framework to formalize, as a case study, a simple countermeasure against website fingerprinting attacks and proved this countermeasure secure.

An interesting direction for future work is the evaluation of more elaborate countermeasures against known time-sensitive attacks, in particular traffic correlation attacks. Since our framework comprehensively models timing attacks, every verification of the abstraction for onion routing yields security guarantees for the actual OR protocol.

For future work there are scenarios in which it is crucial to characterize the network topology in a more detailed way. Possible extensions include adding the latency and the throughput of a link, allowing not only single links between two parties but several links (a multigraph topology), and including weights to each link to model routing preferences. Such extensions could for example be used for the analysis of denial-of-service resistance mechanisms or for the analysis of more sophisticated path selection algorithms for onion routing or analyzing denial of service attacks.

There is a line of work on automated verification techniques for timed automata. It would be interesting to extend existing models based on timed automata to better capture real world networks and adversaries, and explore in which cases timed automata are a sound abstraction for TUC protocols. Such a result would allow obtaining strong guarantees, i.e., against computational adversaries that can perform time-measurements, from established automated verification tools [16, 34, 8, 41, 38, 39].

Moreover, there is an information theoretic analysis of web traffic which uses an abstraction of web-traffic [2]. It would be interesting to utilize TUC to prove that their abstraction is sound, i.e., that all attacks in TUC are reflected in their abstraction.

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